ZKUNSAT **Proving UNSAT in Zero Knowledge**

SAT Reunion Workshop, Simons Institute, UC Berkeley

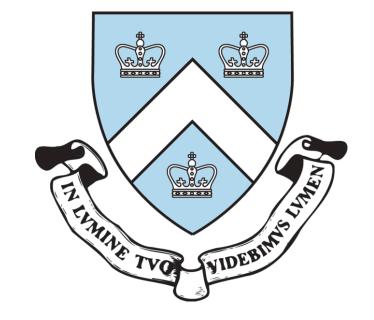
Ning Luo, Timos Antonopoulos, Bill Harris, Ruzica Piskac, Eran Tromer, Xiao Wang

galois



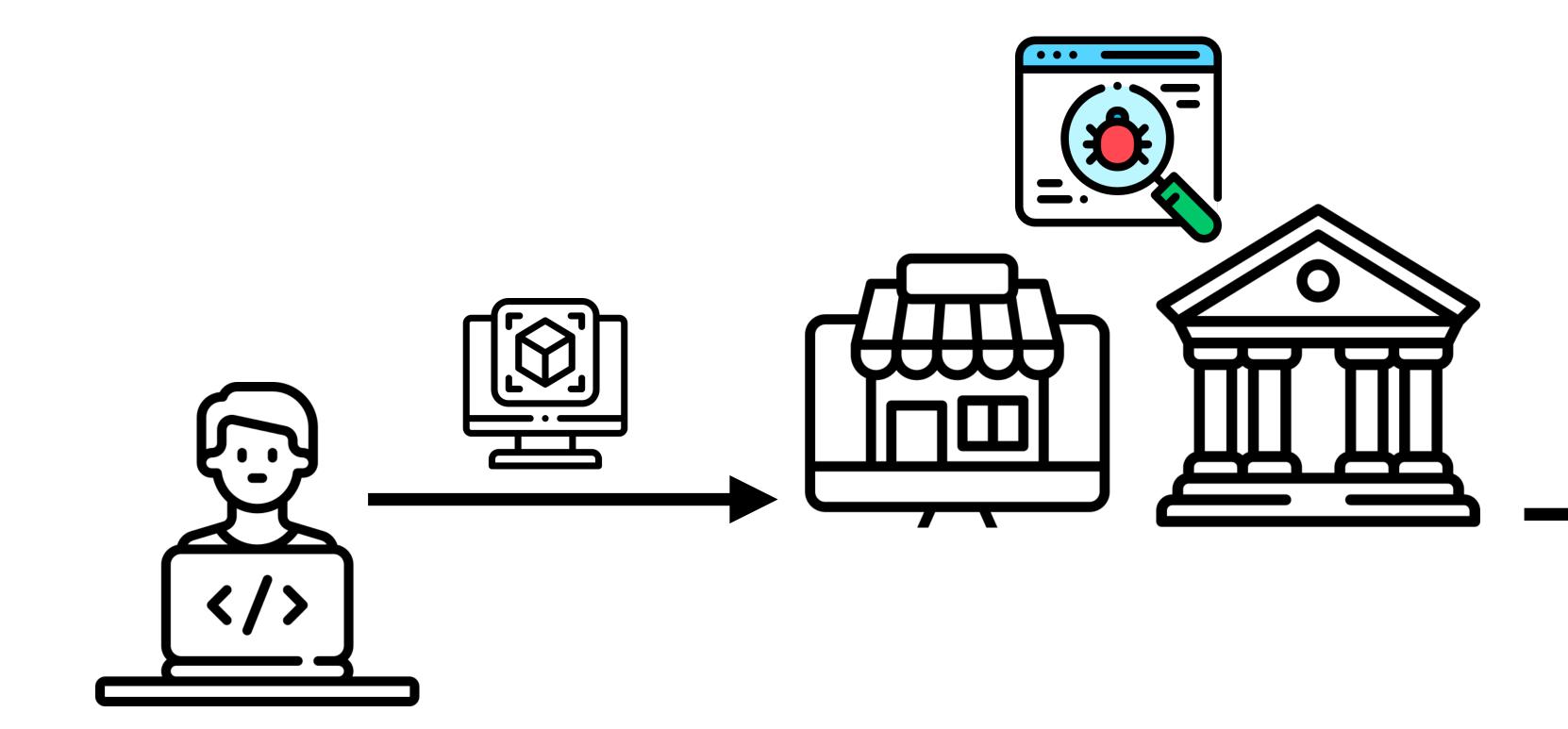


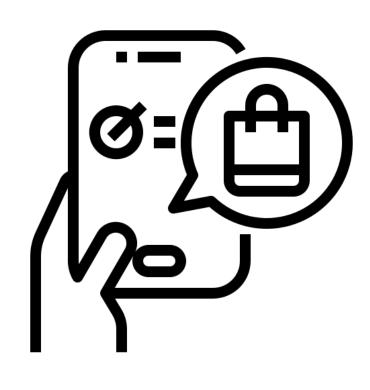
















App Store Review Guidelines

Apps are changing the world, enriching people's lives, and enabling developers like you to

innovate like never ecosystem for milli time developer or creating apps for th be con

Complete guide to GDPR compliance

GDPR.eu is a resource for organiz achieve GDPR compliance.

Here you'll find a library of straigh Proposed Second Amendment to 23 NYCRR Part 500

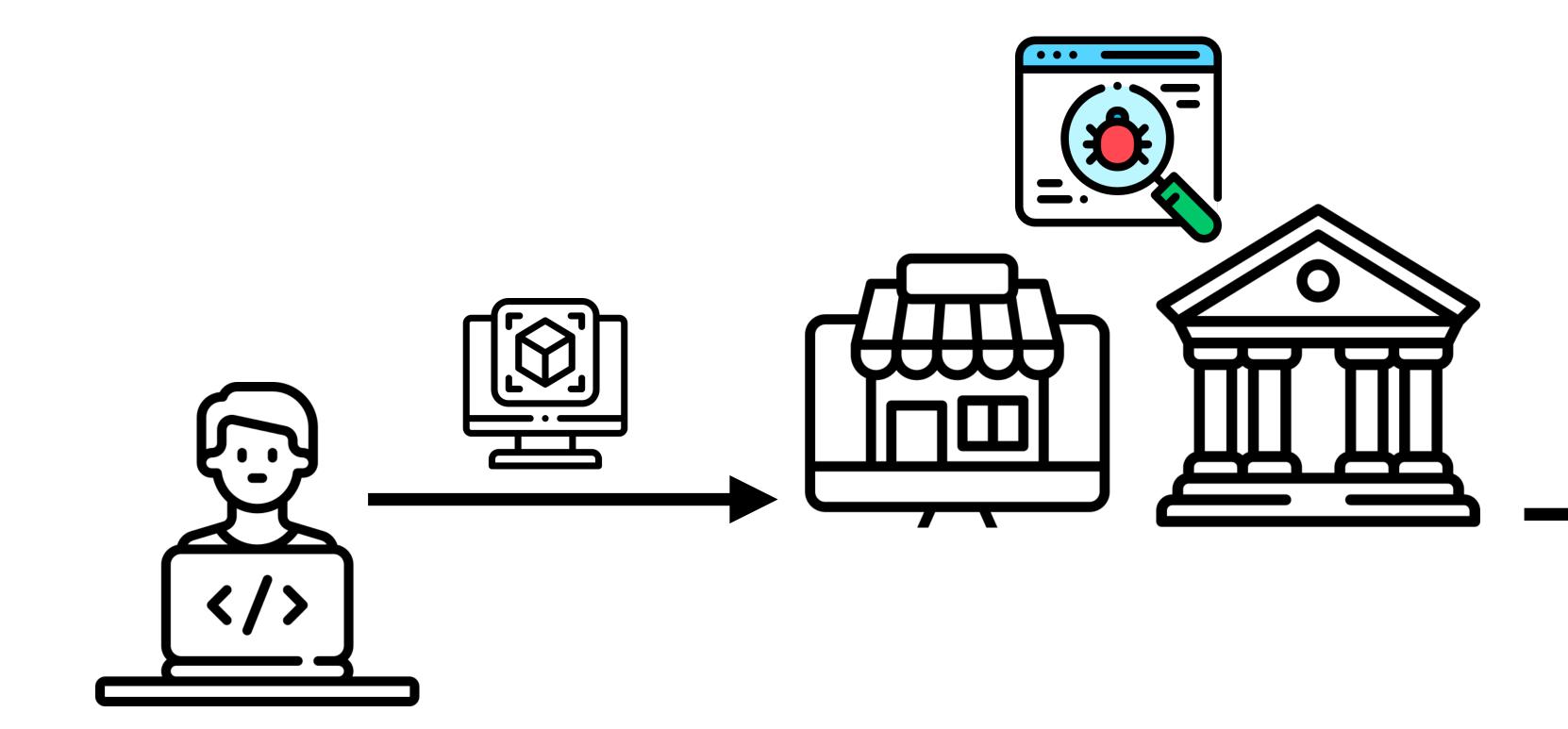
On November 9, 2022, the proposed second amendment to 23 NYCRR Part 500 (DFS Cybersecurity Regulation) was published in the New York State Register. This begins the 60-day comment period. Information about this amendment is available on DFS's Regulations page.

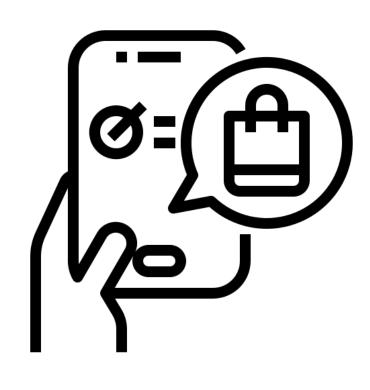
Comments must be submitted in writing to DFS by 5 pm EST on Monday, January 9, 2023. Submissions should be sent by email to cyberamendment@dfs.ny.gov or by mail to the New York State Department of Financial Services c/o Cybersecurity Division, Attn: Joanne Berman, One State Street, Floor 19, New York, NY, 10004. No special form is required.

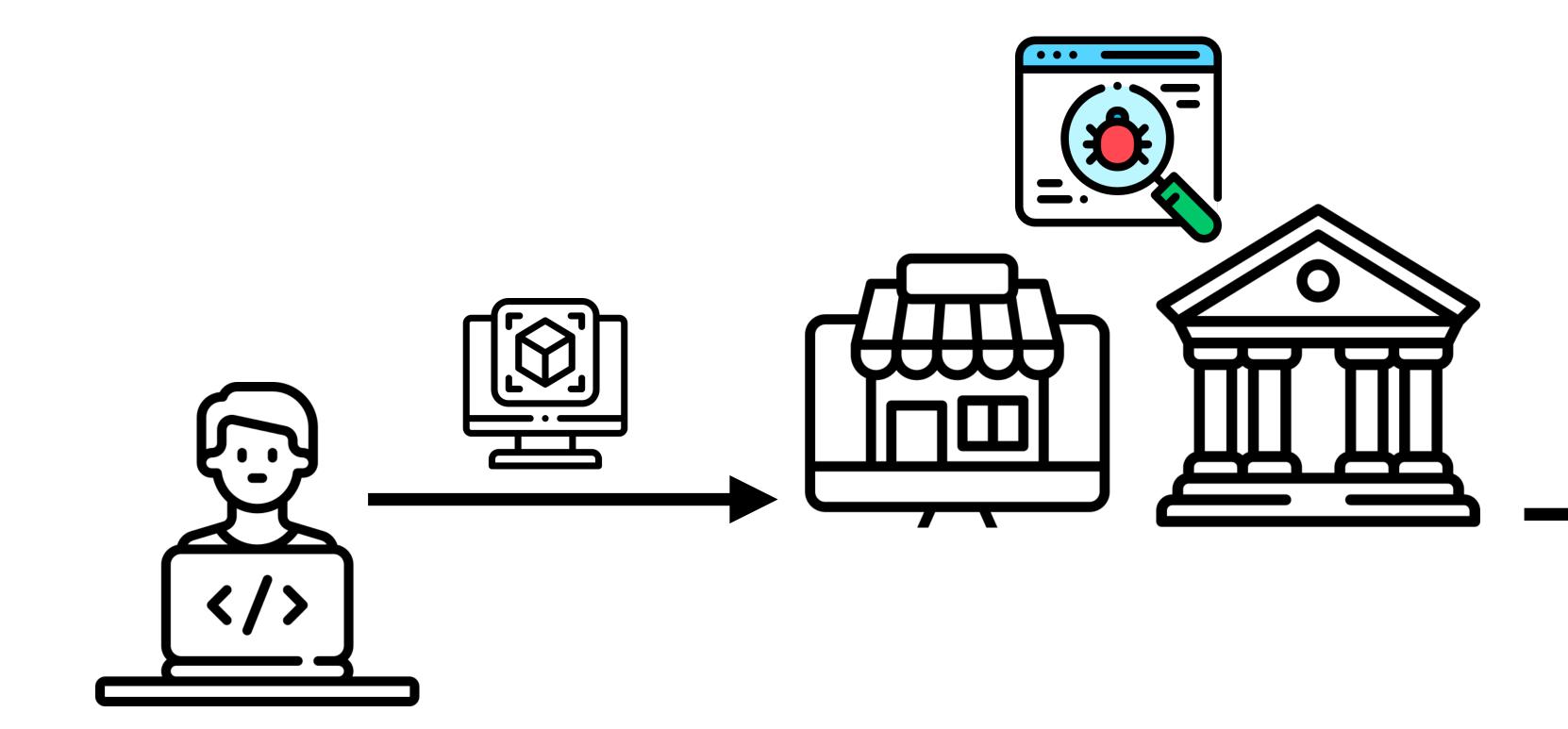
The notice and comment process was integral to shaping the requirements of the original DFS Cybersecurity Regulation and helped ensure the success and durability of the regulation as promulgated. We appreciate the time you spend writing and submitting comments and look forward to considering them.

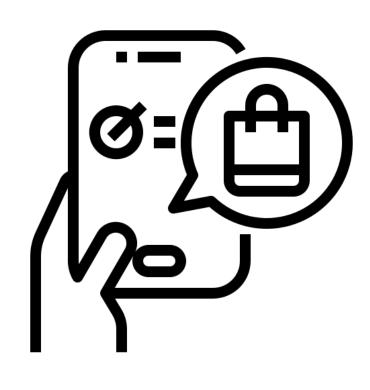
Verifications can be required by outside entities

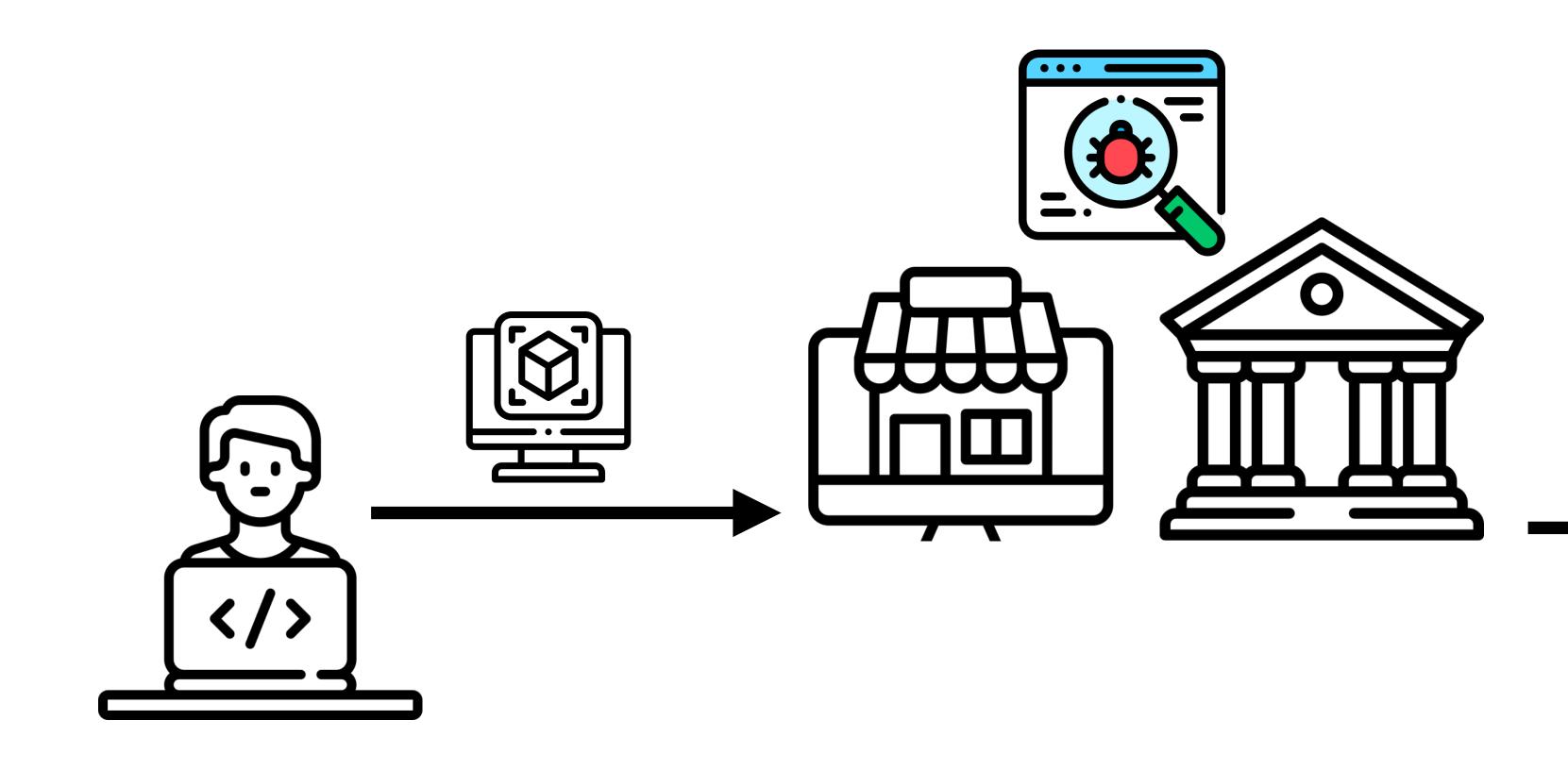








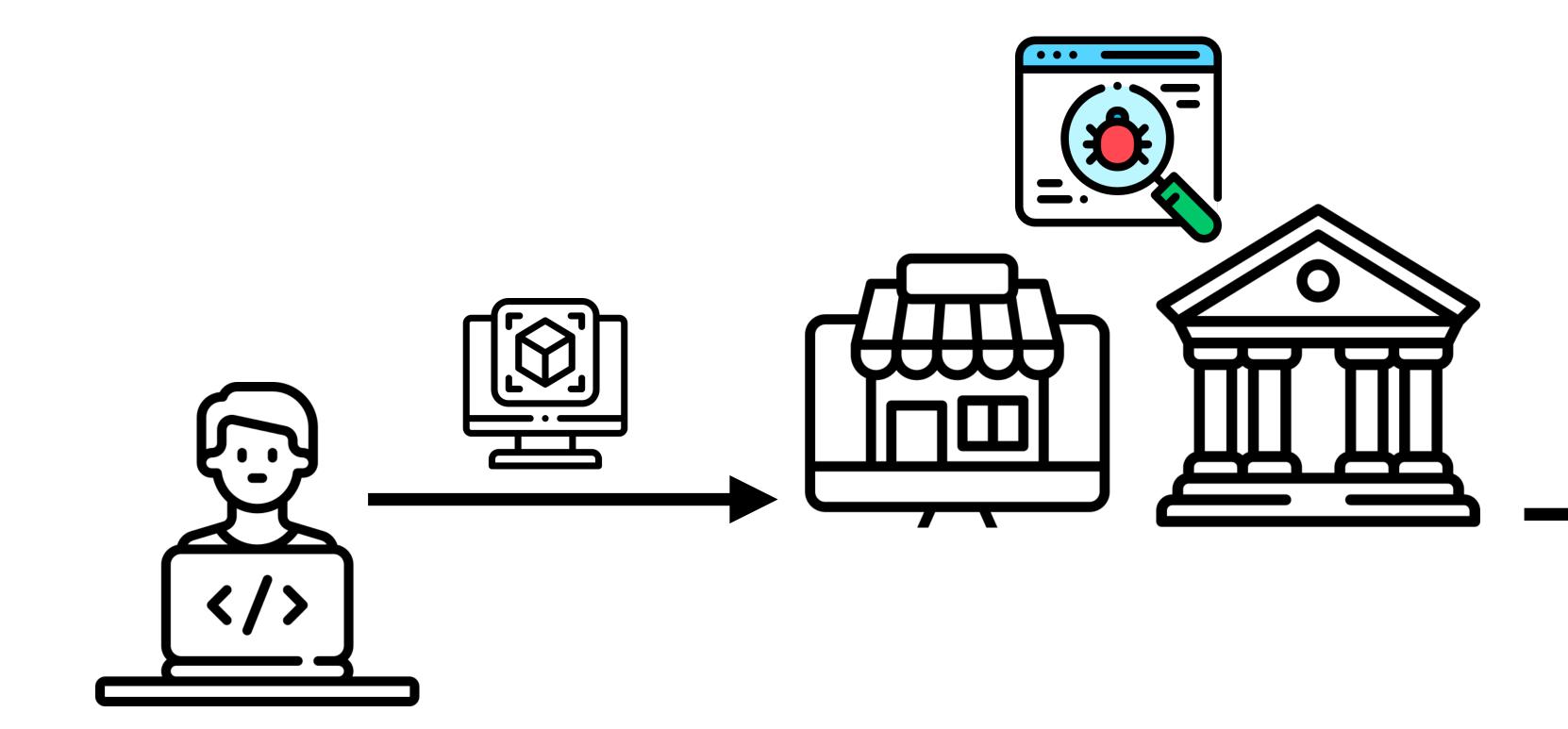


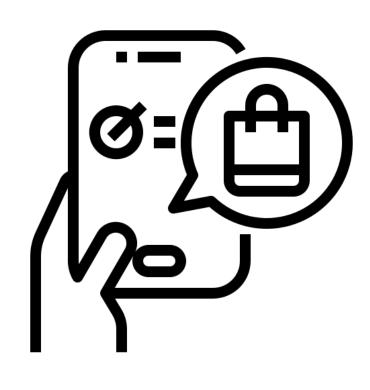


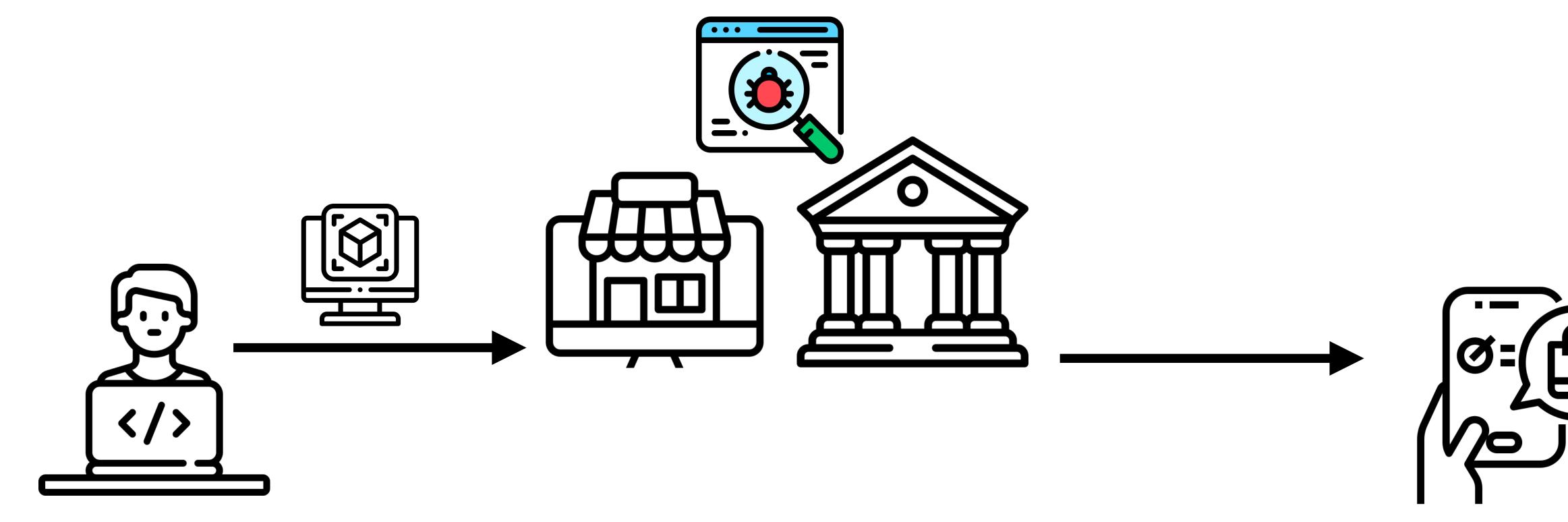
Centralized softwares verification and distribution:

Solution to a lot of problems, eg.supply-chain attacks





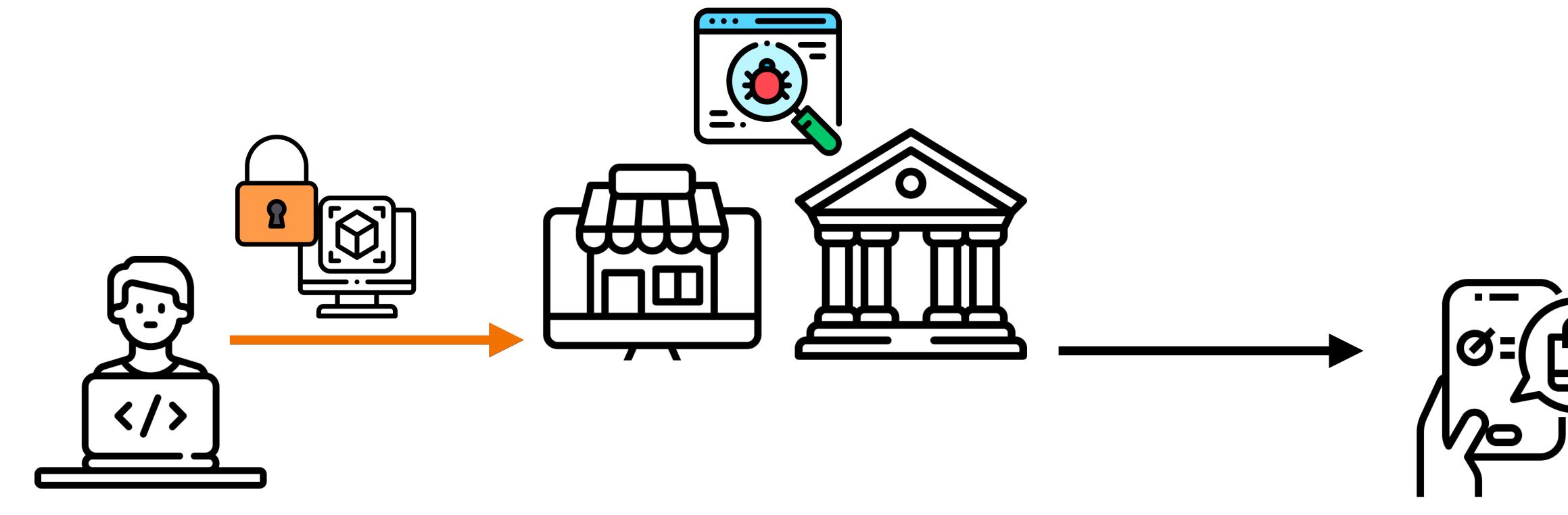




Both the developers and users should trust the centralized marketplace



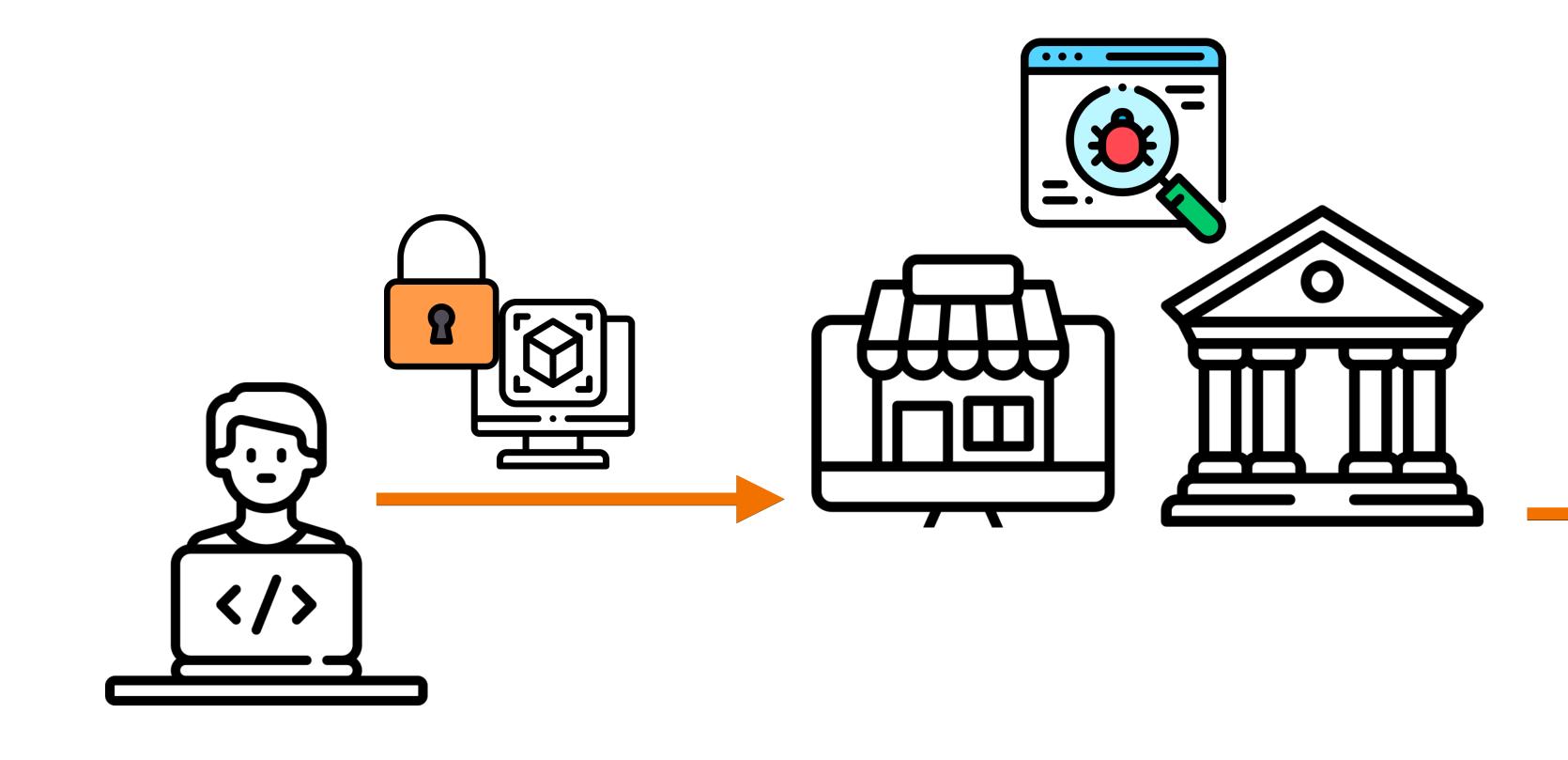




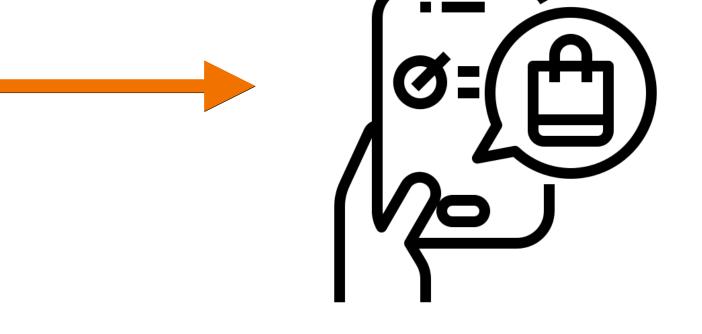
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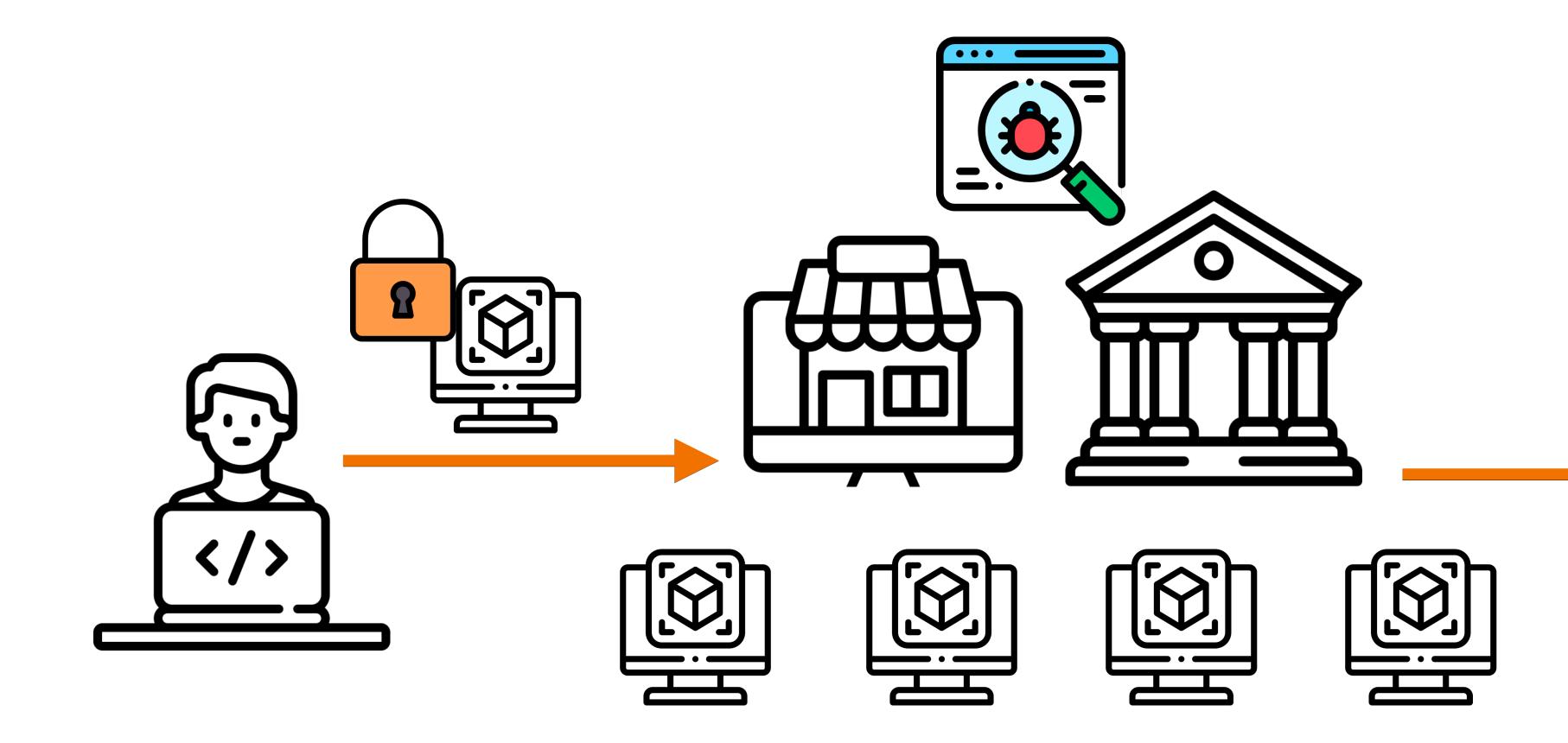




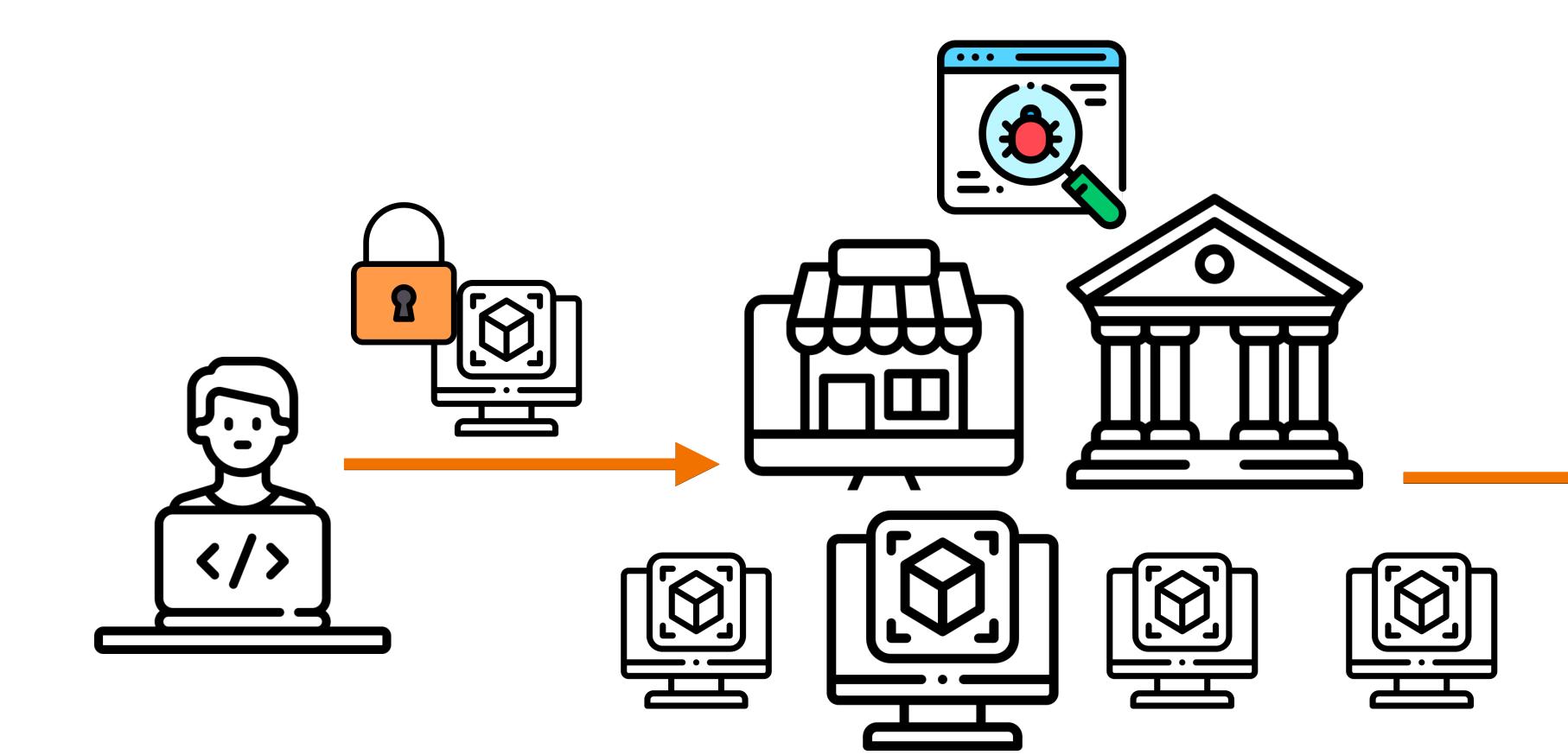
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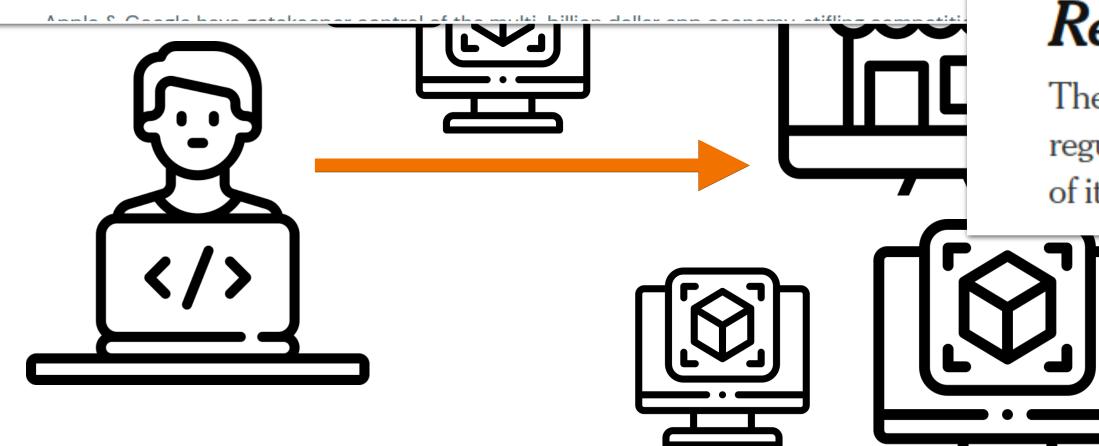






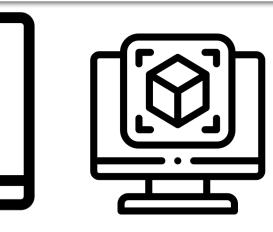
08.11.2021

Blumenthal, Blackburn & Klobuchar Introduce Bipartisan Antitrust Legislation to Promote App Store Competition

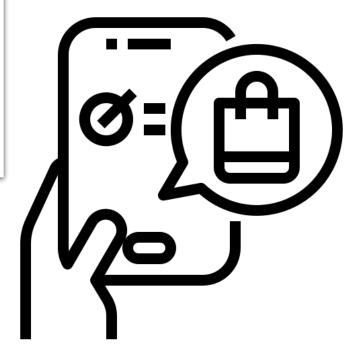


Apple Gives Ground in a Strategic Retreat From Strict App Store Rules

The company, under pressure from app developers and regulators, is making concessions while protecting lucrative parts of its App Store.

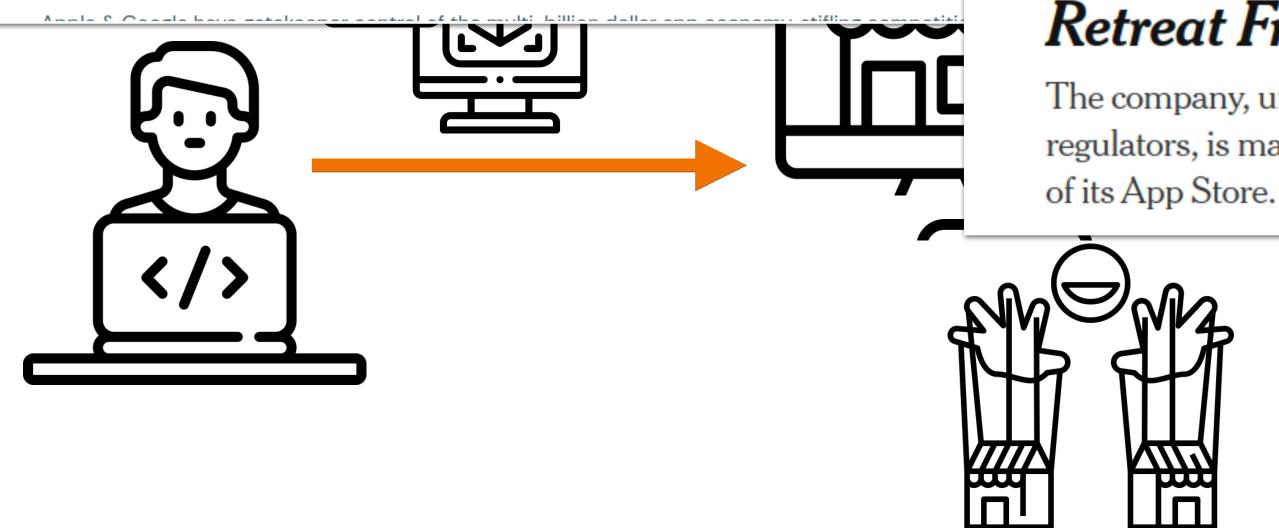








Blumenthal, Blackburn & Klobuchar Introduce Bipartisan Antitrust Legislation to Promote App Store Competition

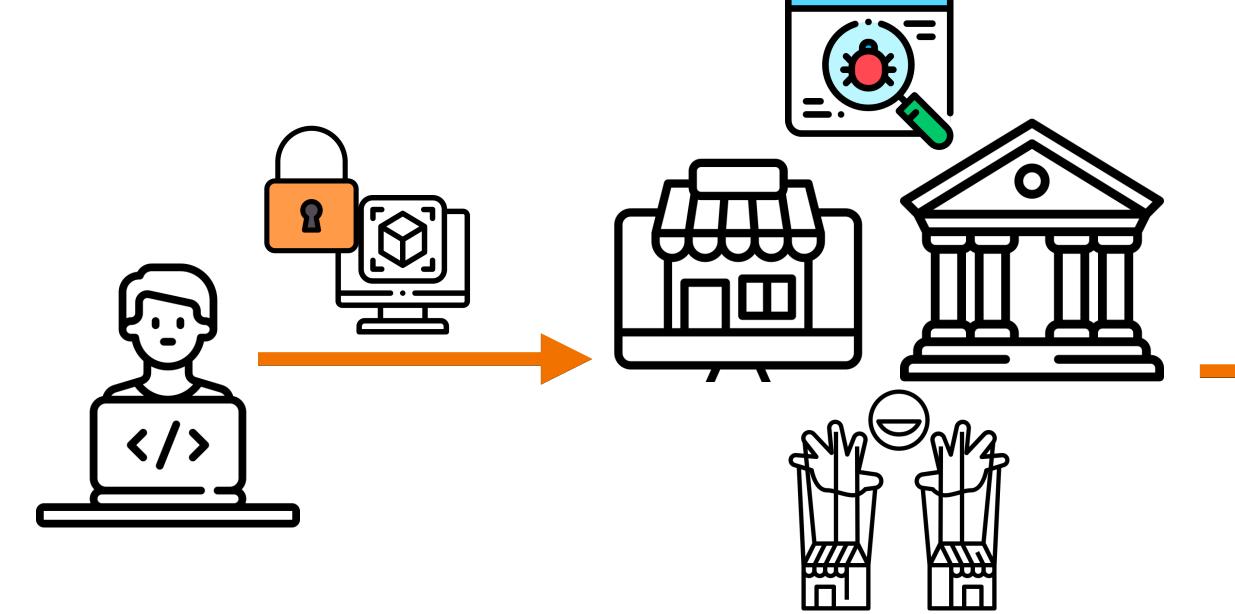


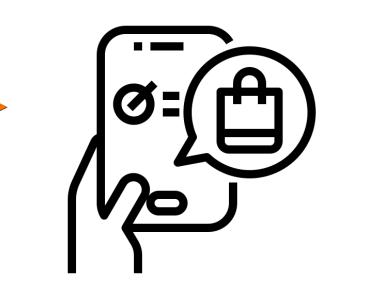
Centralized curation naturally produces a monopoly

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THE SEPARATION OF PLATFORMS AND COMMERCE

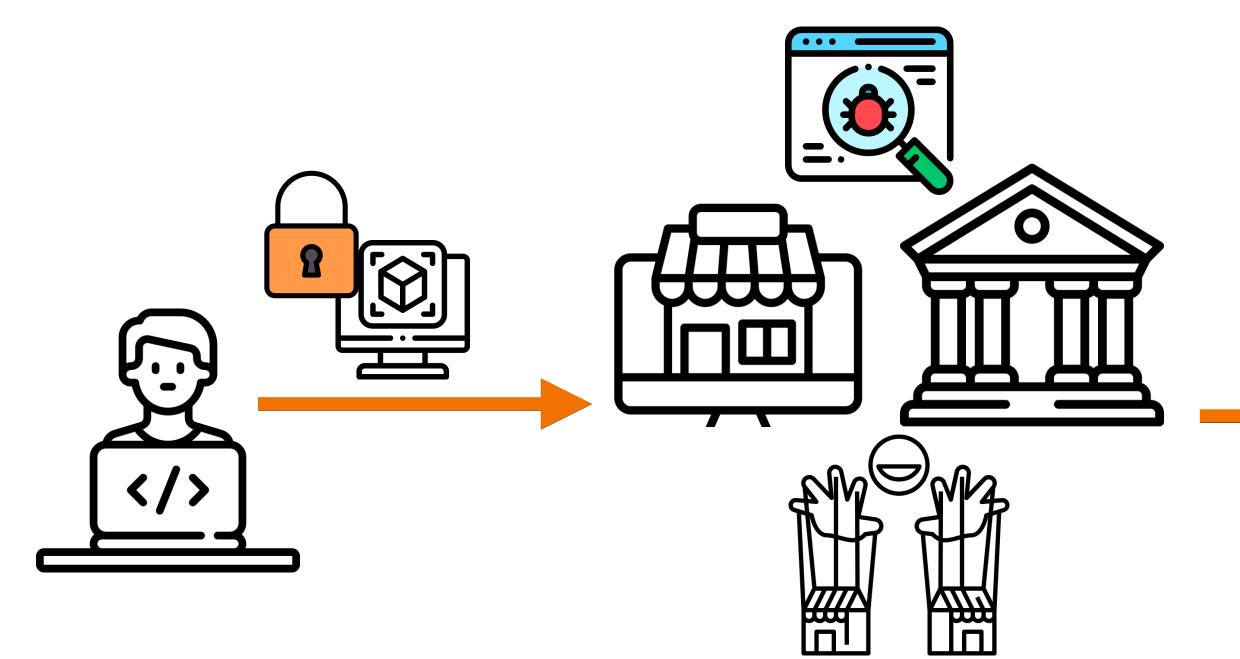
Lina M. Khan*

A handful of digital platforms mediate a growing share of online commerce and communications. By structuring access to markets, these firms function as gatekeepers for billions of dollars in economic activity. One feature dominant digital platforms share is that they have integrated across business lines such that they both operate a platform and market their own goods and services on it. This structure places dominant platforms in direct competition with some of the businesses that depend on them, creating a conflict of interest that platforms can exploit to further entrench their dominance, thwart competition, and stifle innovation.

Read the Antitrust Lawsuit Against Google

Dozens of States Sue Google Over App Store Fees

Software developers have accused the company of harsh policies and taking a large cut of financial transactions in their apps.





THE SEPARATION OF PLATFORMS AND COMMERCE

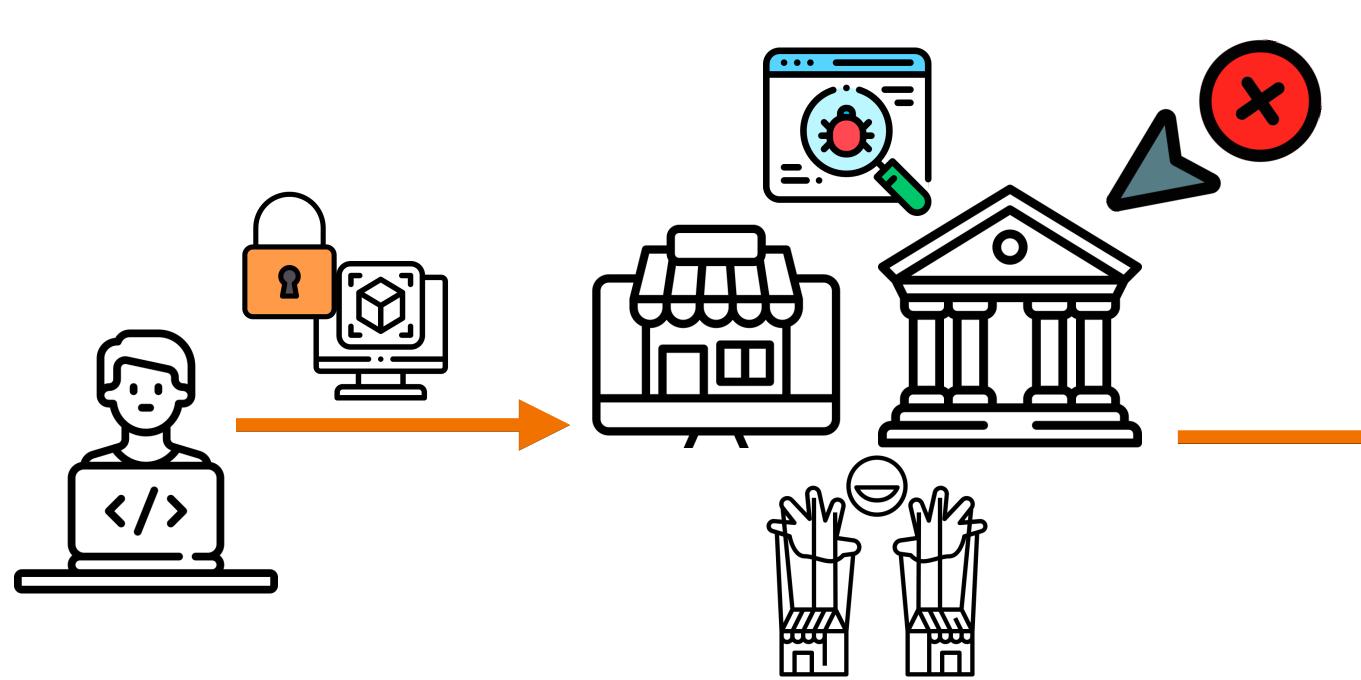
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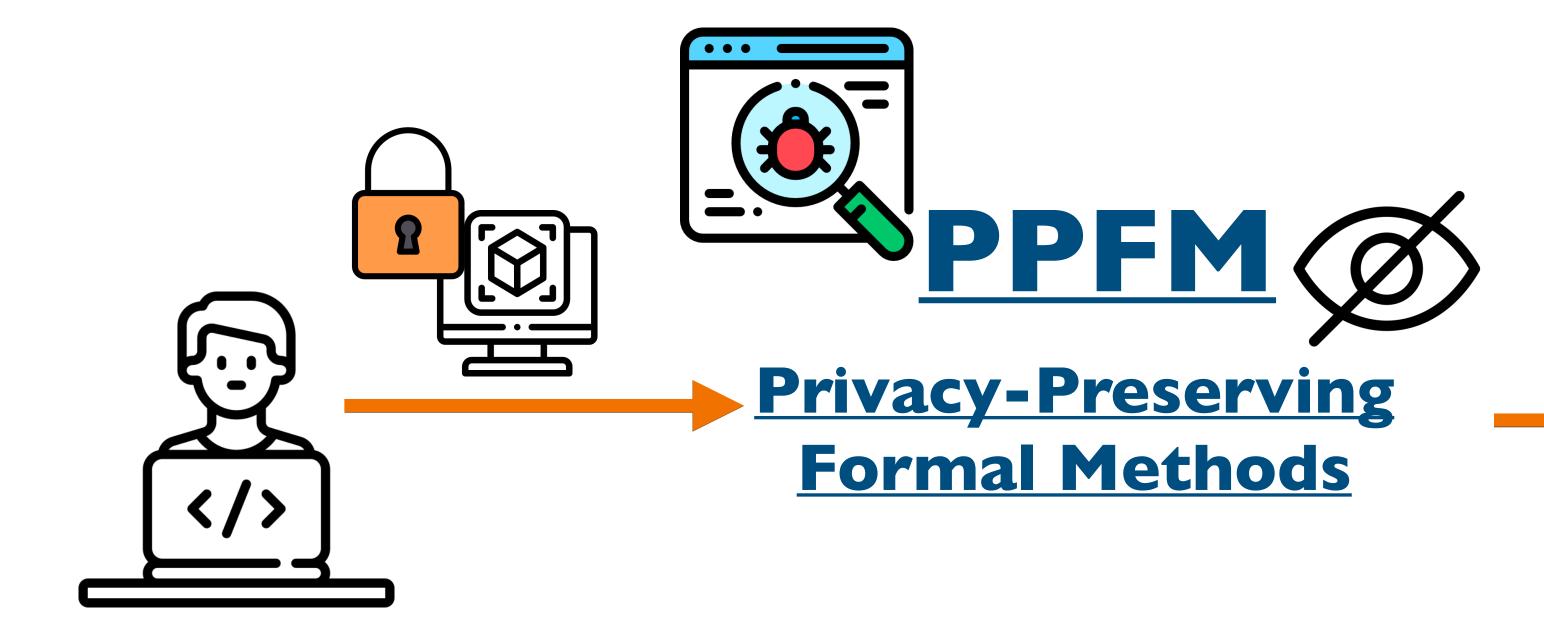
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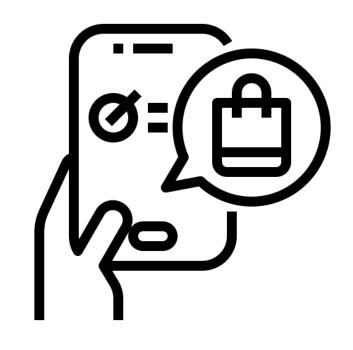
Remove centralized verification and distribution

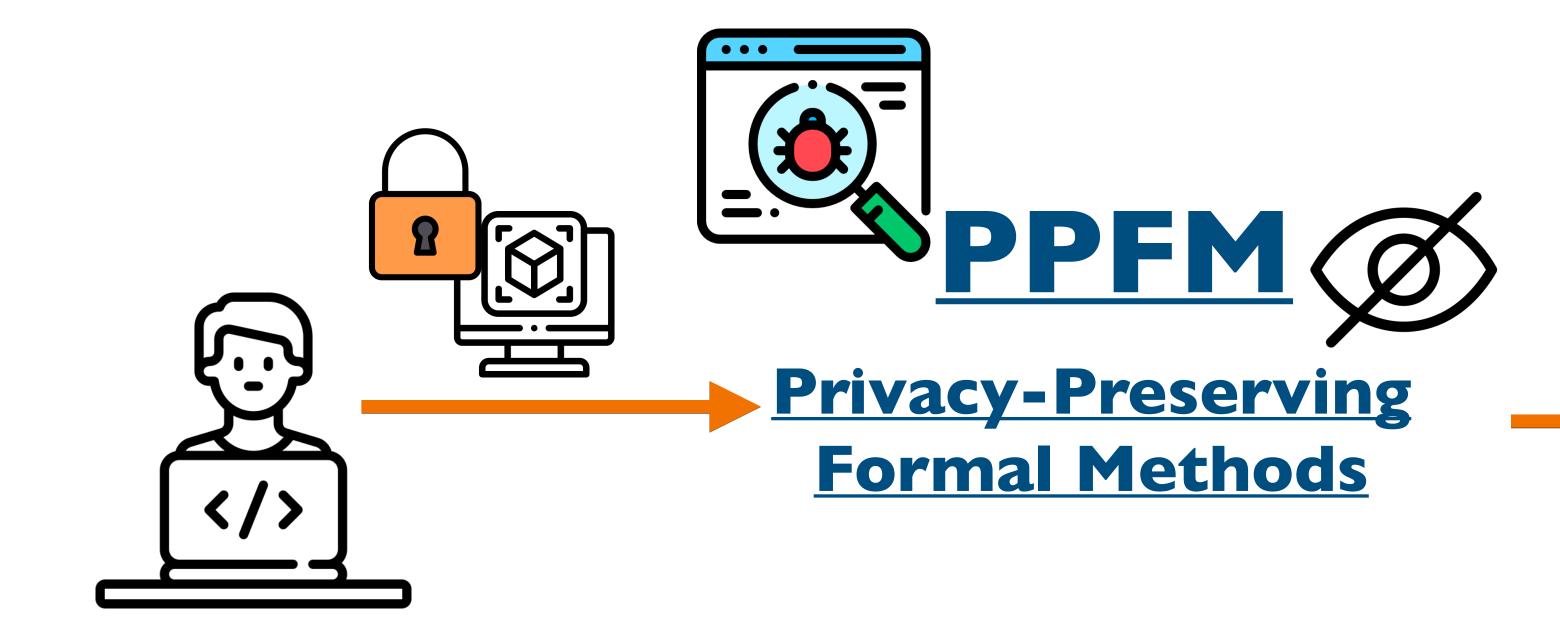


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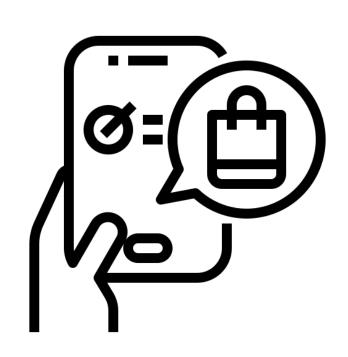




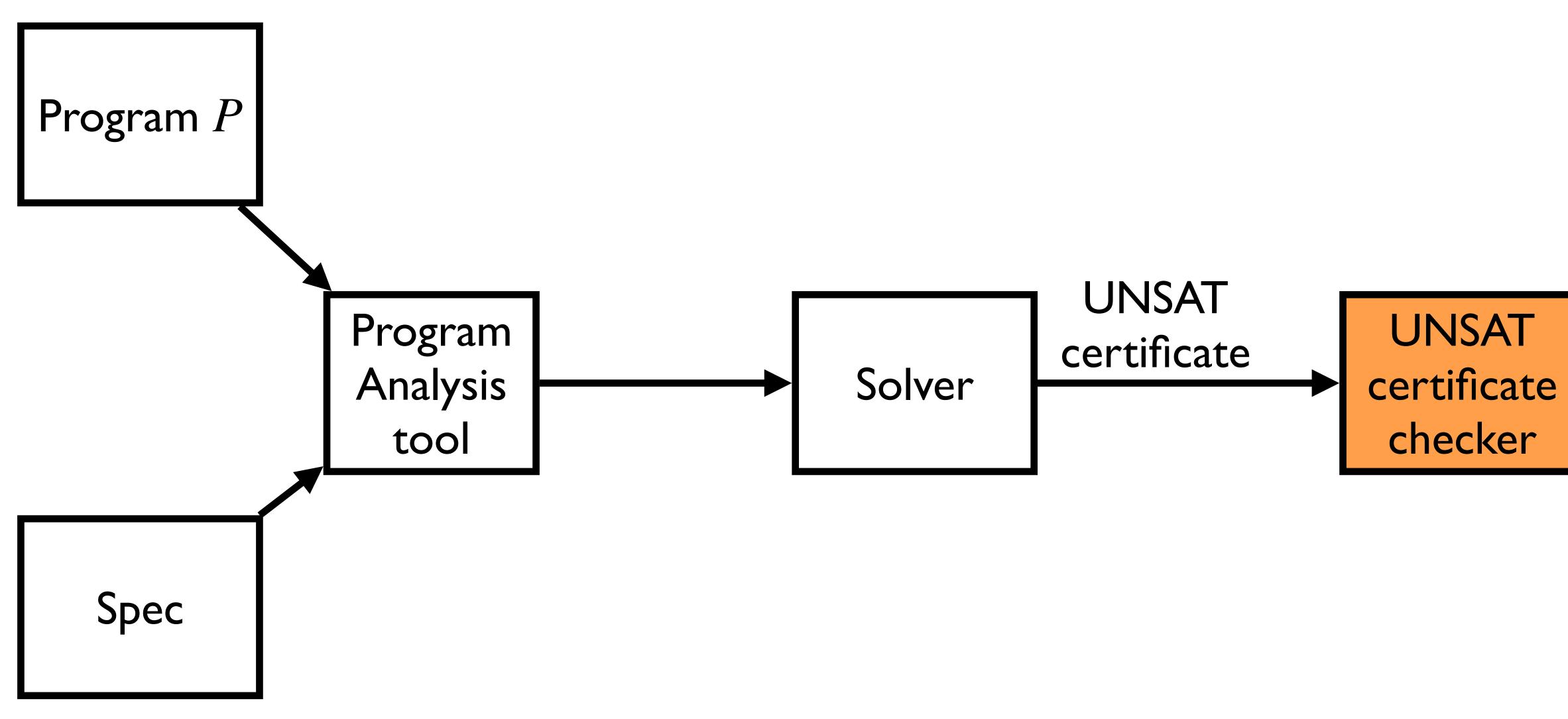




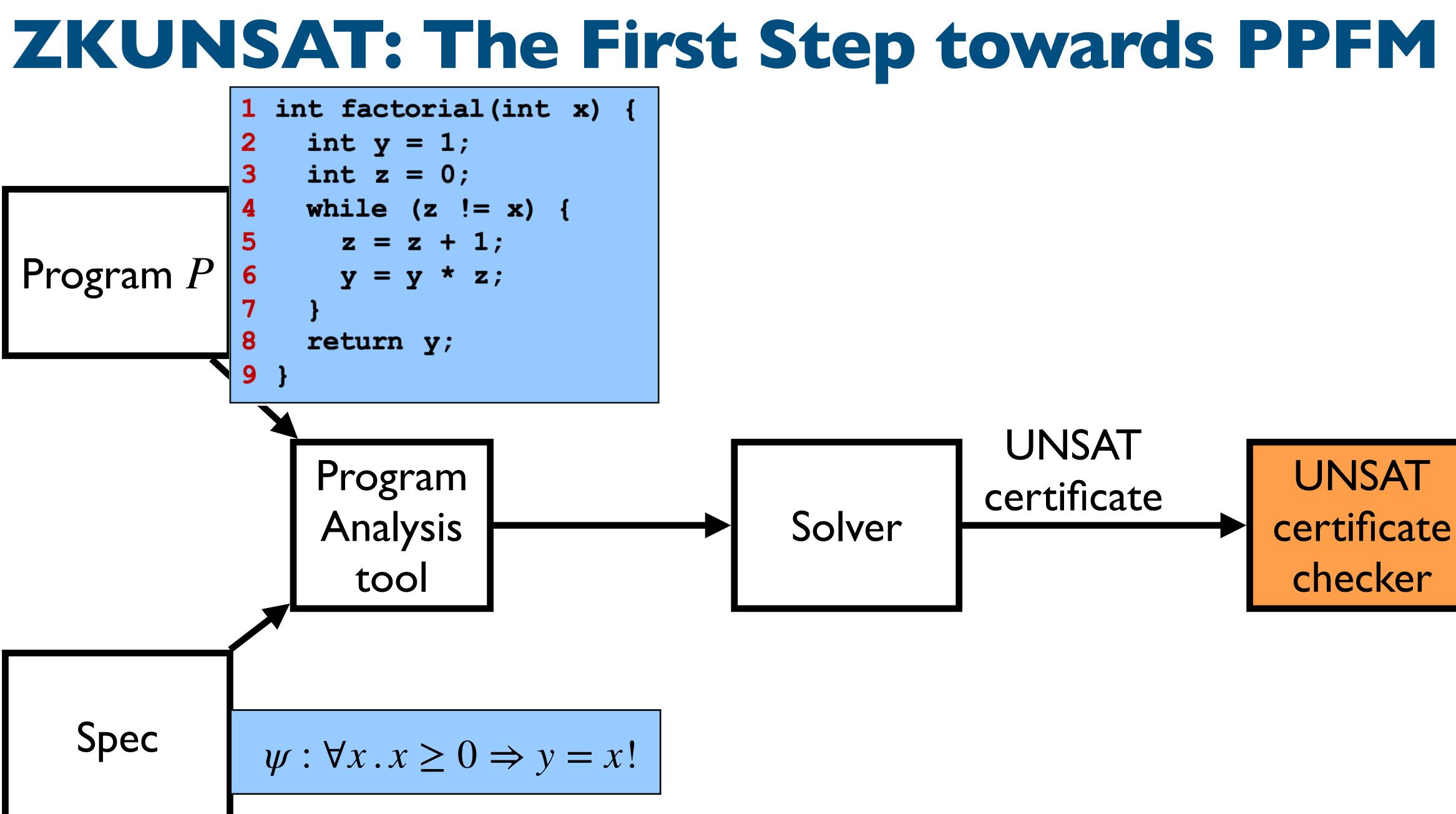
ZKUNSAT: The First Step towards PPFM



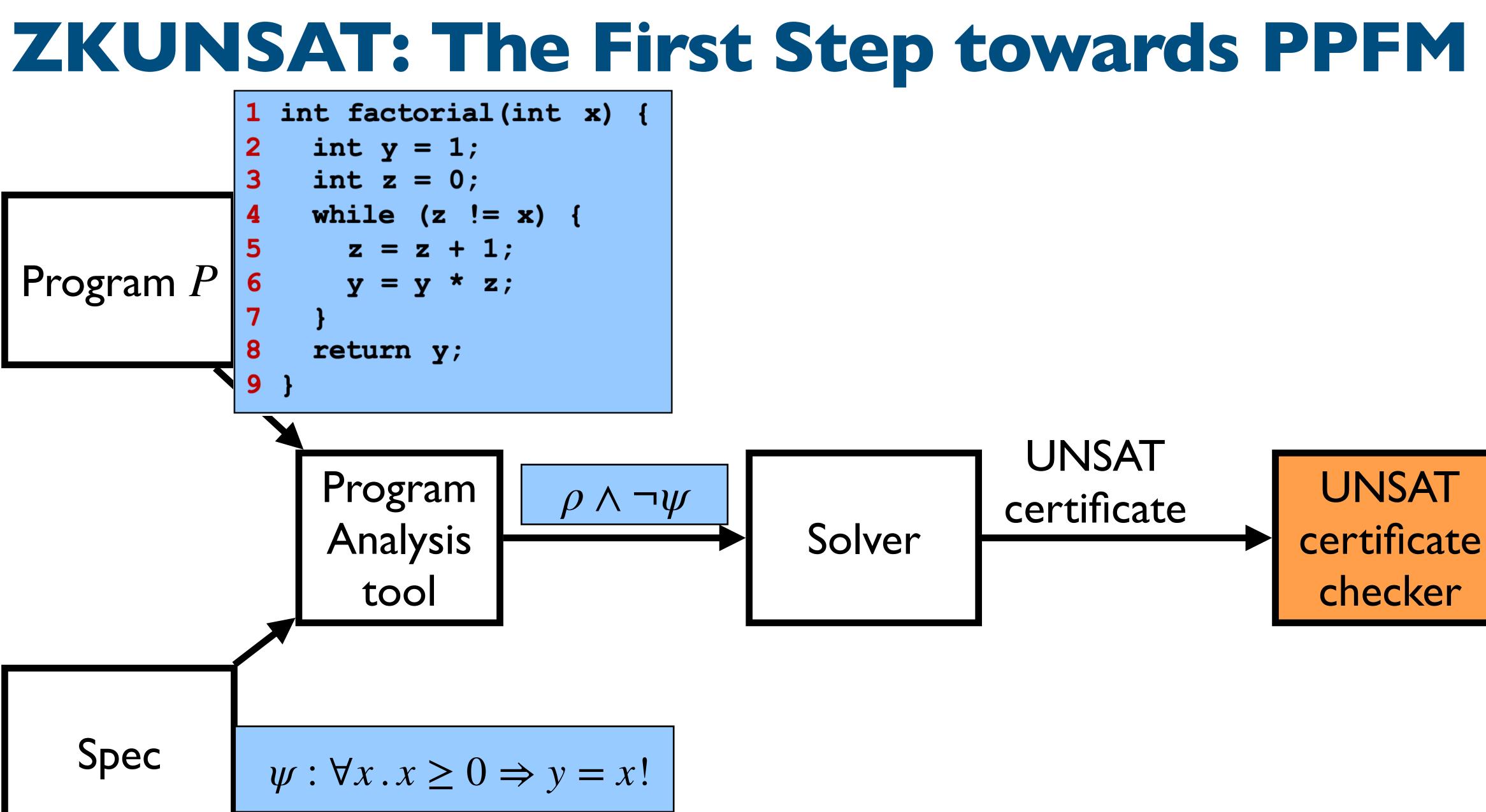
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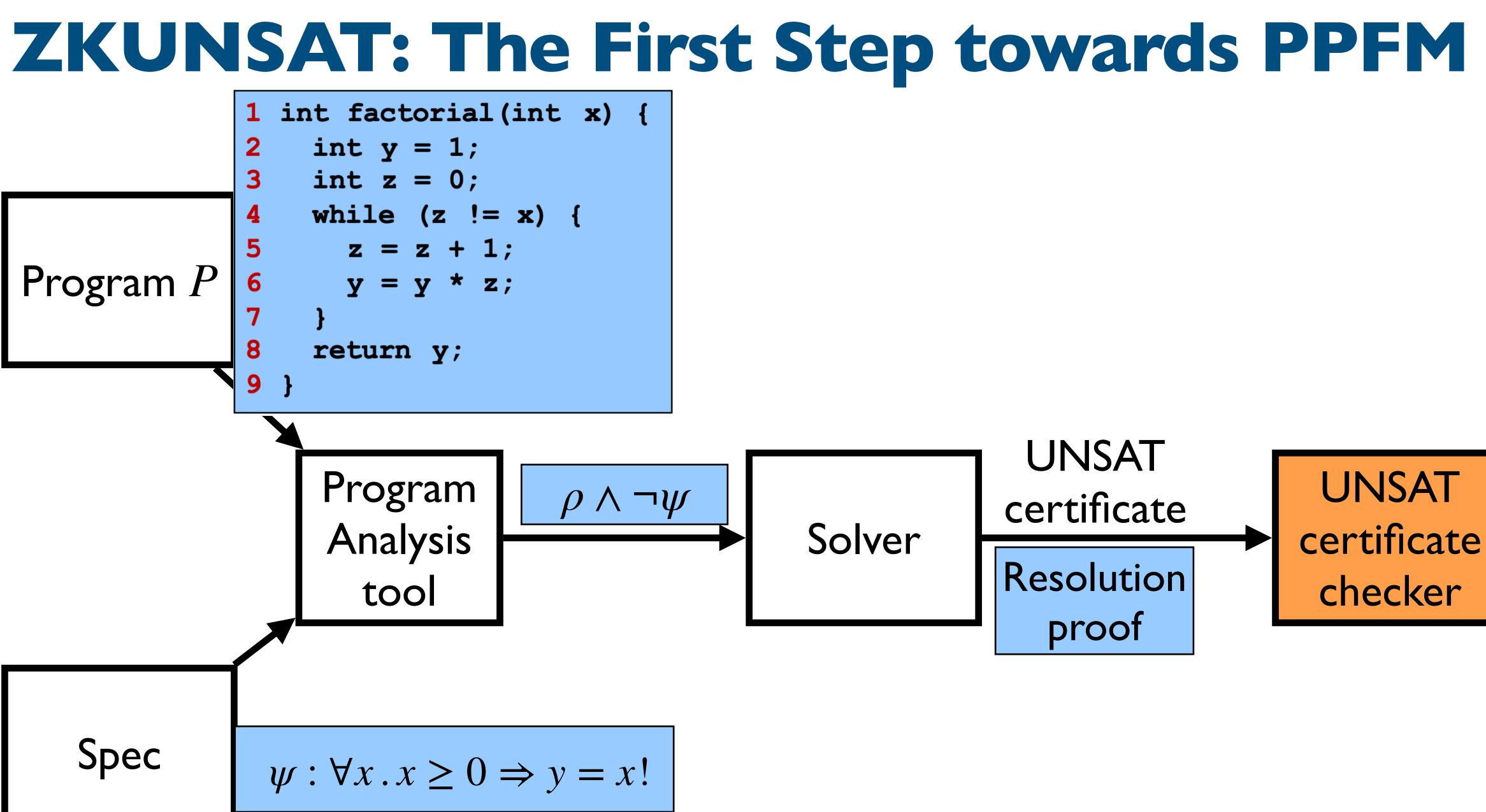




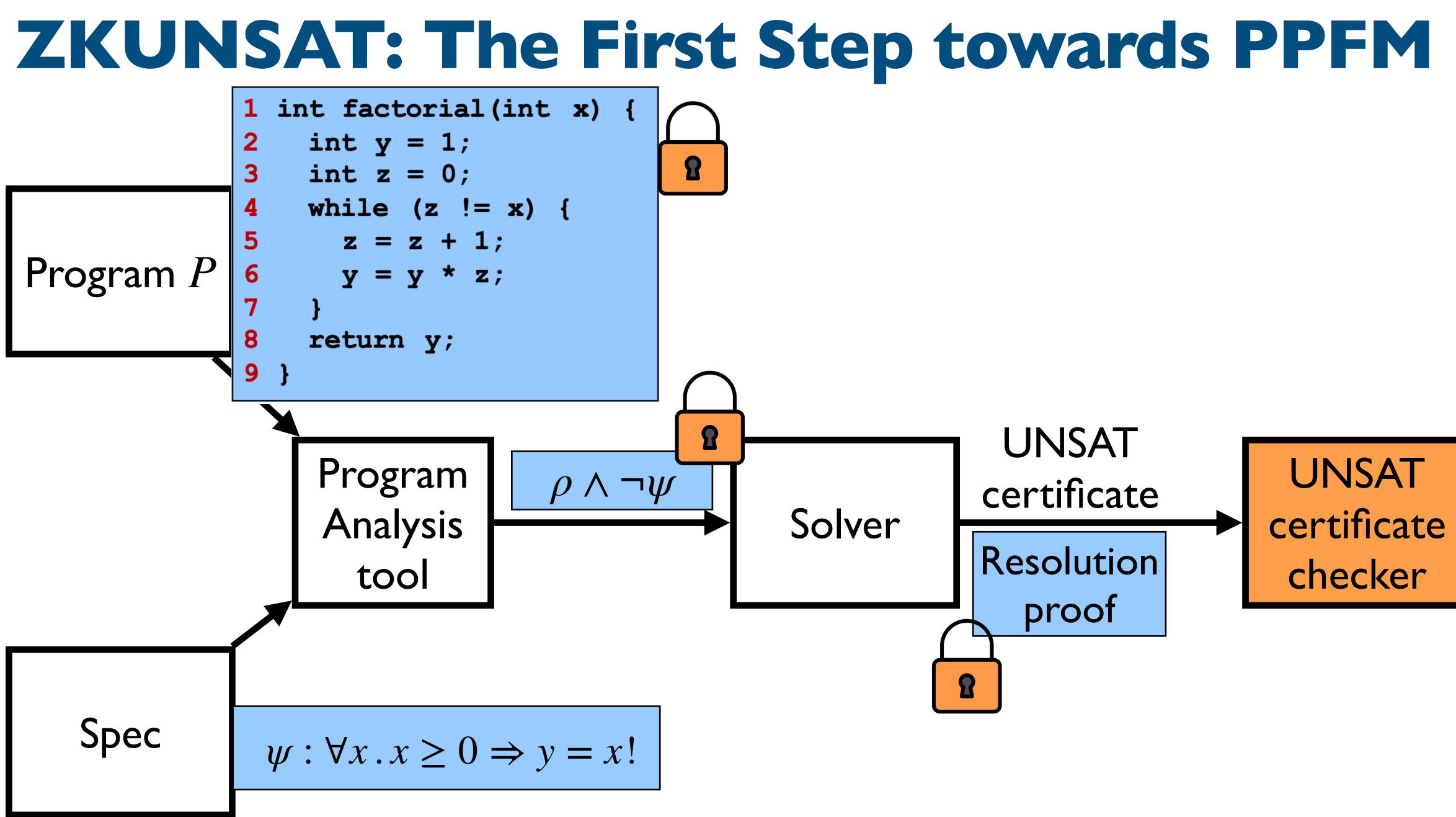




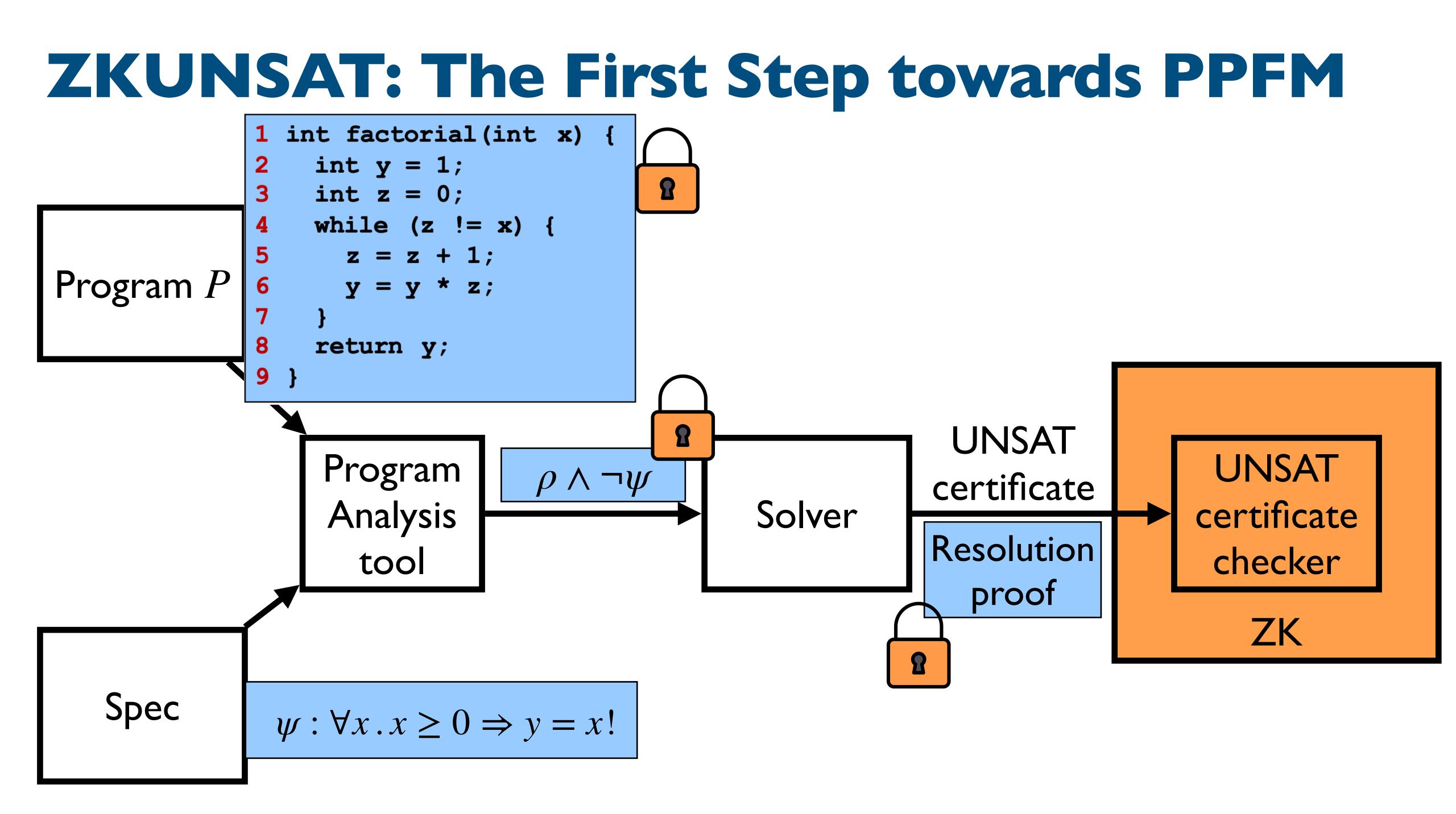


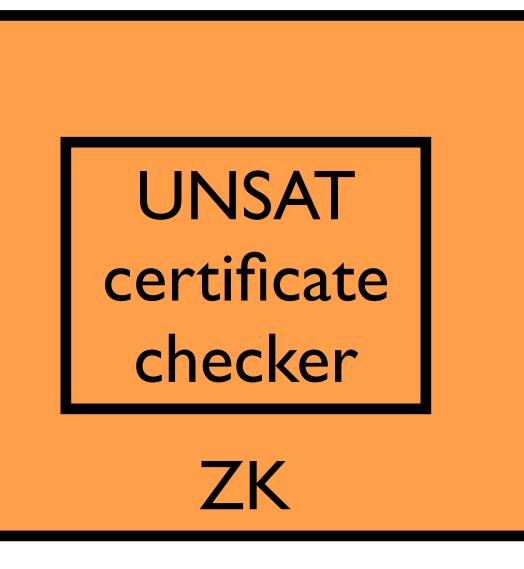




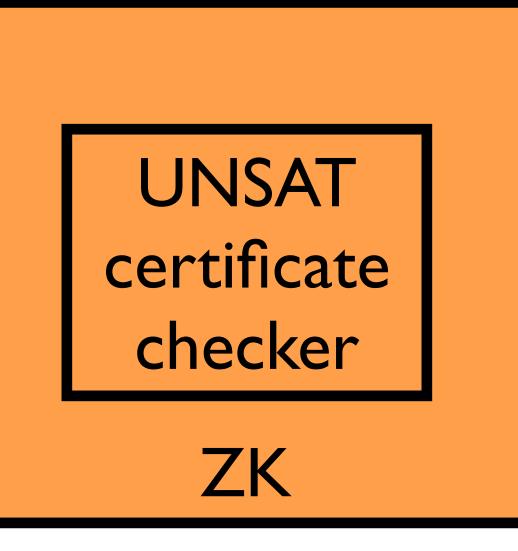








generic implementation of the certificate checker in ZK











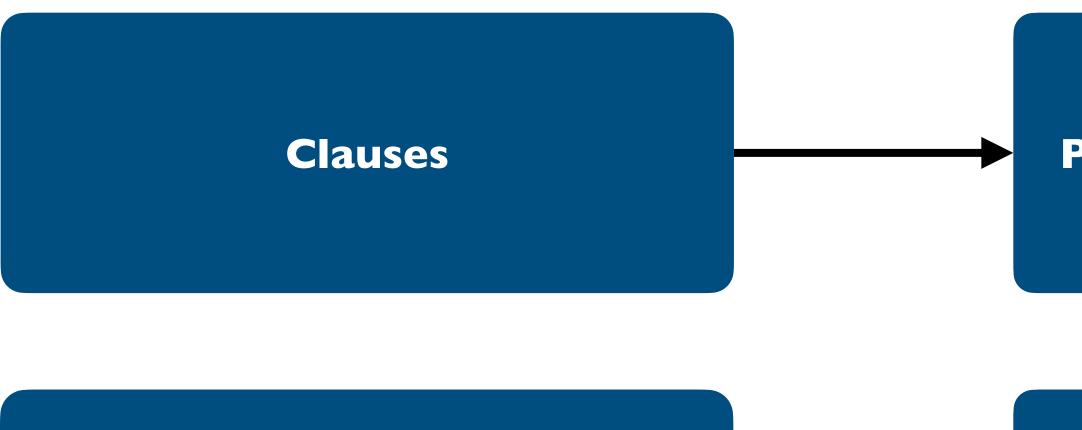




Resolution proof checking

Polynomials over a finite field

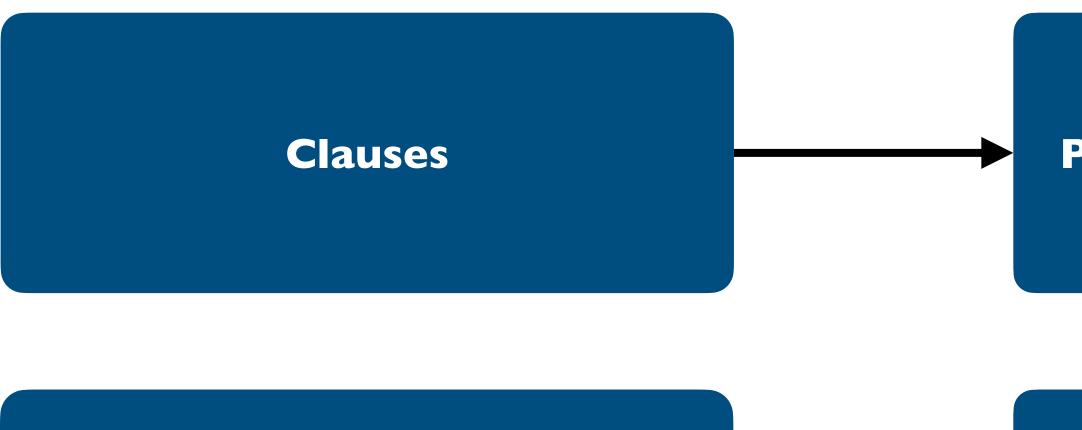
Polynomial relations checking



Resolution proof checking

Polynomials over a finite field

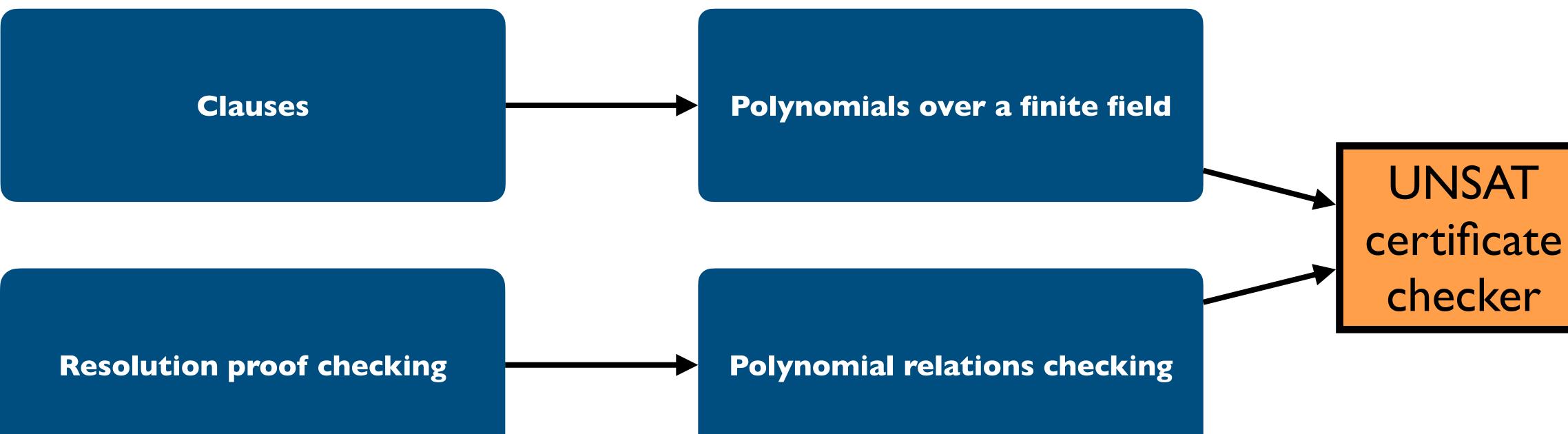
Polynomial relations checking



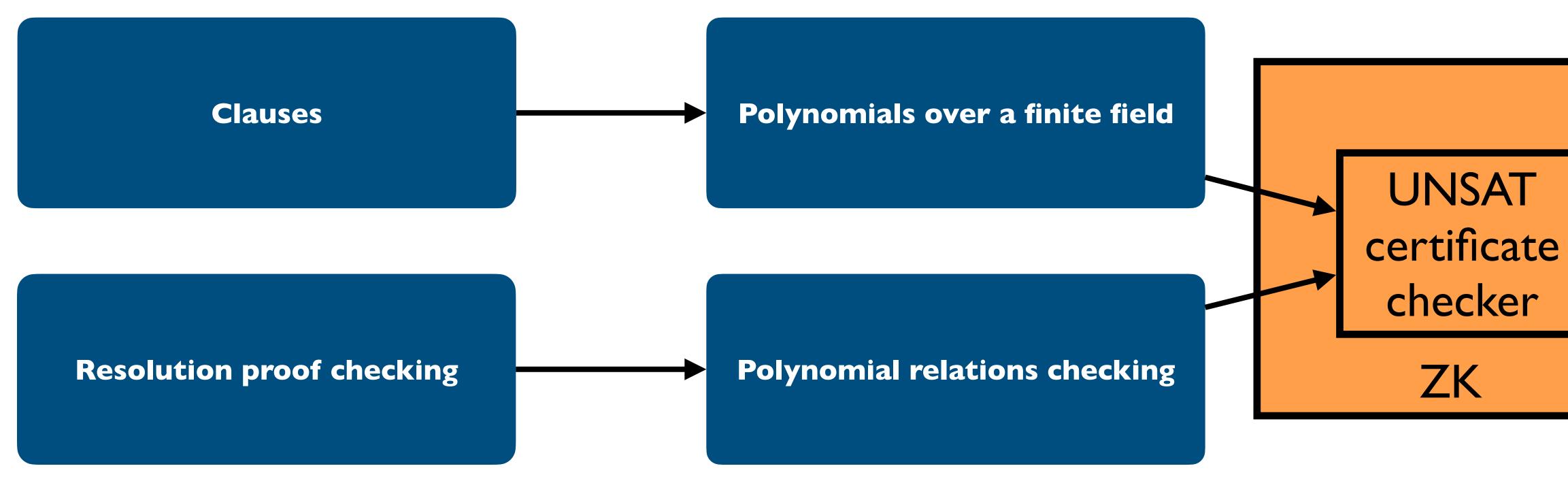
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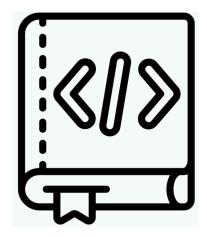
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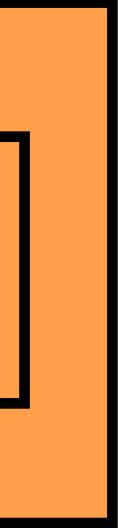






Efficient library for checking relations between polynomials in zero knowledge







- Background \bullet
 - Resolution Proof
 - Zero knowledge proof

• **ZKUNSAT**

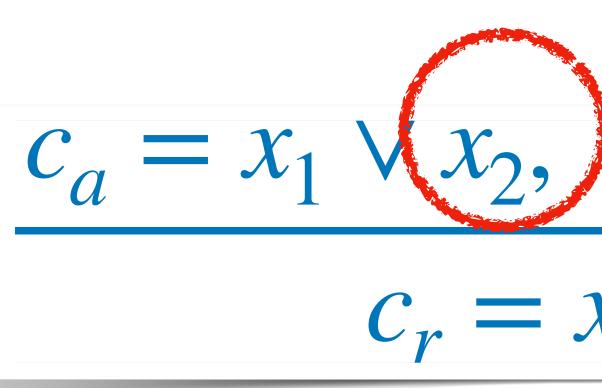
- Clause encoding and validating resolvents
- Clause access and checking consistency
- Evaluation
- Future Work

Refutation Proof Resolution Rule [Robinson, 65]

$$c_a = x_1 \lor x_2,$$
$$c_r = J$$

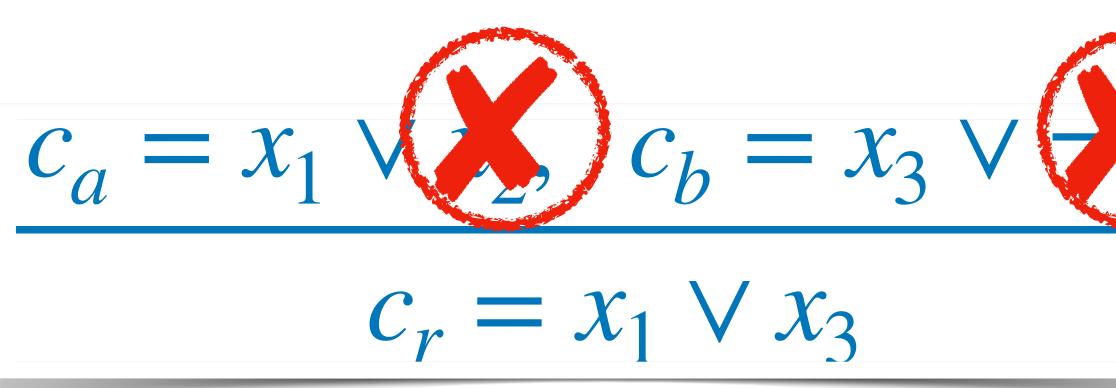
 $c_b = x_3 \vee \neg x_2$ $x_1 \vee x_3$

Refutation Proof Resolution Rule [Robinson, 65]



 $c_a = x_1 \lor x_2, \ c_b = x_3 \lor \neg x_2$ $C_r = x_1 \vee x_3$

Refutation Proof Resolution Rule [Robinson, 65]



 $C_r = x_1 \vee x_3$

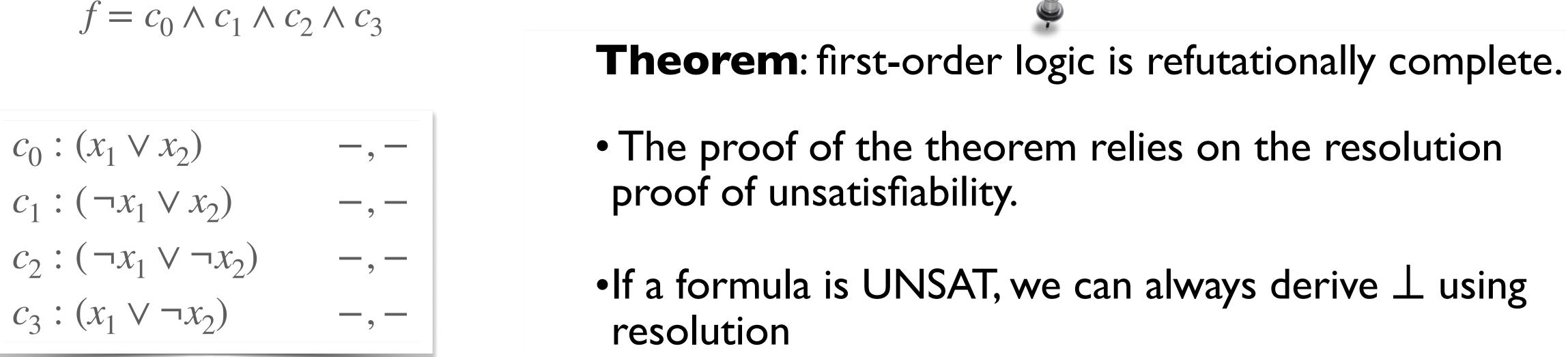
$$f = c_0 \wedge c_1 \wedge c_2 \wedge c_3$$

$$c_{0} : (x_{1} \lor x_{2}) \qquad -, -$$

$$c_{1} : (\neg x_{1} \lor x_{2}) \qquad -, -$$

$$c_{2} : (\neg x_{1} \lor \neg x_{2}) \qquad -, -$$

$$c_{3} : (x_{1} \lor \neg x_{2}) \qquad -, -$$



$$c_0: (x_1 \lor x_2)$$
 $-, c_1: (\neg x_1 \lor x_2)$
 $-, c_2: (\neg x_1 \lor \neg x_2)$
 $-, c_3: (x_1 \lor \neg x_2)$
 $-, c_4: x_2$
 $0, 1$
 $c_5: \neg x_2$
 $2, 3$
 $c_6: \bot$
 $4, 5$

 $c_0: (x_1 \lor x_2)$ $c_1: (\neg x_1 \lor x_2)$ $c_2:(\neg x_1 \lor \neg x_2)$ $c_3:(x_1 \vee \neg x_2)$ ____ 0, 1 $c_4: x_2$ 2, 3 $c_5: \neg x_2$ c_6 : \bot 4, 5

- resolution



 The proof of the theorem relies on the resolution proof of unsatisfiability.

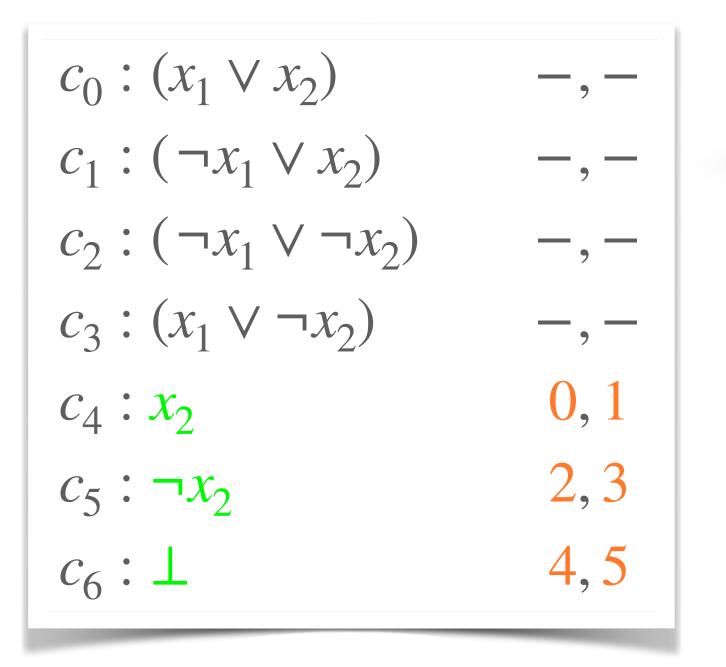
•If a formula is UNSAT, we can always derive \perp using

 $c_0: (x_1 \lor x_2) \qquad -,$ $c_1:(\neg x_1 \lor x_2) \qquad -,$ $c_2:(\neg x_1 \lor \neg x_2) \qquad -,$ $c_3: (x_1 \lor \neg x_2)$ _, _ 0, 1 $c_4 : x_2$ 2, 3 $c_5: \neg x_2$ c_6 : \bot 4, 5





Theorem: first-order logic is refutationally complete.



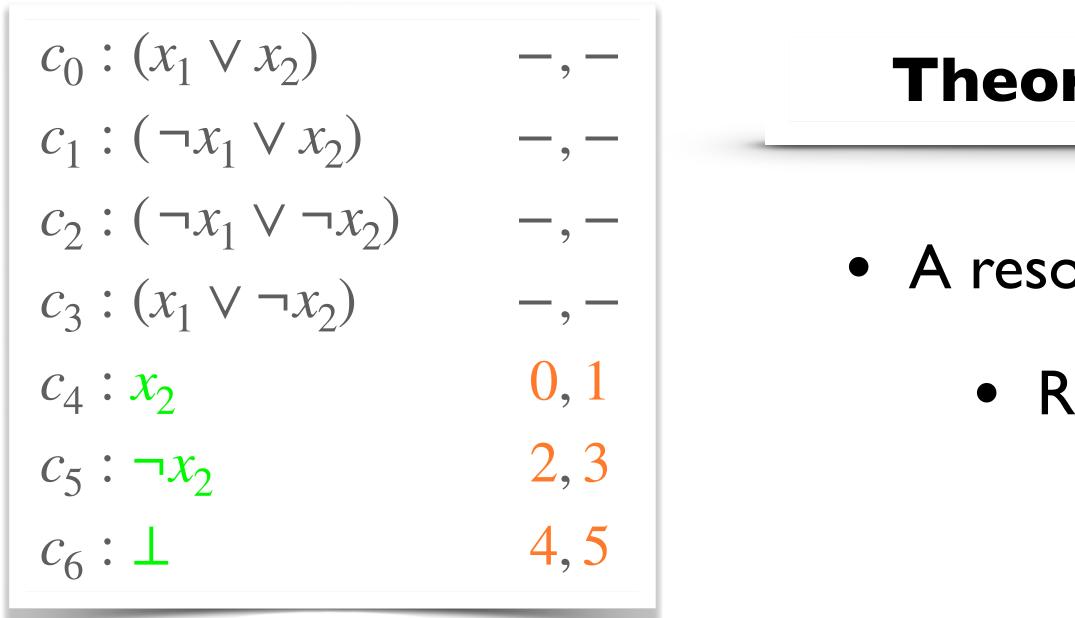


• A resolution proof consists of a list of



Theorem: first-order logic is refutationally complete.

9.



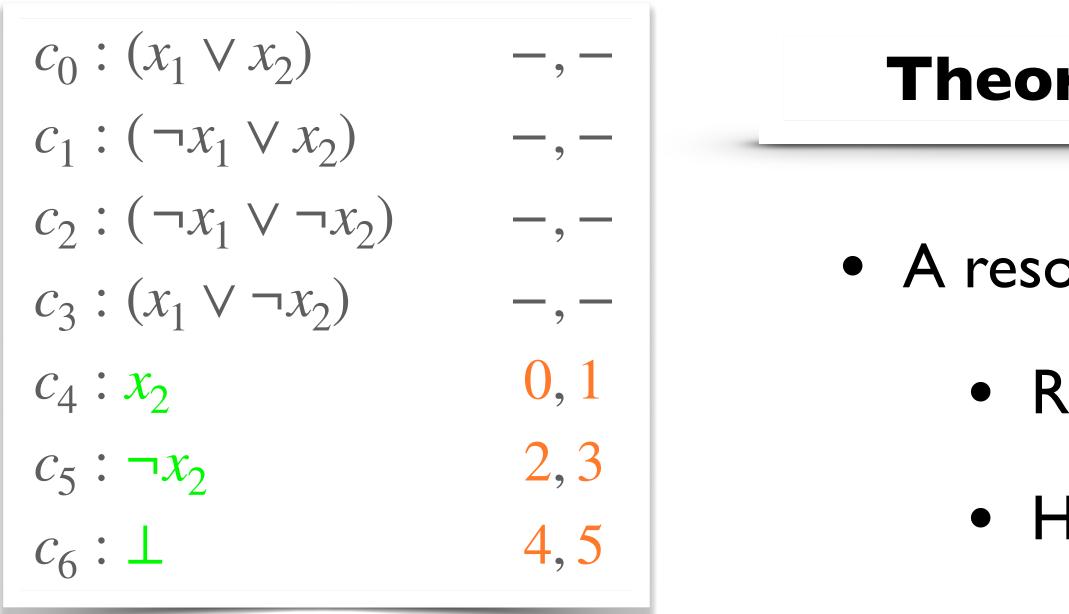


Theorem: first-order logic is refutationally complete.

• A resolution proof consists of a list of

Resolvents

9.





Theorem: first-order logic is refutationally complete.

• A resolution proof consists of a list of

Resolvents

• How these resolvents could be obtained.

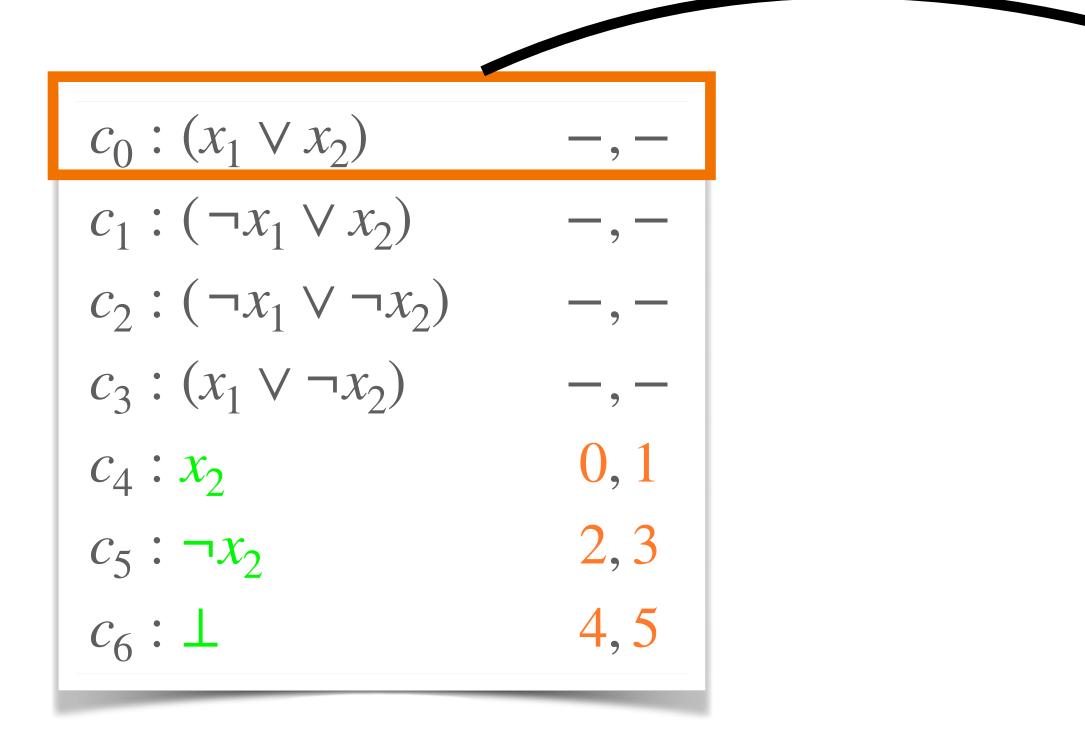
9.

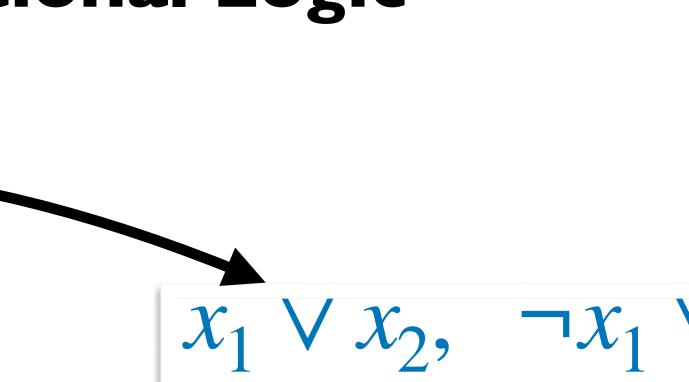
$$c_0: (x_1 \lor x_2)$$
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 $-, c_3: (x_1 \lor \neg x_2)$
 $-, c_4: x_2$
 $0, 1$
 $c_5: \neg x_2$
 $2, 3$
 $c_6: \bot$
 $4, 5$

$$\begin{array}{c} x_1 \lor x_2, \ \neg x_1 \lor x_2 \\ x_2 \end{array}$$

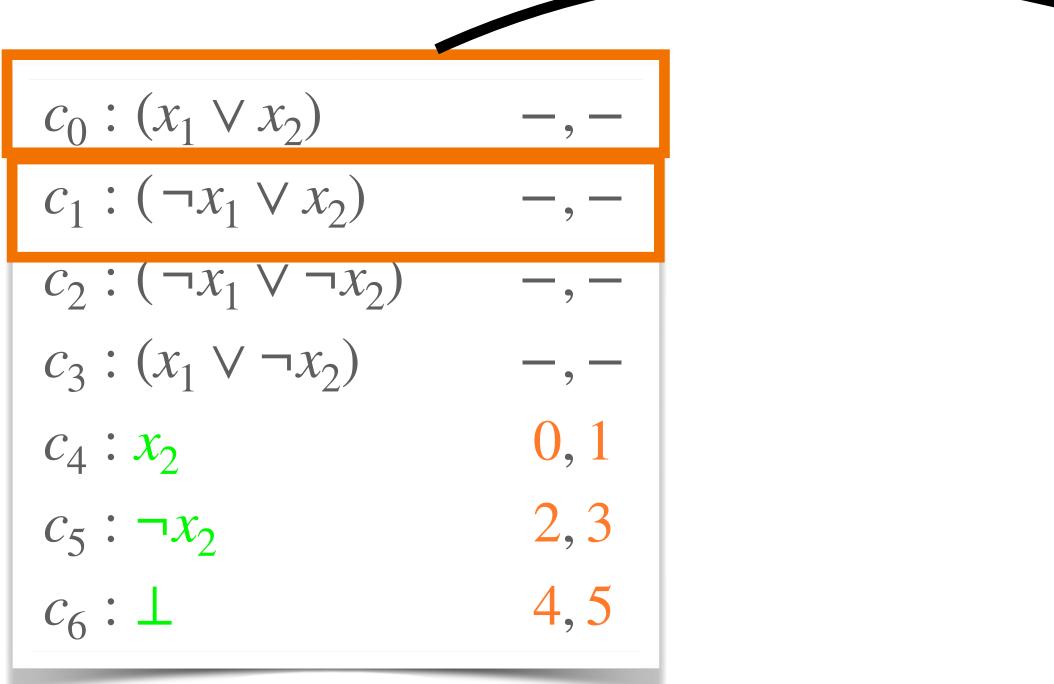
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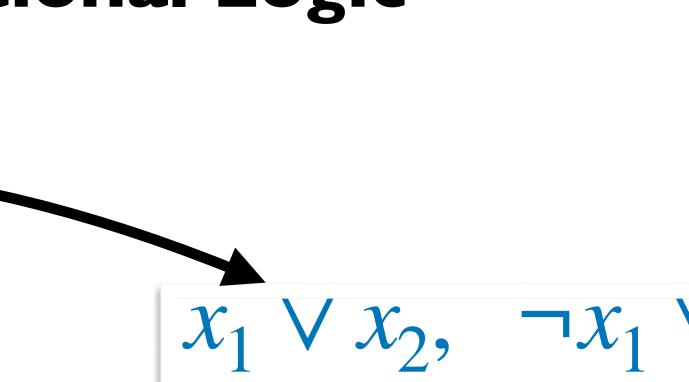
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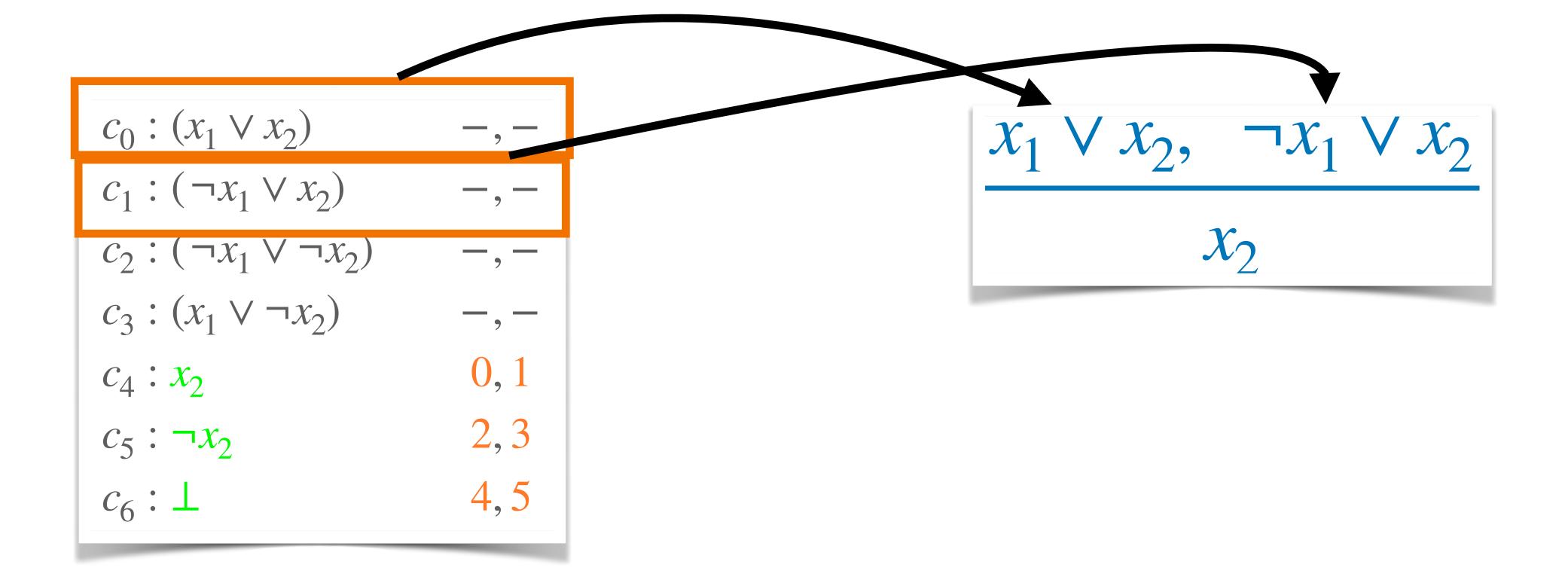


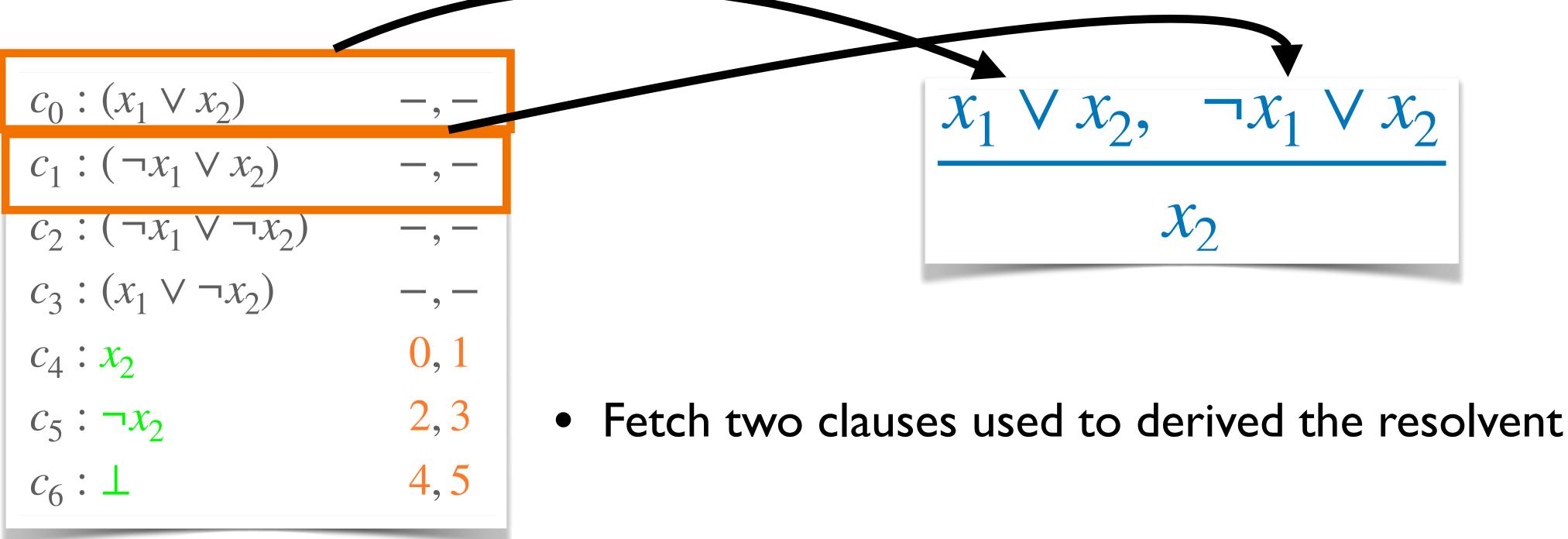
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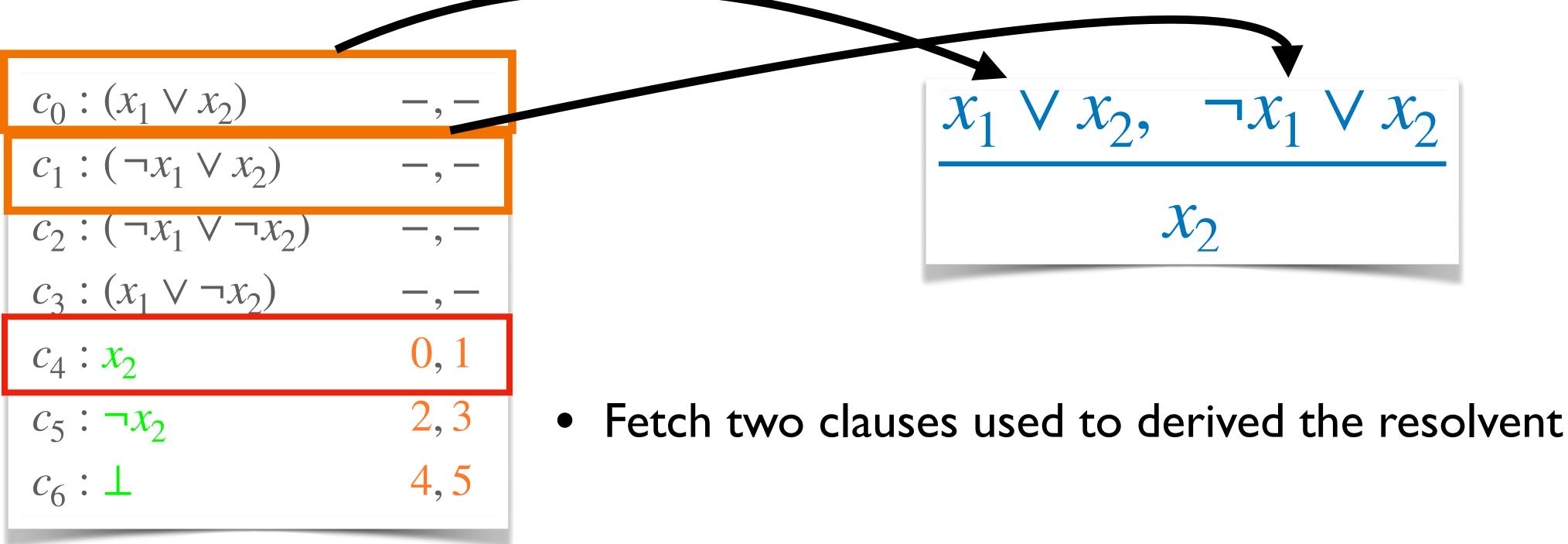


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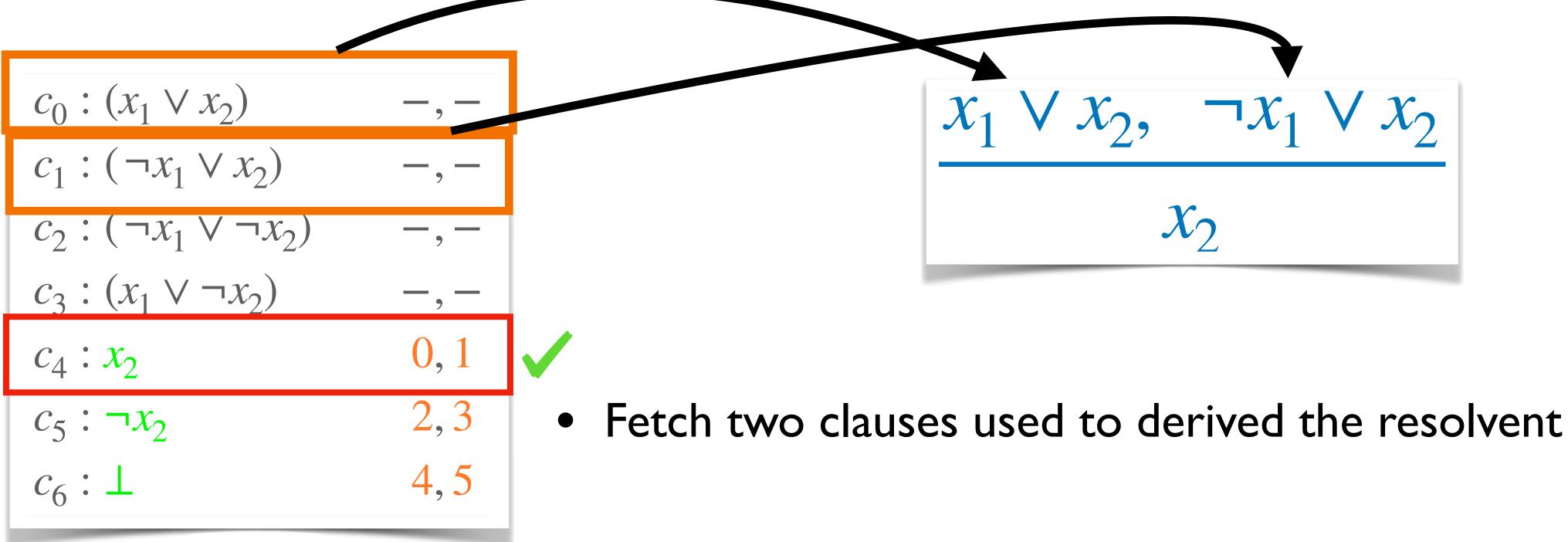




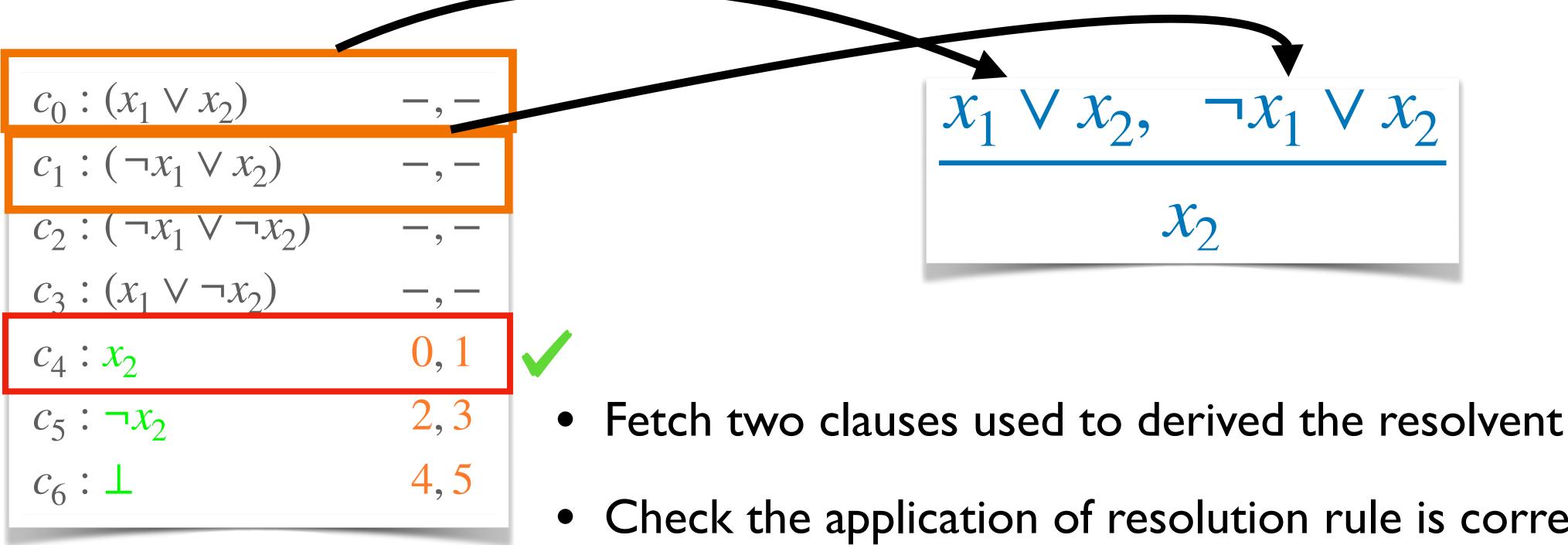














- Check the application of resolution rule is correctly executed



$$c_{0} : (x_{1} \lor x_{2})$$

$$c_{1} : (\neg x_{1} \lor x_{2})$$

$$c_{2} : (\neg x_{1} \lor \neg x_{2})$$

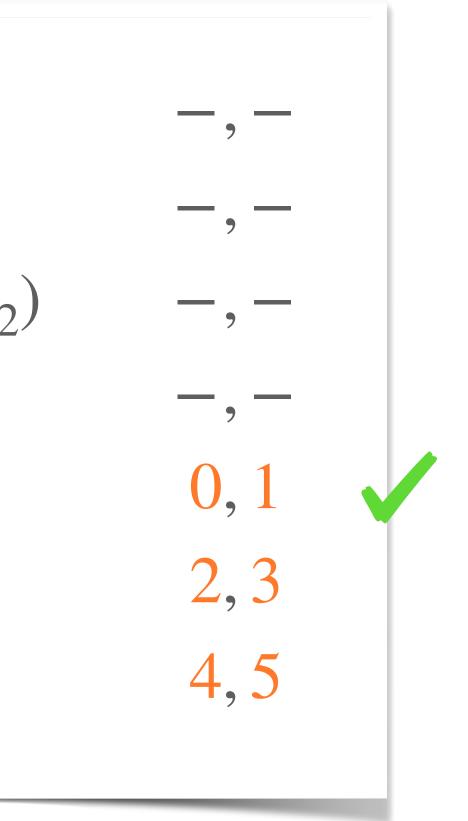
$$c_{3} : (x_{1} \lor \neg x_{2})$$

$$c_{4} : x_{2}$$

$$c_{5} : \neg x_{2}$$

$$c_{6} : \bot$$

Repeat for each resolvent until meet a contradiction



$$c_{0} : (x_{1} \lor x_{2})$$

$$c_{1} : (\neg x_{1} \lor x_{2})$$

$$c_{2} : (\neg x_{1} \lor \neg x_{2})$$

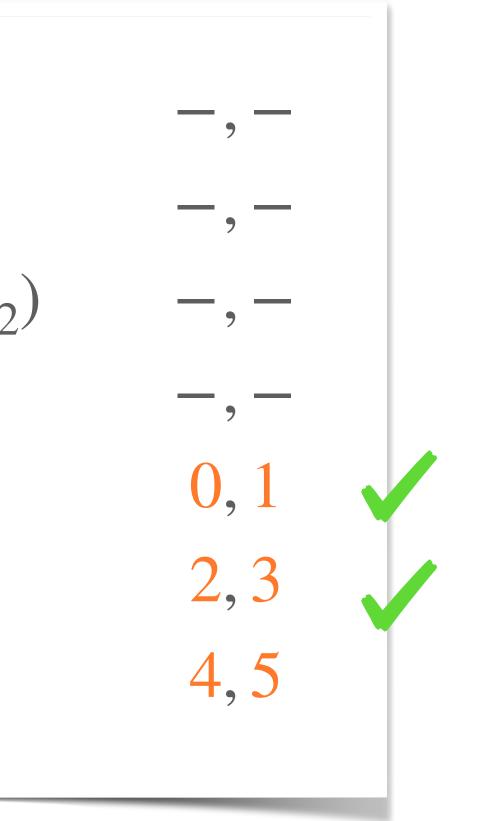
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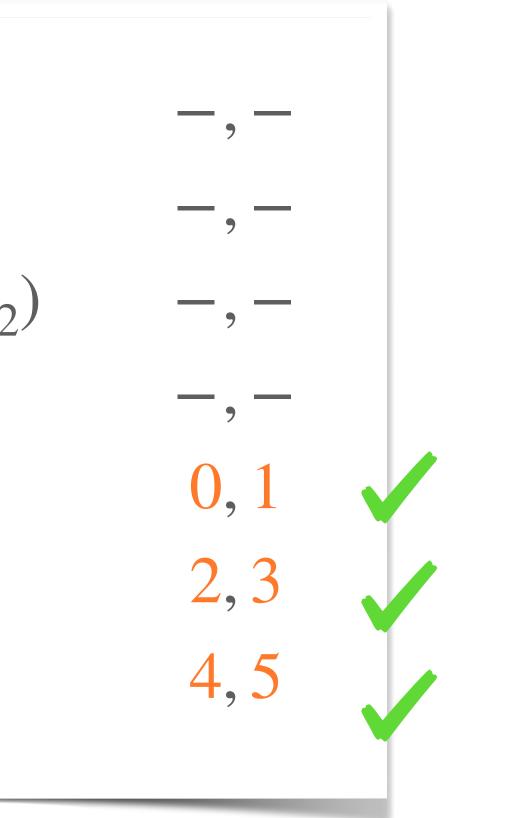
$$c_{3} : (x_{1} \lor \neg x_{2})$$

$$c_{4} : x_{2}$$

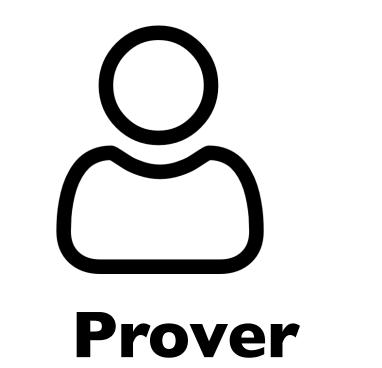
$$c_{5} : \neg x_{2}$$

$$c_{6} : \bot$$

Repeat for each resolvent until meet a contradiction



Zero Knowledge Proof : Coke or Pepsi



I know a method to tell coke or pepsi!

show me how otherwise I won't believe

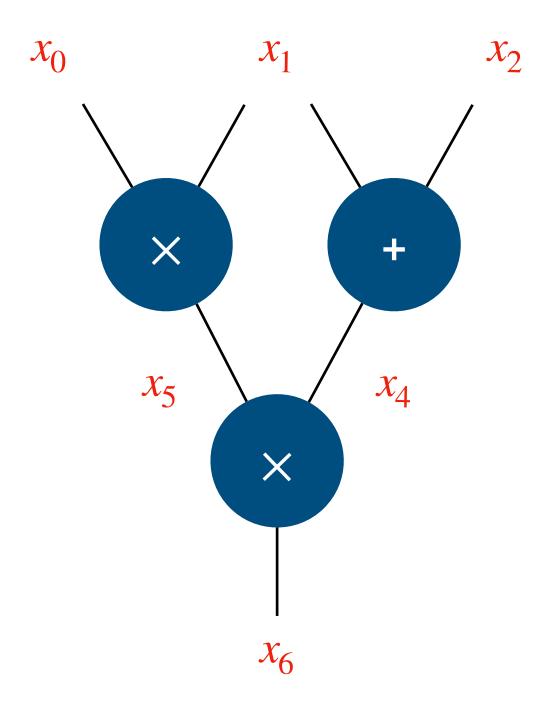
I will prove that I know without showing how





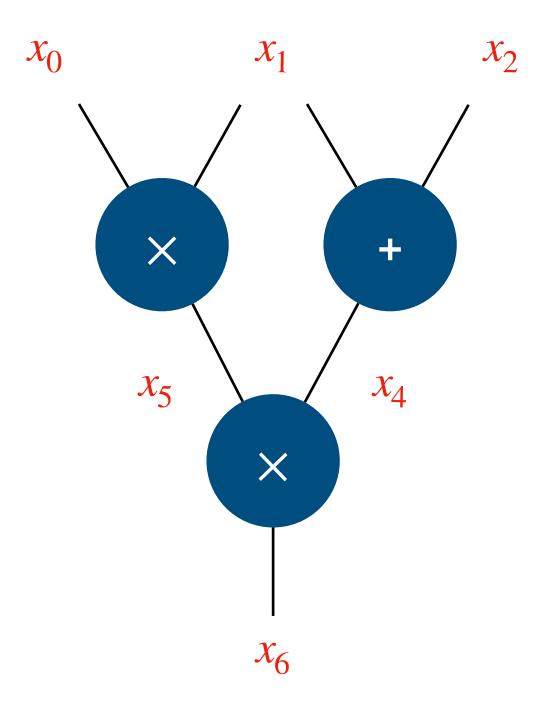






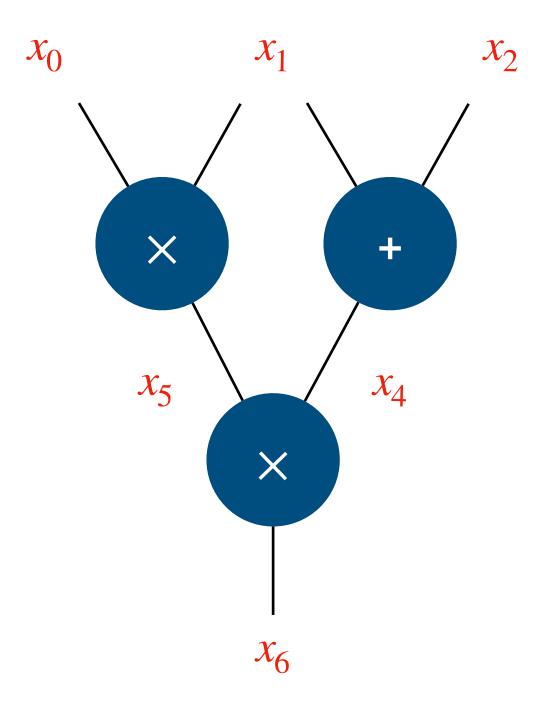
- **Prover** knows an input w such that C(w) = 1, and tries to
 - Convince the verifier that C(w) = 1
 - Keep information of *W* private





- **Prover** knows an input w such that C(w) = 1, and tries to
 - Convince the verifier that C(w) = 1
 - Keep information of *W* private
- Verifier:
 - Validate prover's claim about the circuit C



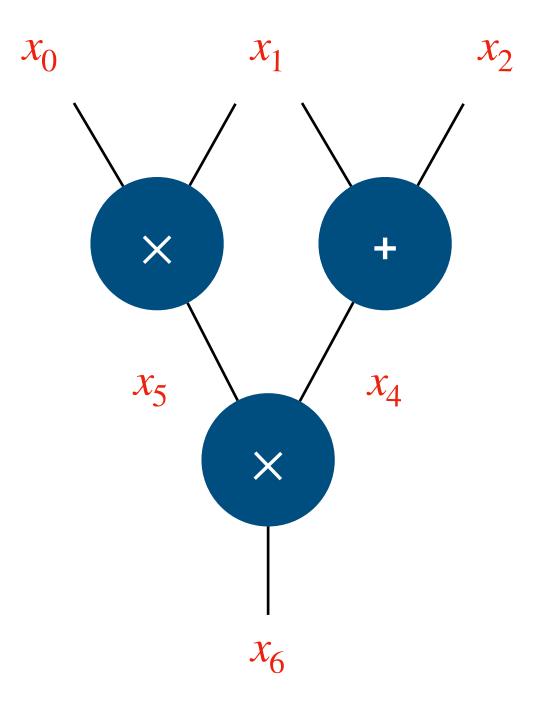


- **Prover** knows an input w such that C(w) = 1, and tries to
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- **Prover** knows an input w such that C(w) = 1, and tries to
 - Convince the verifier that C(w) = 1
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- Verifier:
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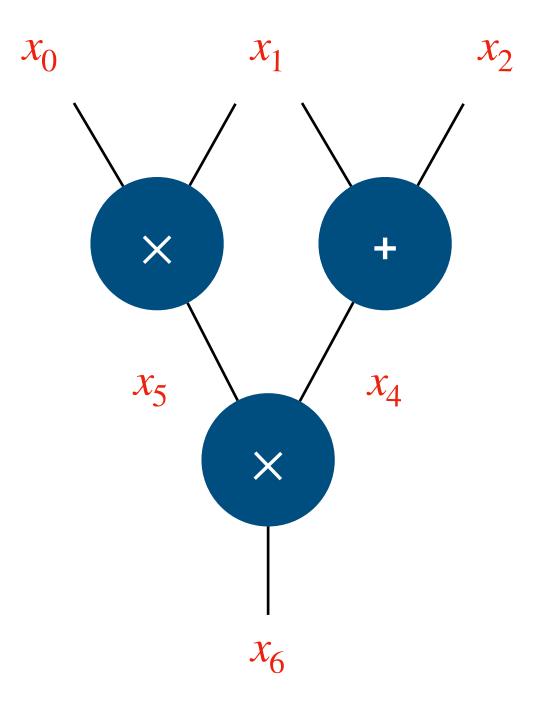
Both of prover and verifier can be malicious.

- Prover might cheat
- Verifier tries to learn w

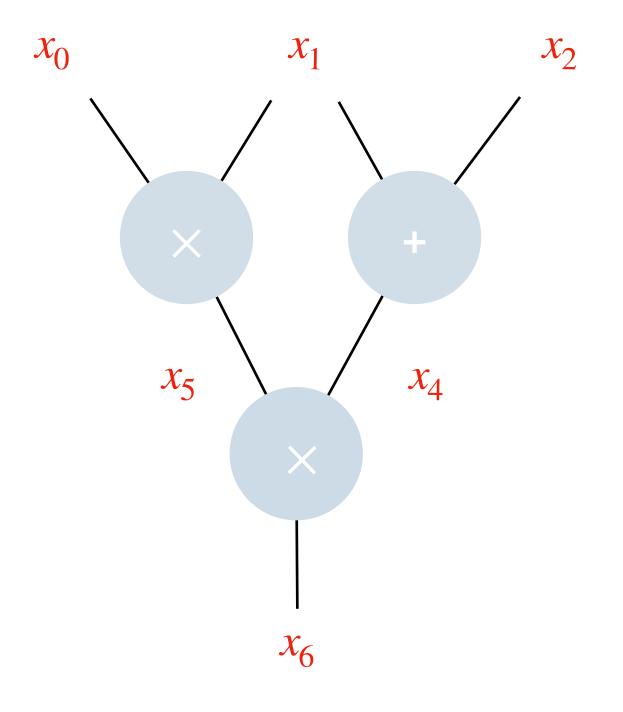






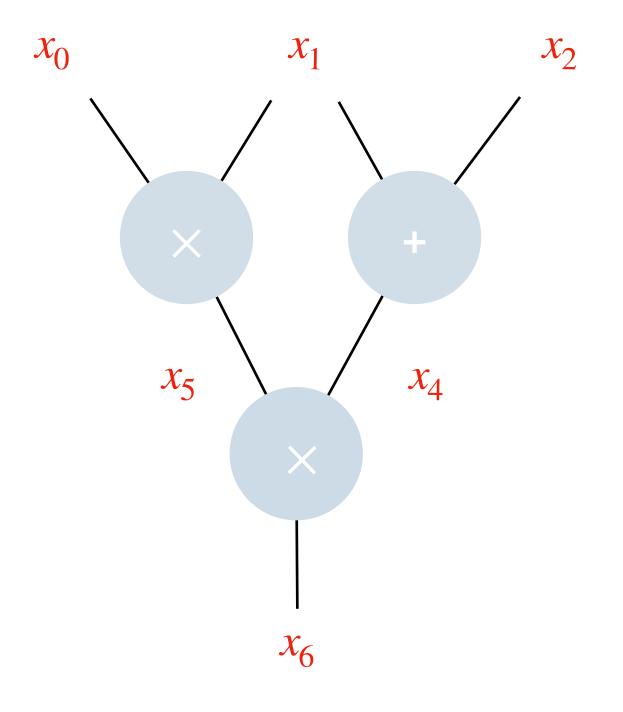






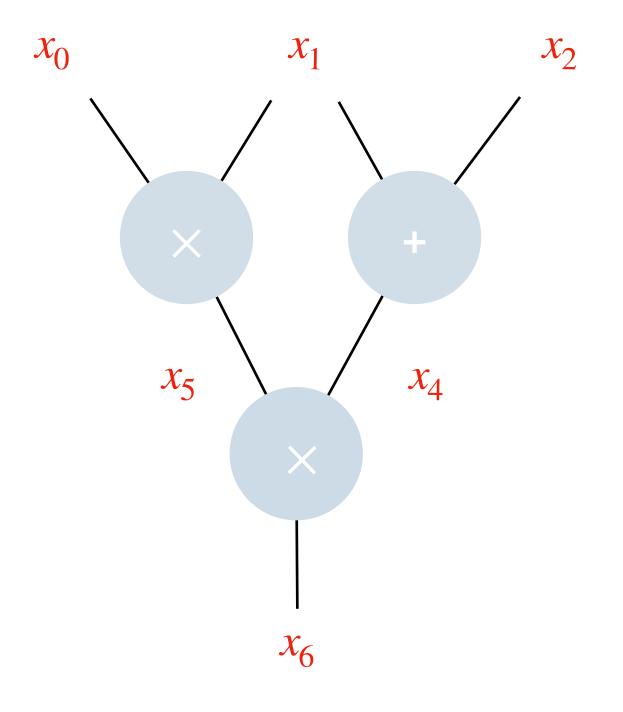


• Authenticated value: ciphertext that



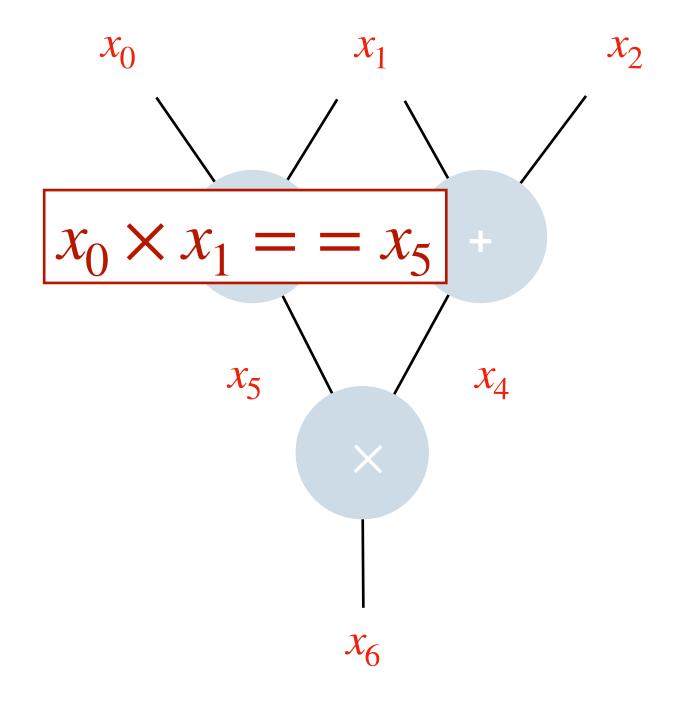
- Authenticated value: ciphertext that
 - Hide underlying value





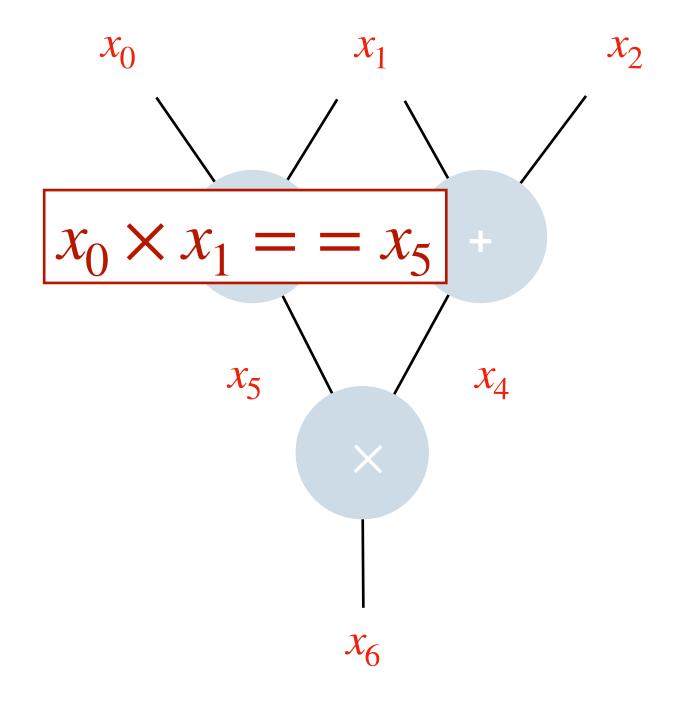
- Authenticated value: ciphertext that
 - Hide underlying value





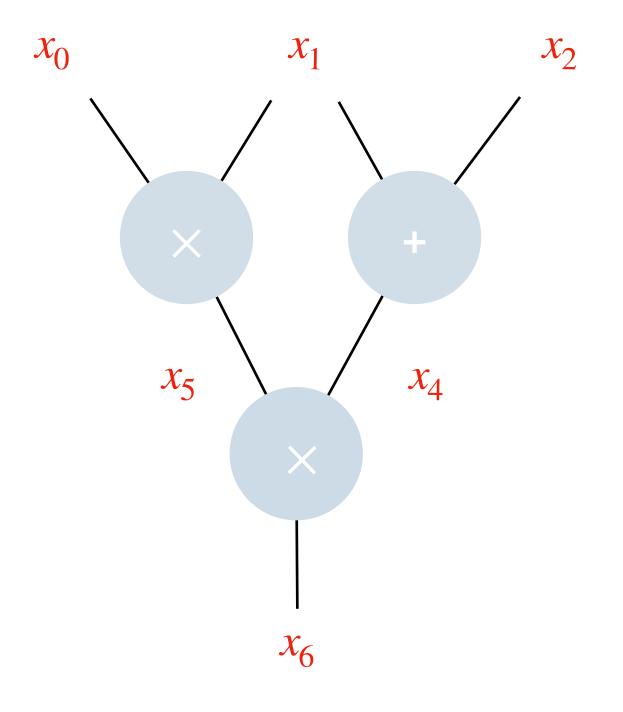
- Authenticated value: ciphertext that
 - Hide underlying value
 - Enable verifier to check the relations





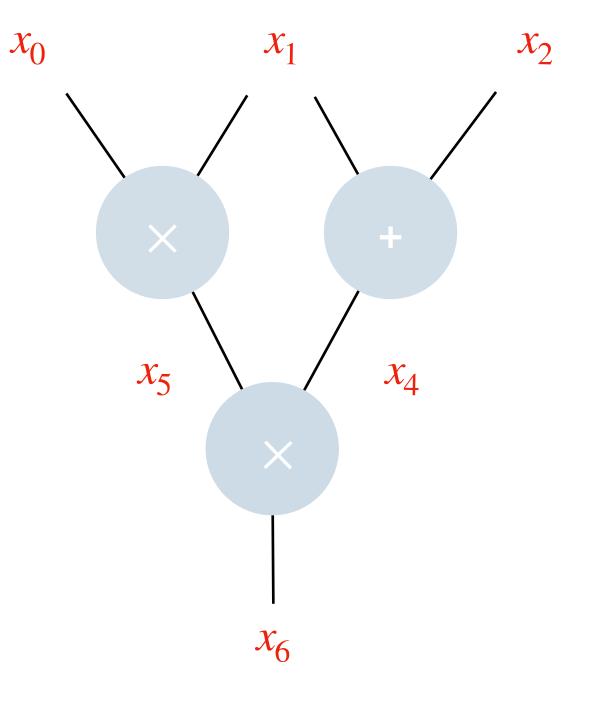
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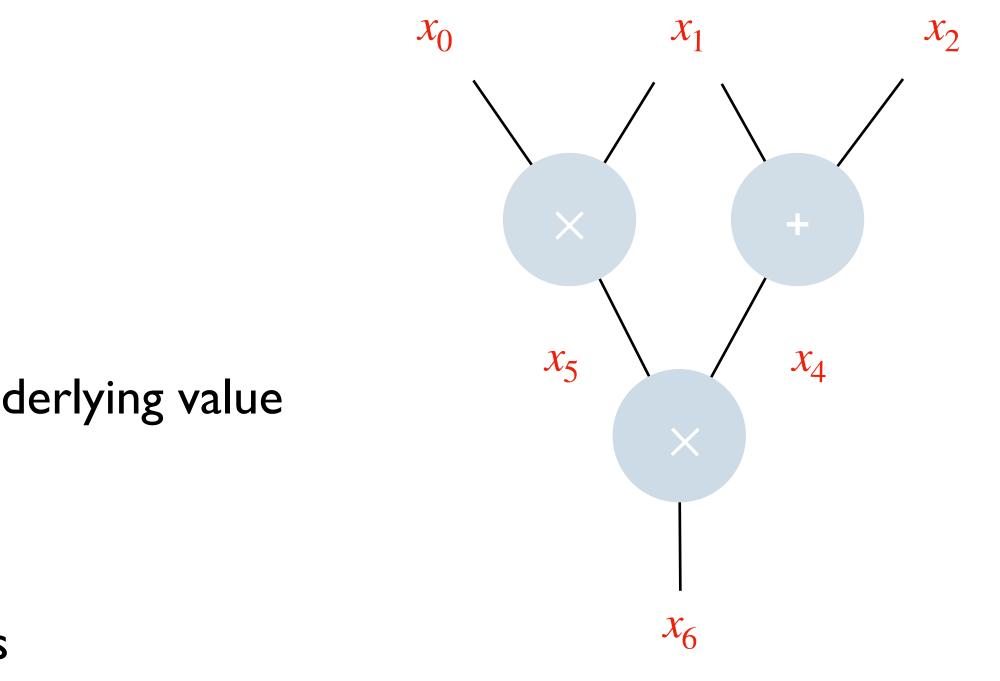
- Authenticated value: ciphertext that
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 - Enable verifier to check the relations
 - Prevent prover from cheating about the underlying value





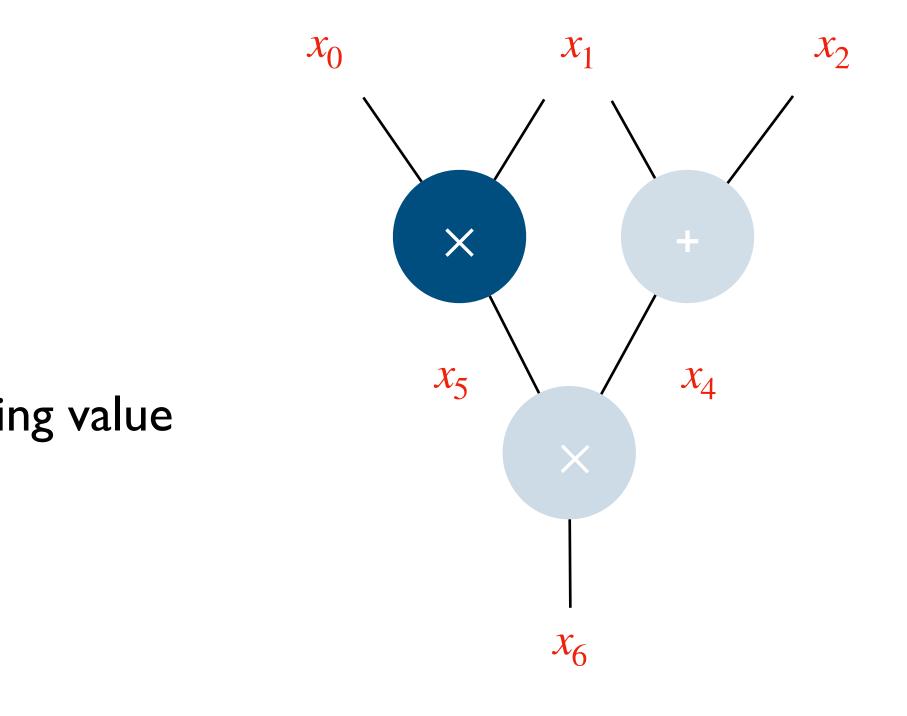
- Authenticated value: ciphertext that
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- Gate-by-gate paradigm [Beaver et al. 90]:
 - Prover authenticates the value over all wires
 - Verifier checks if input and output of each gate is consistent over the *ciphertext*
 - Prover reveals the value of output of the circuit





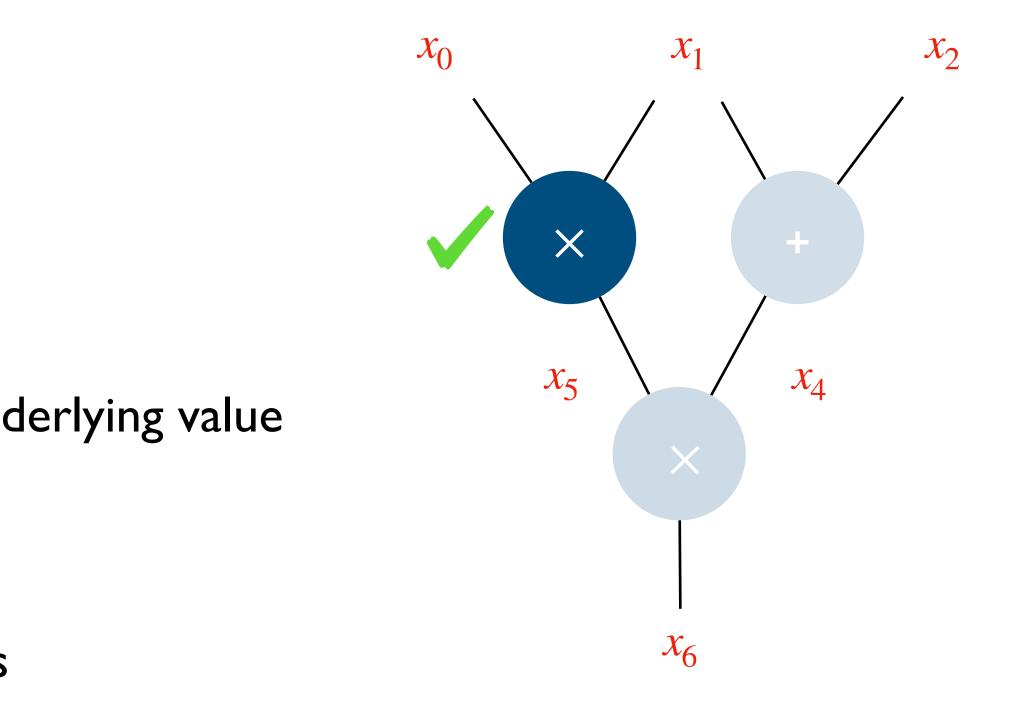
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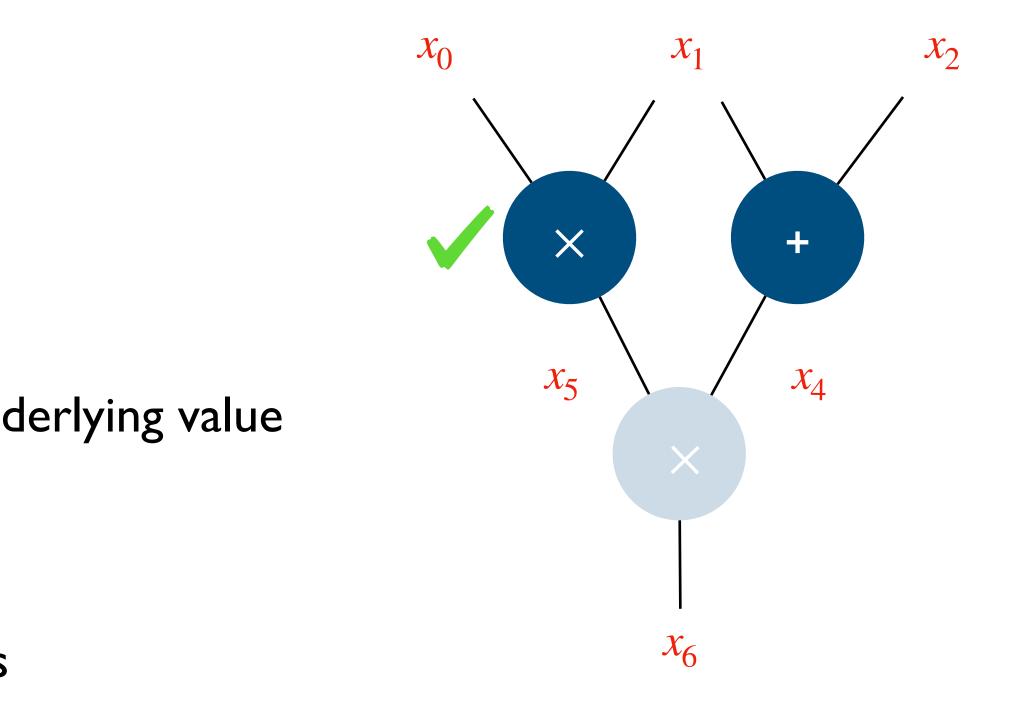
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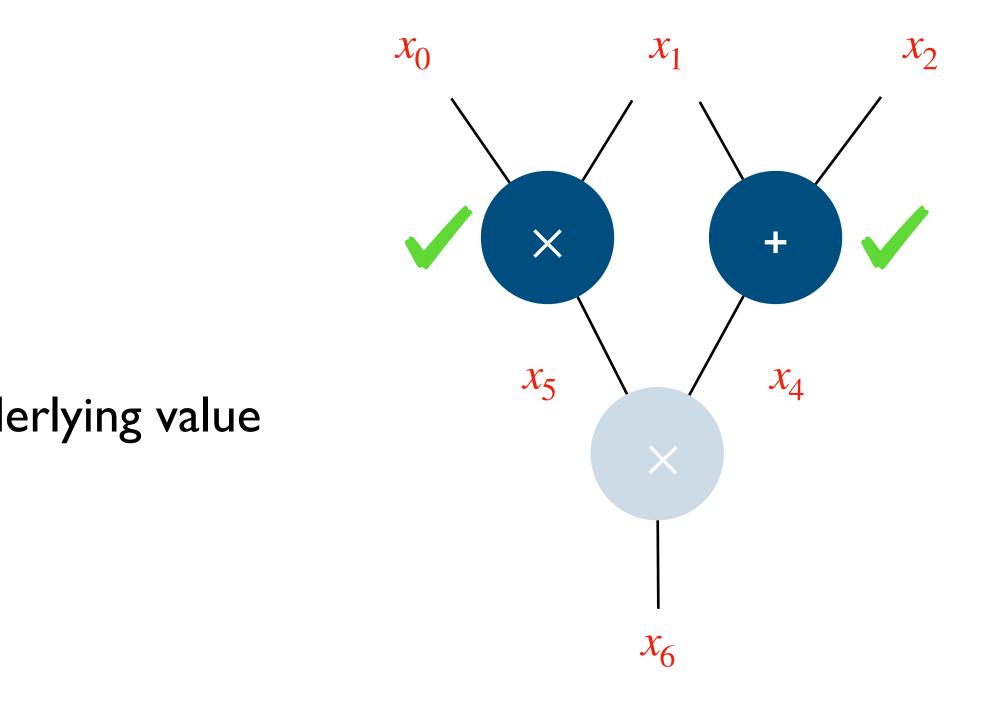
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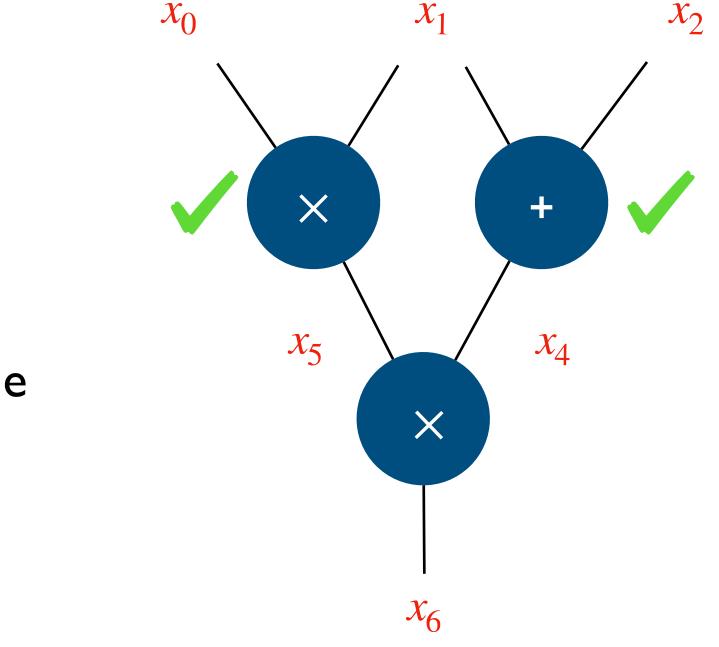
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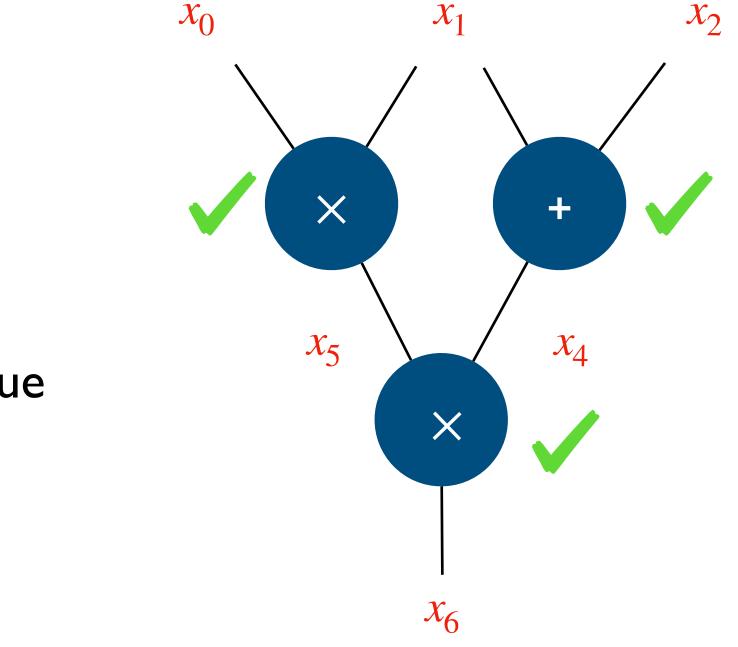
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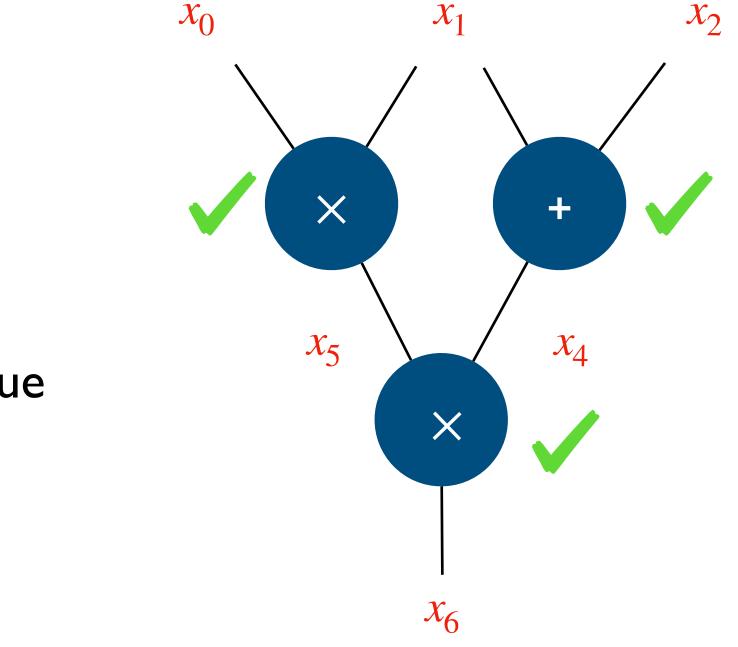
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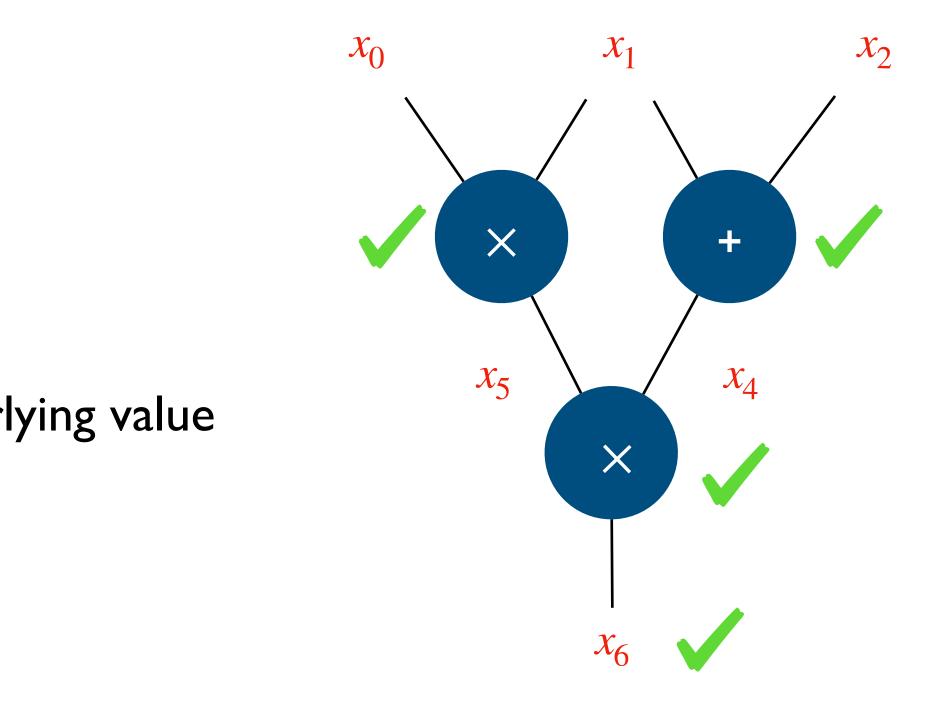
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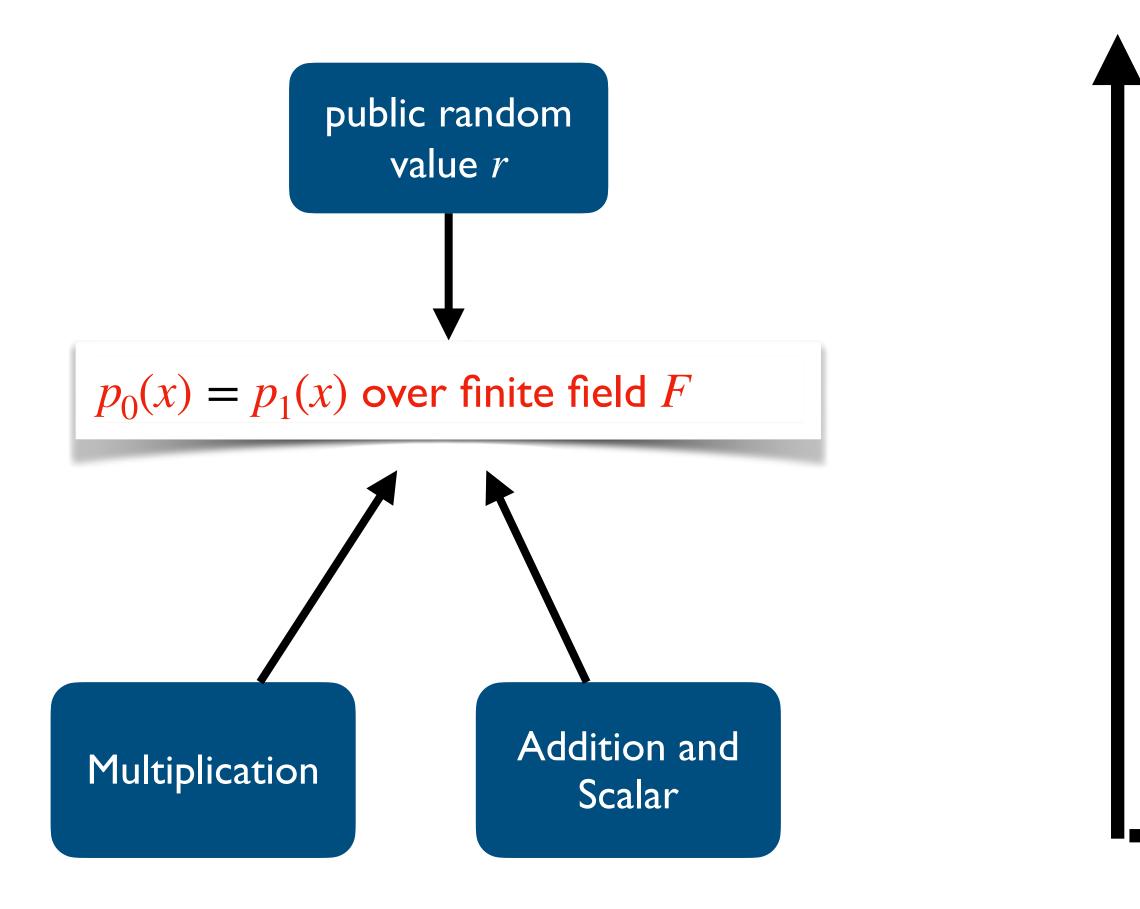




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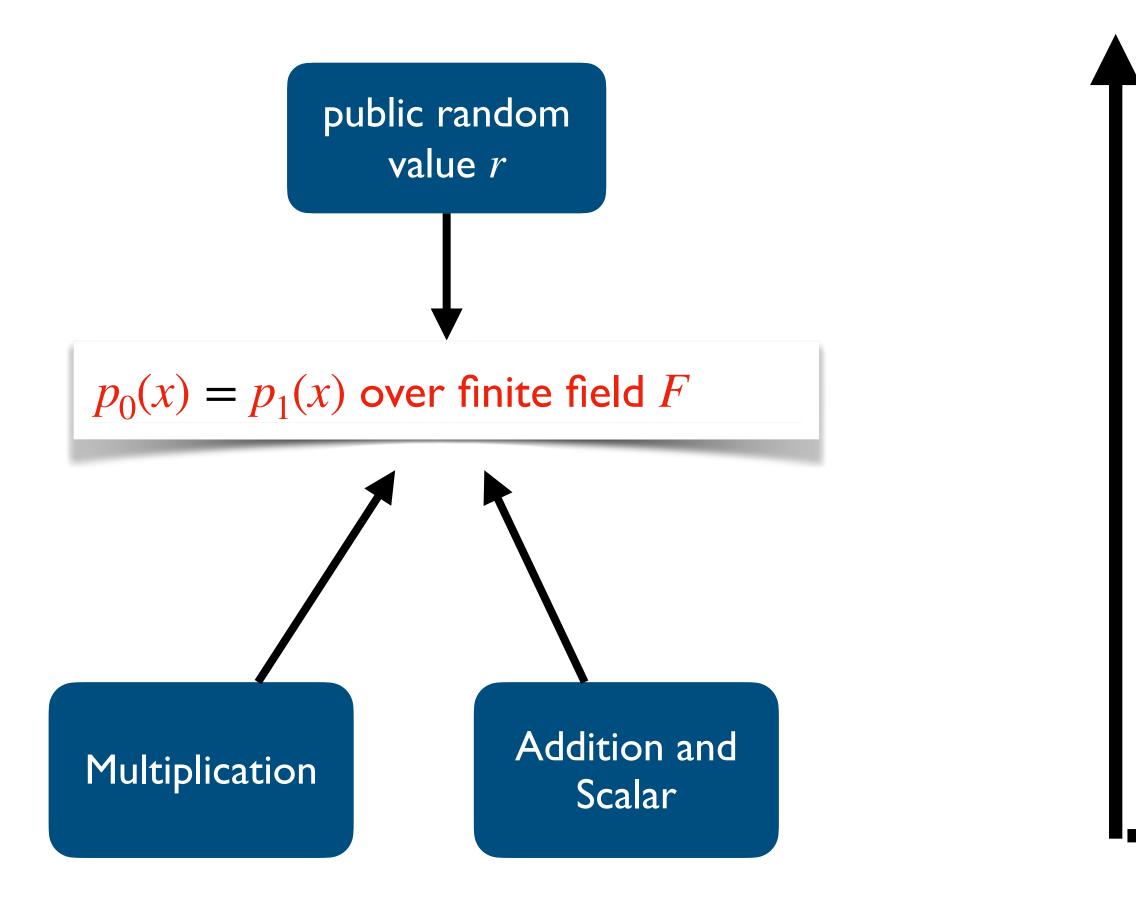




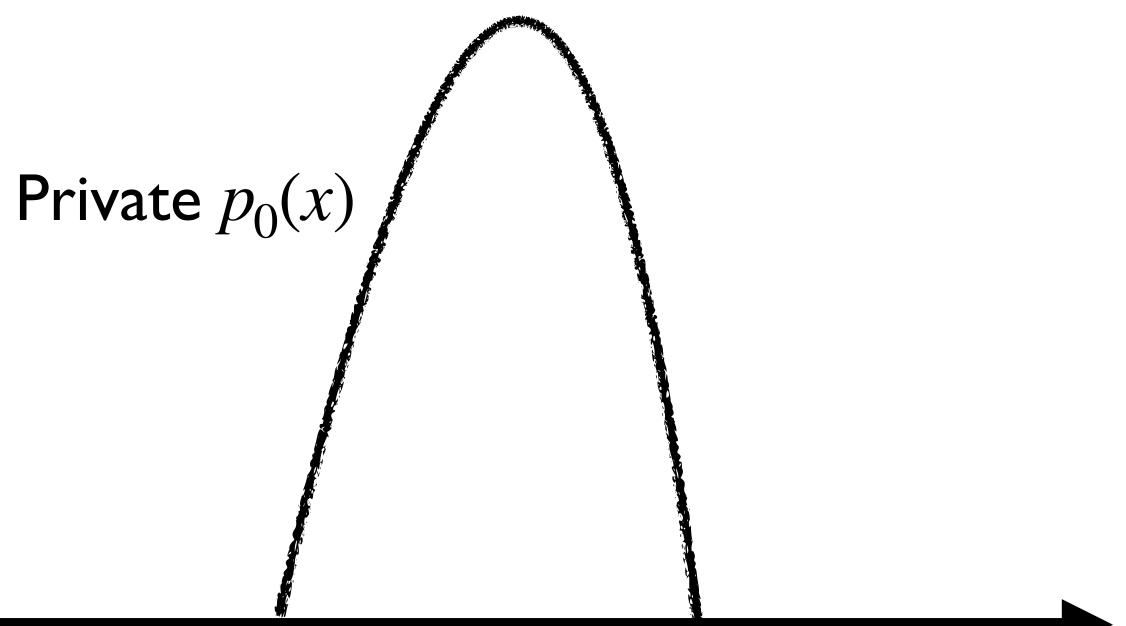


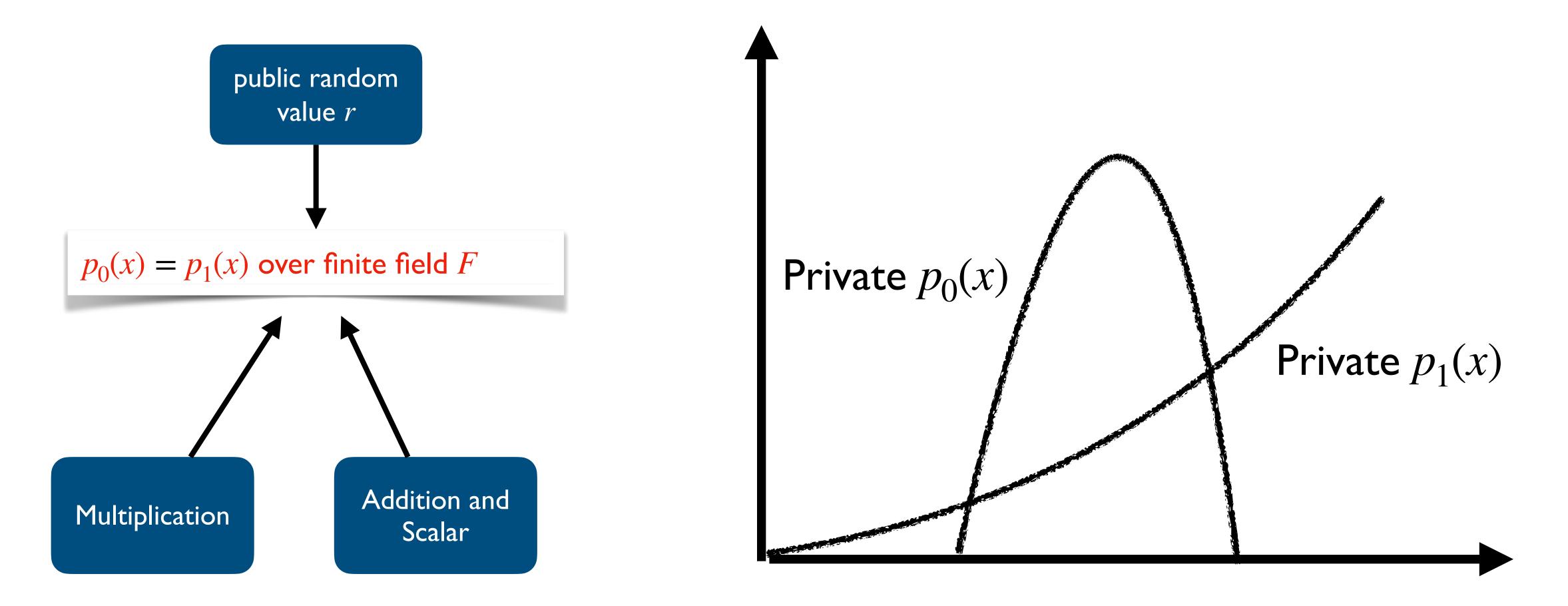




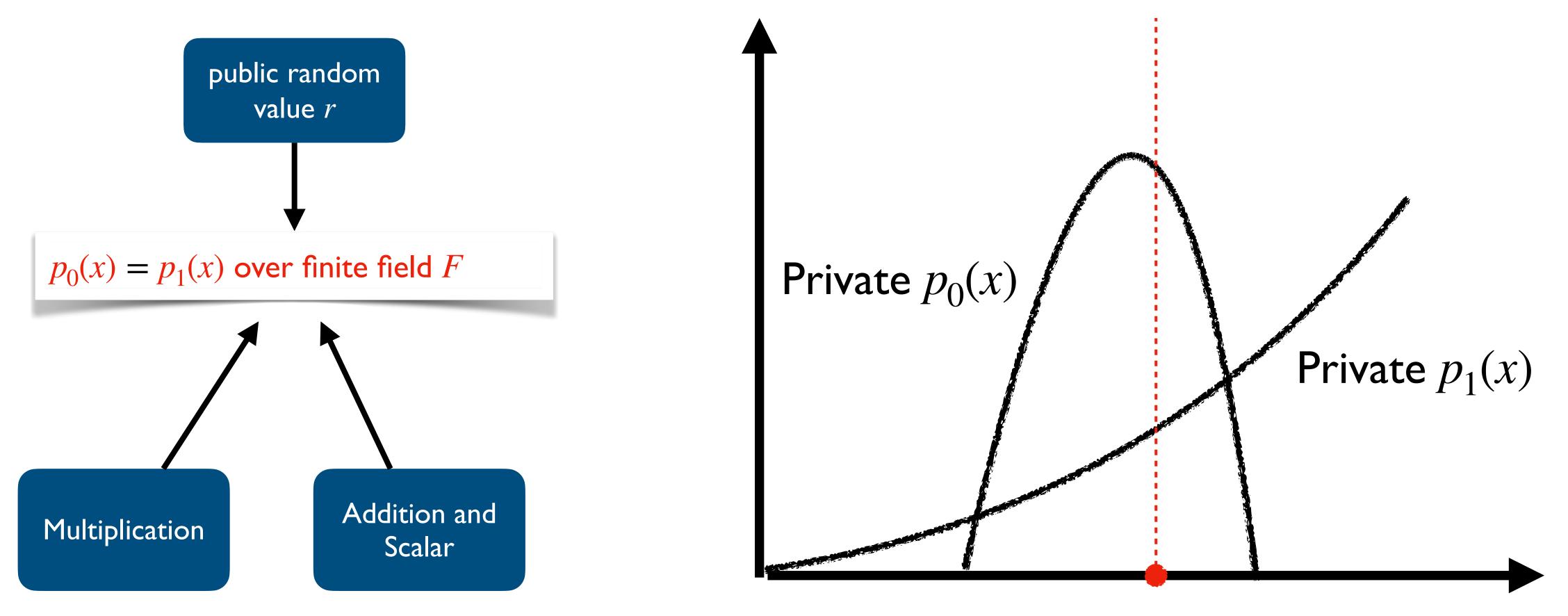




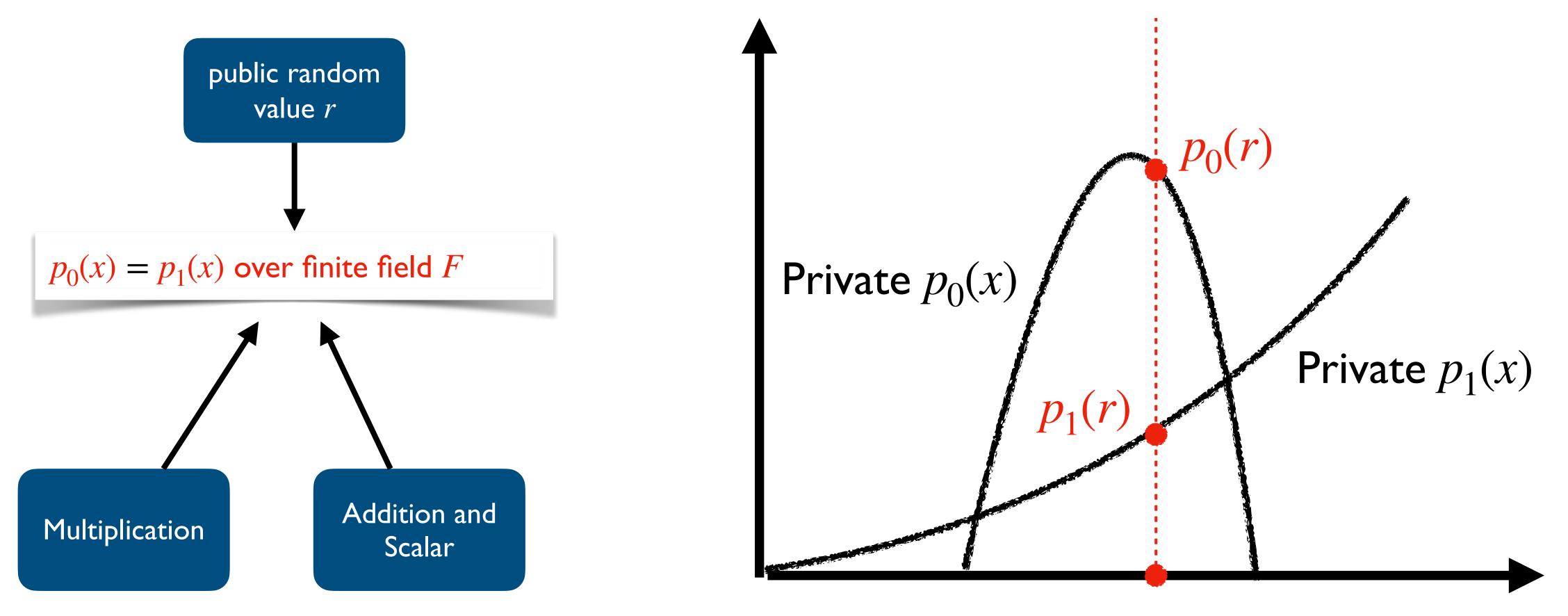




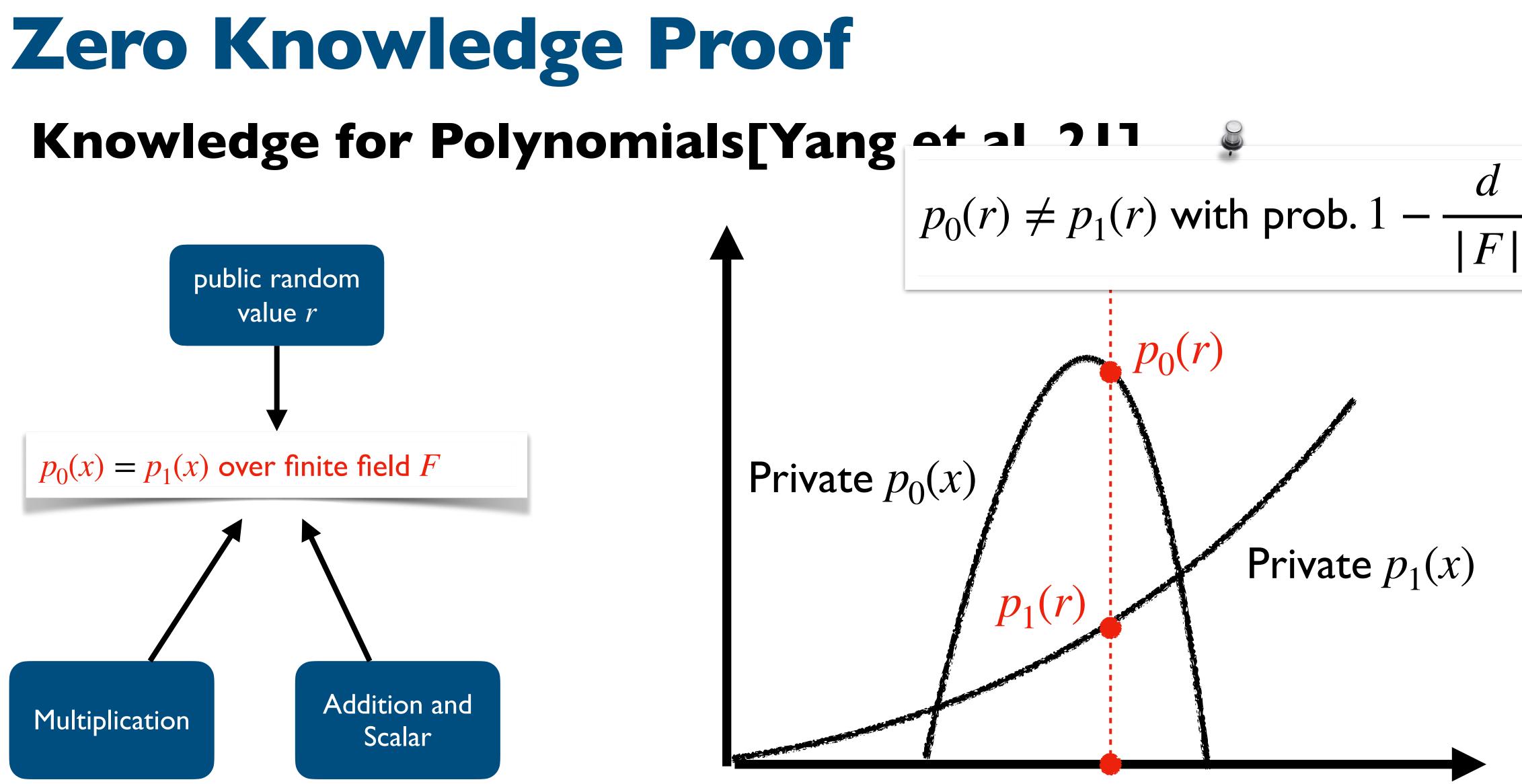


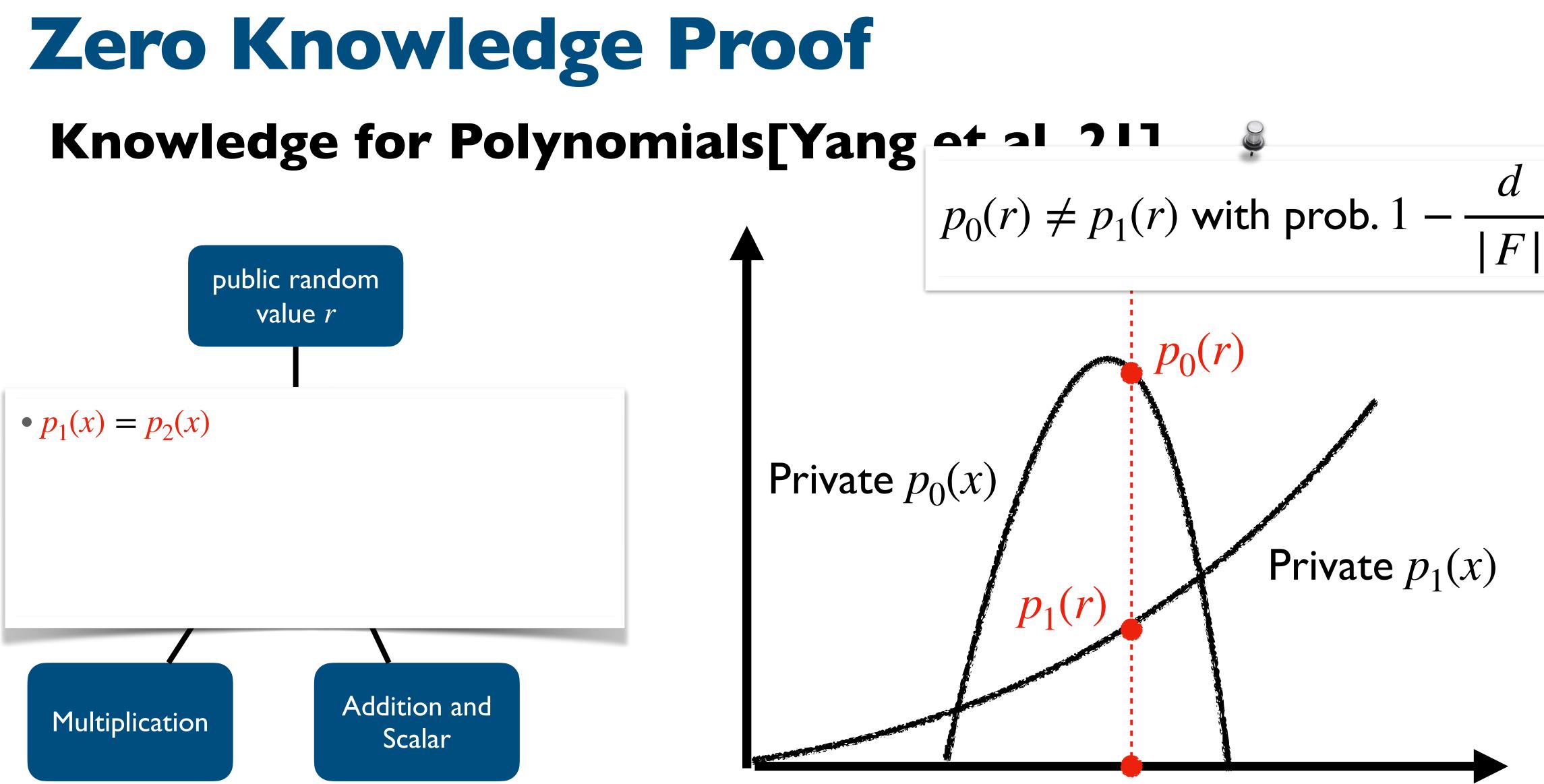


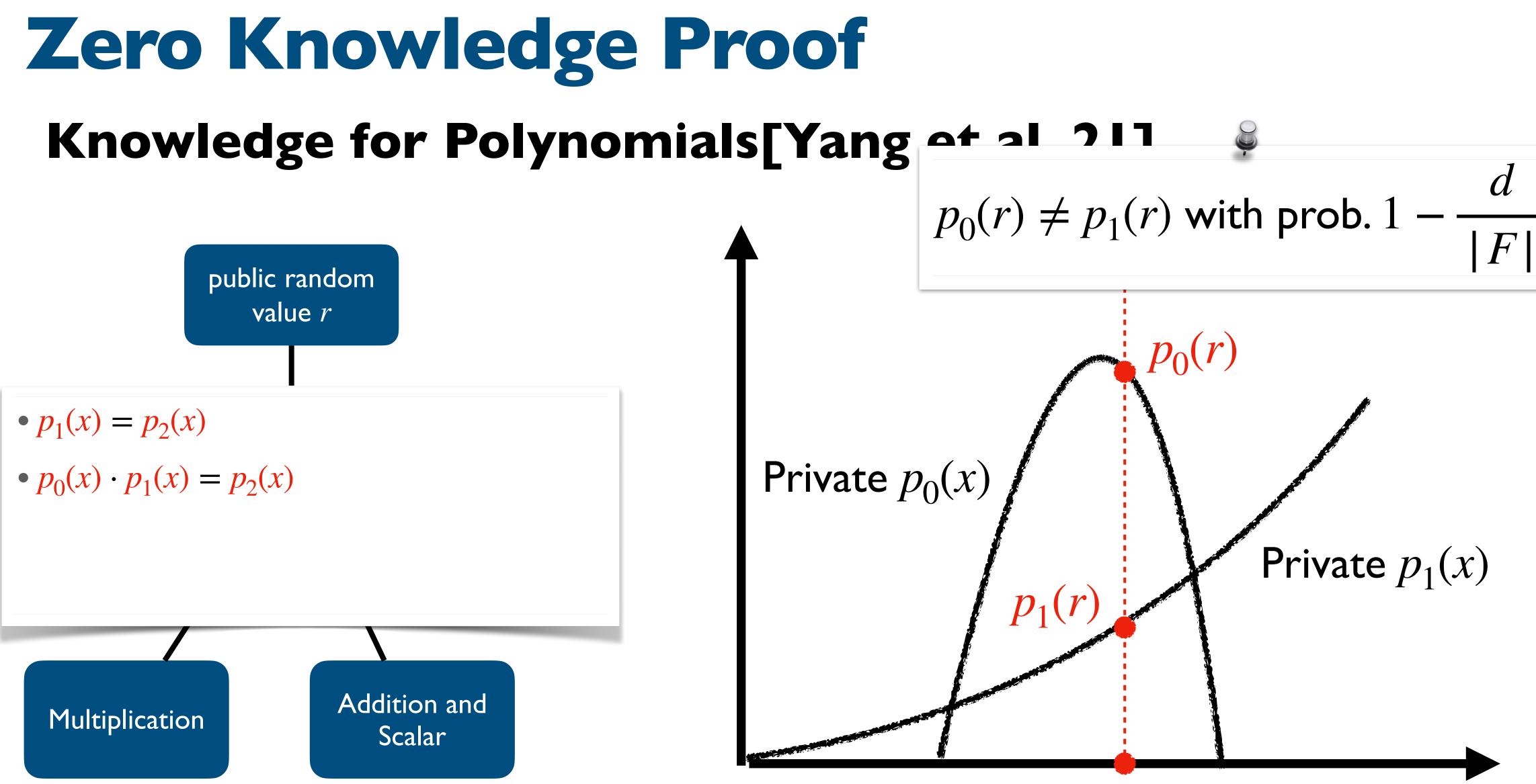


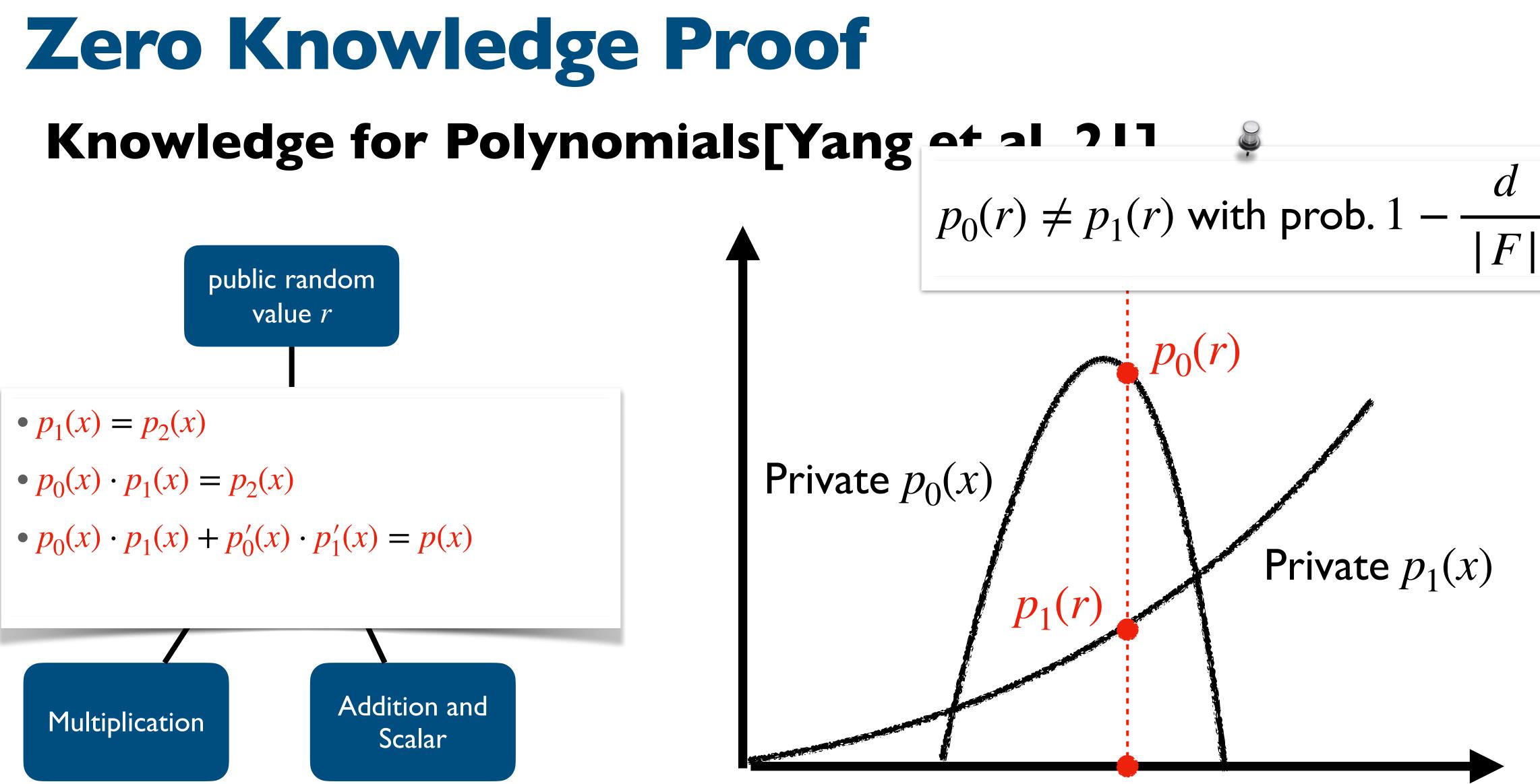


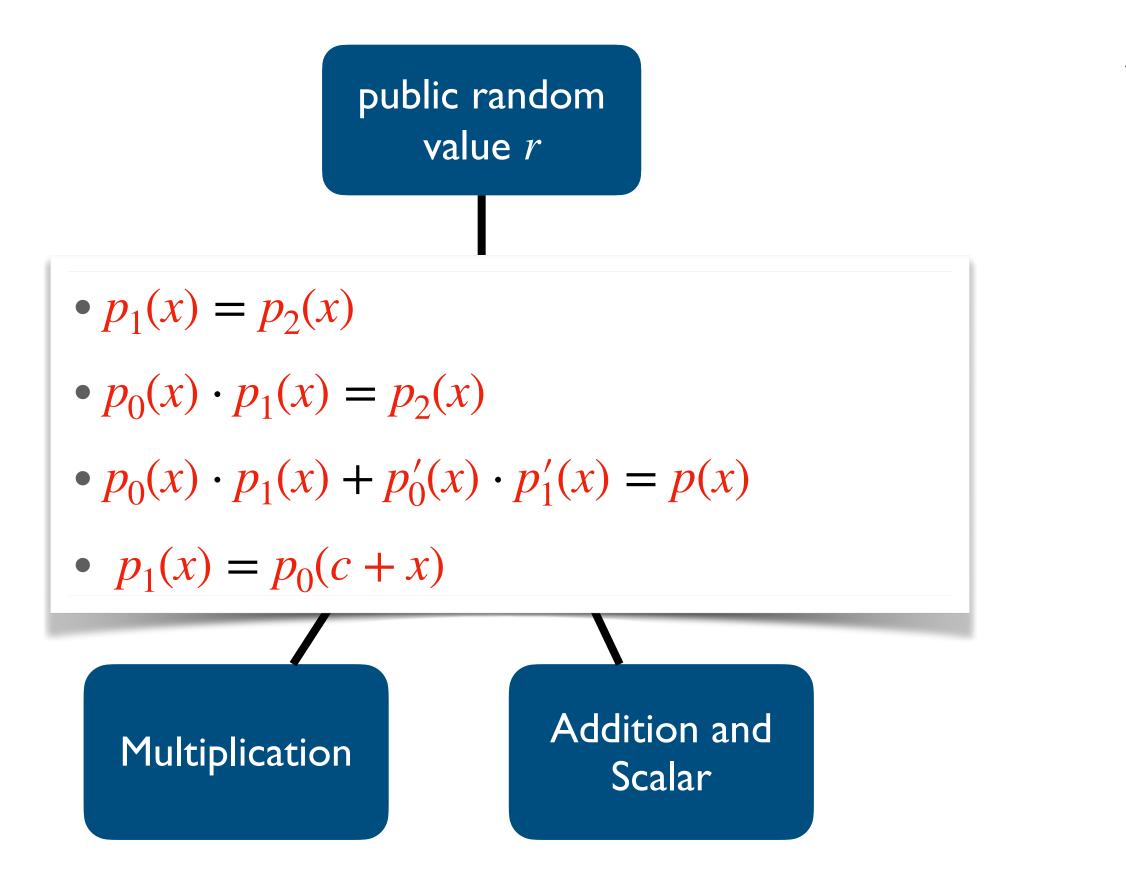


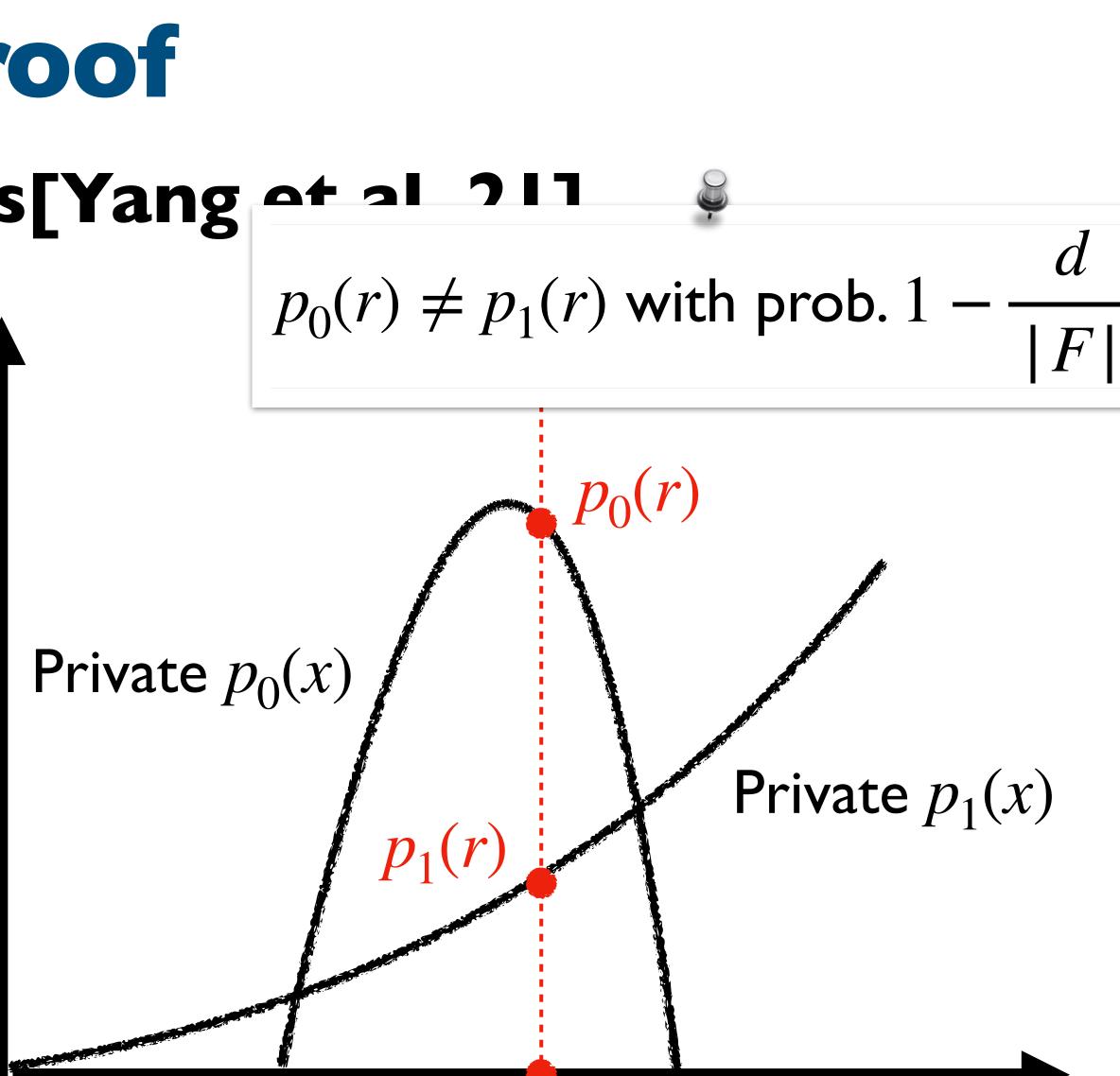








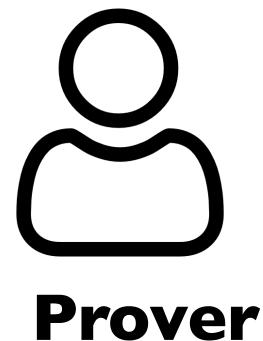




Zero Knowledge Proof: Take Away

Verifier can **verify** the relations between **private** values or polynomials without learning the values themselves

 p_a, p_b, p_c





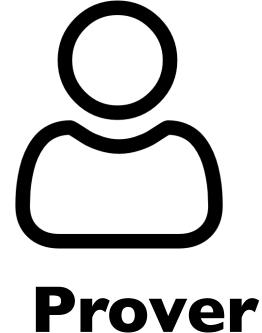
- \mathcal{C}



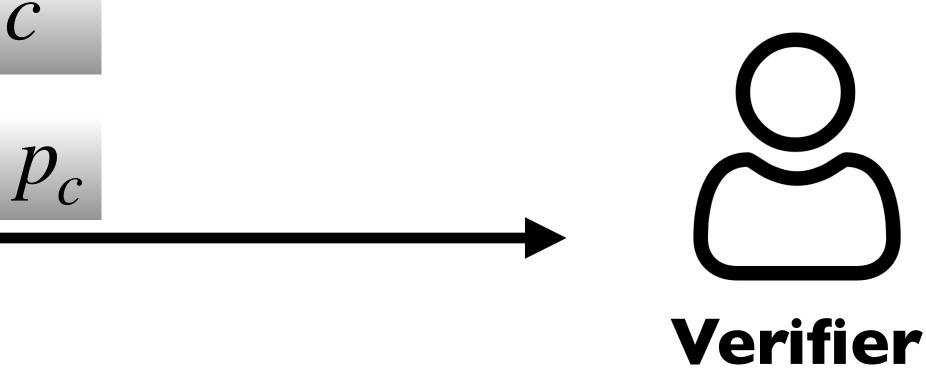
Zero Knowledge Proof: Take Away

Verifier can **verify** the relations between **private** values or polynomials without learning the values themselves

$$p_a, p_b,$$



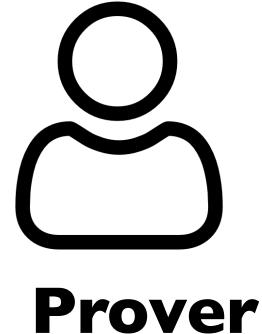




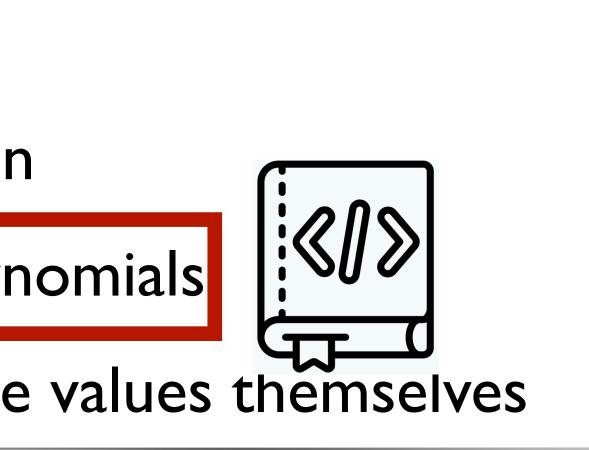
Zero Knowledge Proof: Take Away

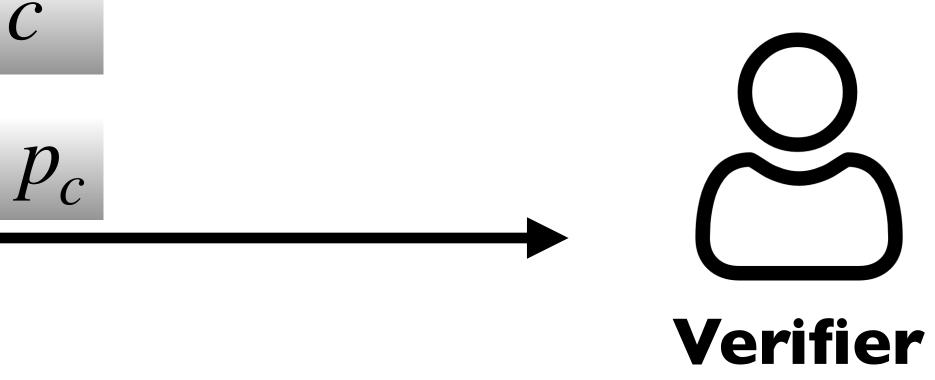
Verifier can **verify** the relations between **private** values or polynomials without learning the values themselves

$$p_a, p_b,$$





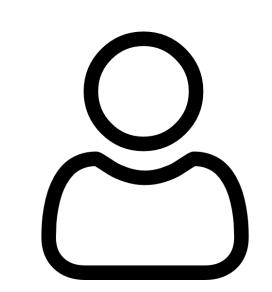




•Prover knows a resolution proof Prf for a formula ϕ

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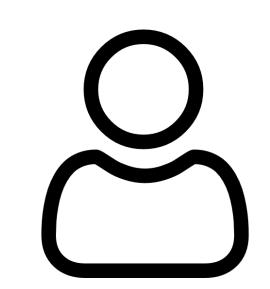
 $\phi = c_0 \wedge c_1 \wedge c_2 \wedge c_3$ **Resolution Proof**



- Prover knows a resolution proof Prf for a formula ϕ
 - Convince the verifier that ϕ is **unsatisfiable**

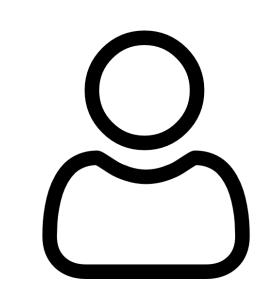
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Resolution Proof

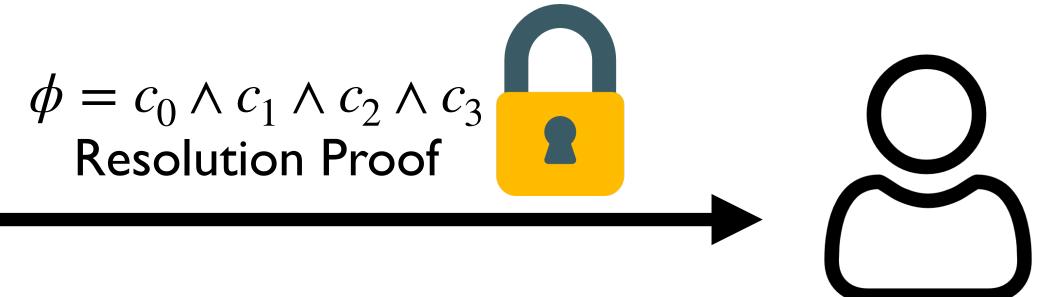


- Prover knows a resolution proof Prf for a formula ϕ
 - Convince the verifier that ϕ is **unsatisfiable**
 - •Keep information about Prf and ϕ private

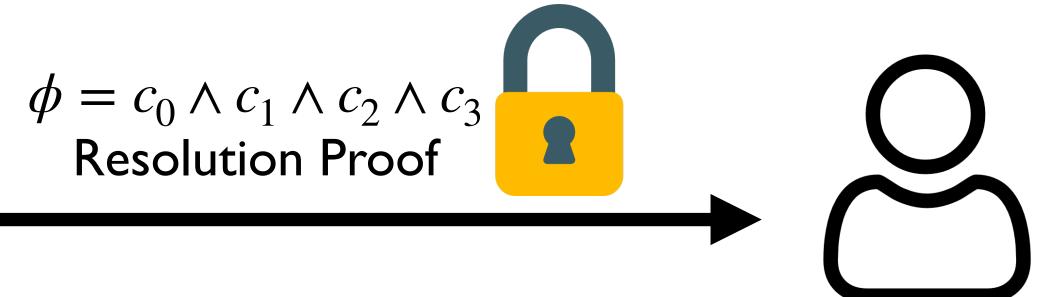
 $\phi = c_0 \wedge c_1 \wedge c_2 \wedge c_3$ **Resolution Proof**



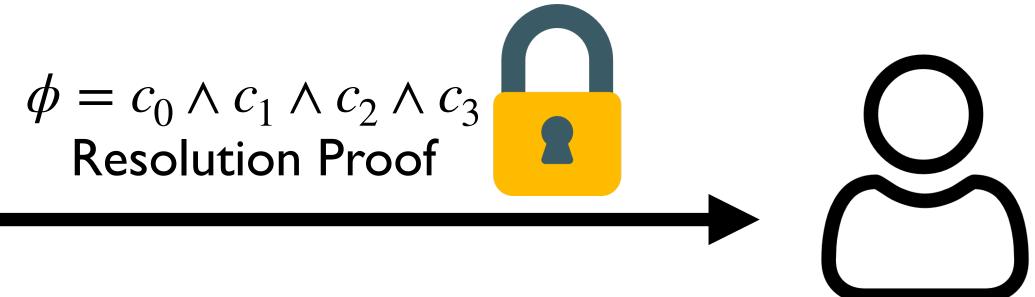
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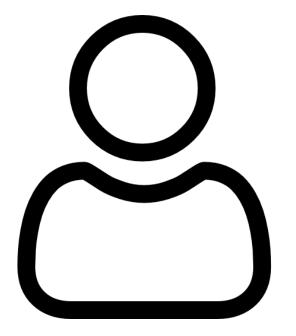


- Prover knows a resolution proof Prf for a formula ϕ
 - Convince the verifier that ϕ is **unsatisfiable**
 - •Keep information about Prf and ϕ private
- Verifier:

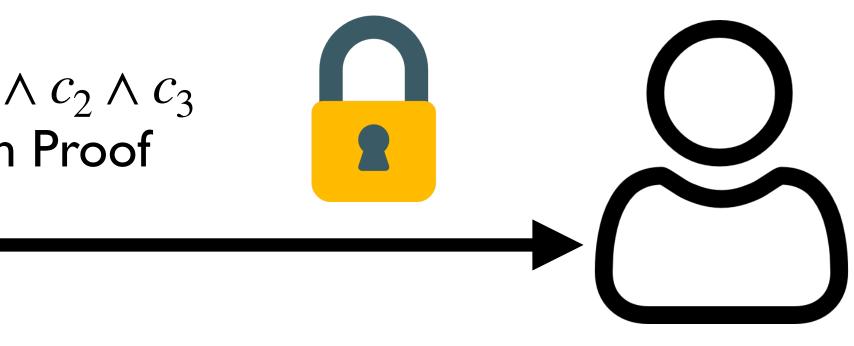


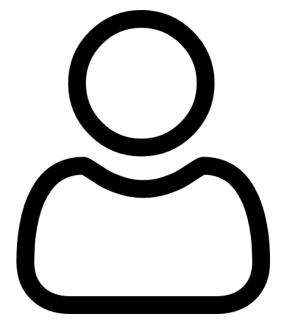
- Prover knows a resolution proof Prf for a formula ϕ
 - Convince the verifier that ϕ is **unsatisfiable**
 - •Keep information about Prf and ϕ private
- Verifier:
 - Validate prover's claim about ϕ





 $\phi = c_0 \wedge c_1 \wedge c_2 \wedge c_3$ Resolution Proof



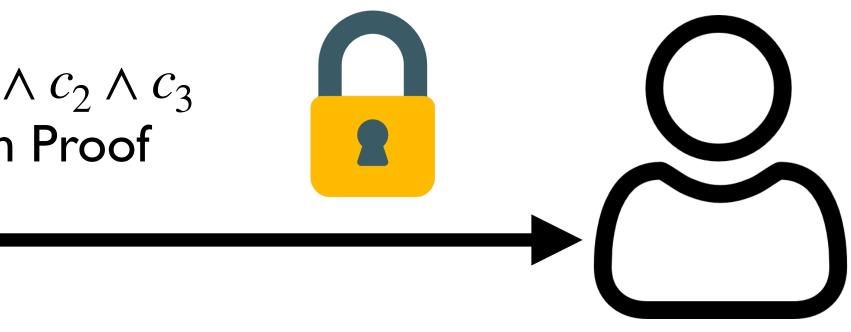


Both Prover and Verifier can be malicious.

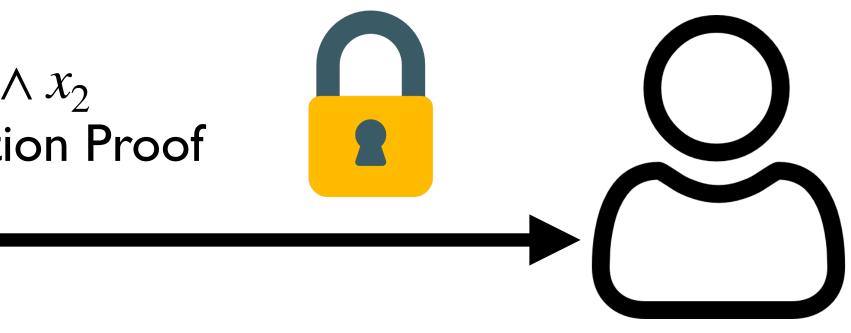




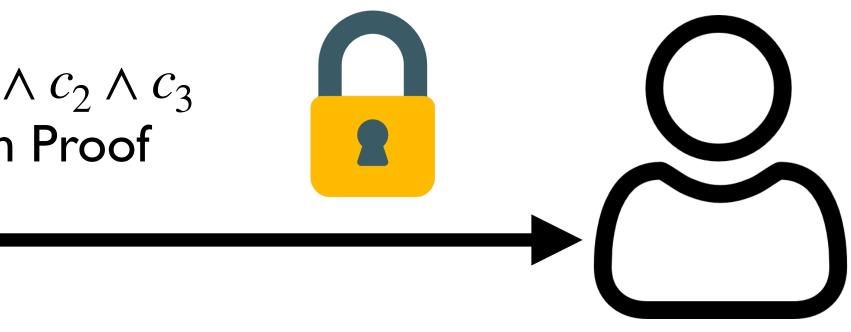
 $\phi = c_0 \wedge c_1 \wedge c_2 \wedge c_3$ Resolution Proof



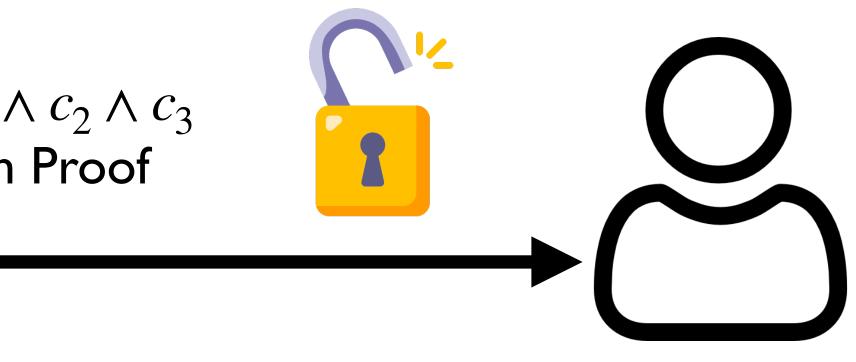
 $\phi = x_1 \wedge x_2$ Fake Resolution Proof



 $\phi = c_0 \wedge c_1 \wedge c_2 \wedge c_3$ Resolution Proof



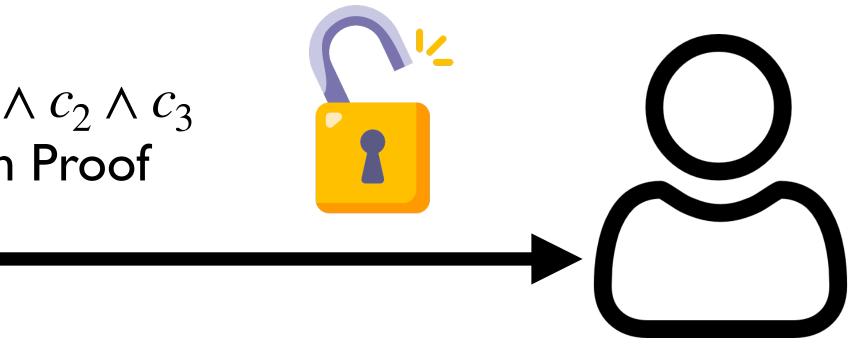
 $\phi = c_0 \wedge c_1 \wedge c_2 \wedge c_3$ Resolution Proof



 $\phi = c_0 \wedge c_1 \wedge c_2 \wedge c_3$ Resolution Proof

Both Prover and Verifier can be malicious.

- -Prover might cheat about unsatisfiability of ϕ
- $\bullet \text{Verifier tries to learn information about } Pr\!f$ and ϕ



Unsatisfiability in Zero Knowledge Proof Technique challenges and design overview

ZKUNSAT

$c_0: (x_1 \lor x_2)$	_, _
$c_1:(\neg x_1 \lor x_2)$	_, _
$c_2:(\neg x_1 \lor \neg x_2)$	_, _
$c_3:(x_1 \vee \neg x_2)$	_, _
$c_4 : x_2$	0, 1
c_5 : $\neg x_2$	2, 3
<i>c</i> ₆ : _	4, 5



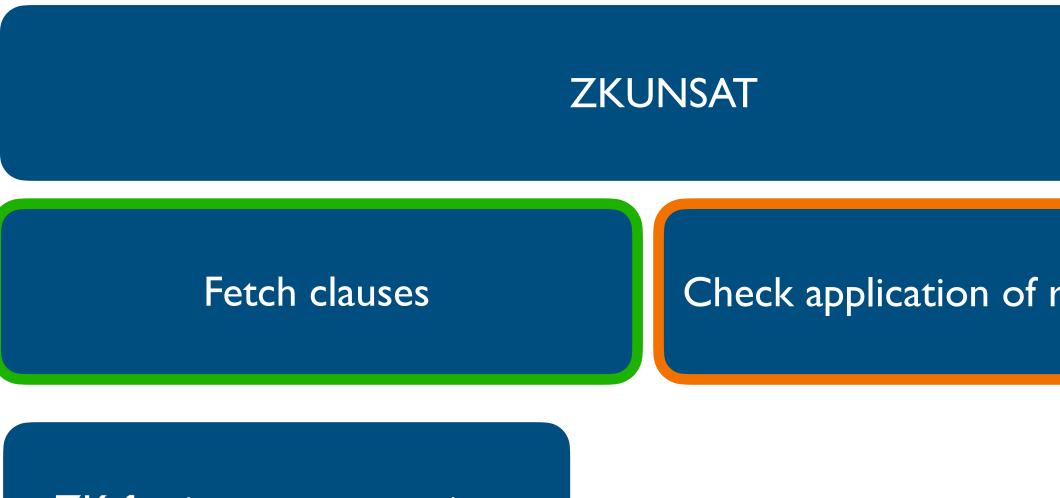
Fetch clauses

$c_0: (x_1 \lor x_2)$	_, _
$c_1:(\neg x_1 \lor x_2)$	_, _
$c_2:(\neg x_1 \lor \neg x_2)$	_, _
$c_3:(x_1 \vee \neg x_2)$	_, _
$c_4 : x_2$	0, 1
c_5 : $\neg x_2$	2, 3
<i>c</i> ₆ : _	4, 5



resolution rule	

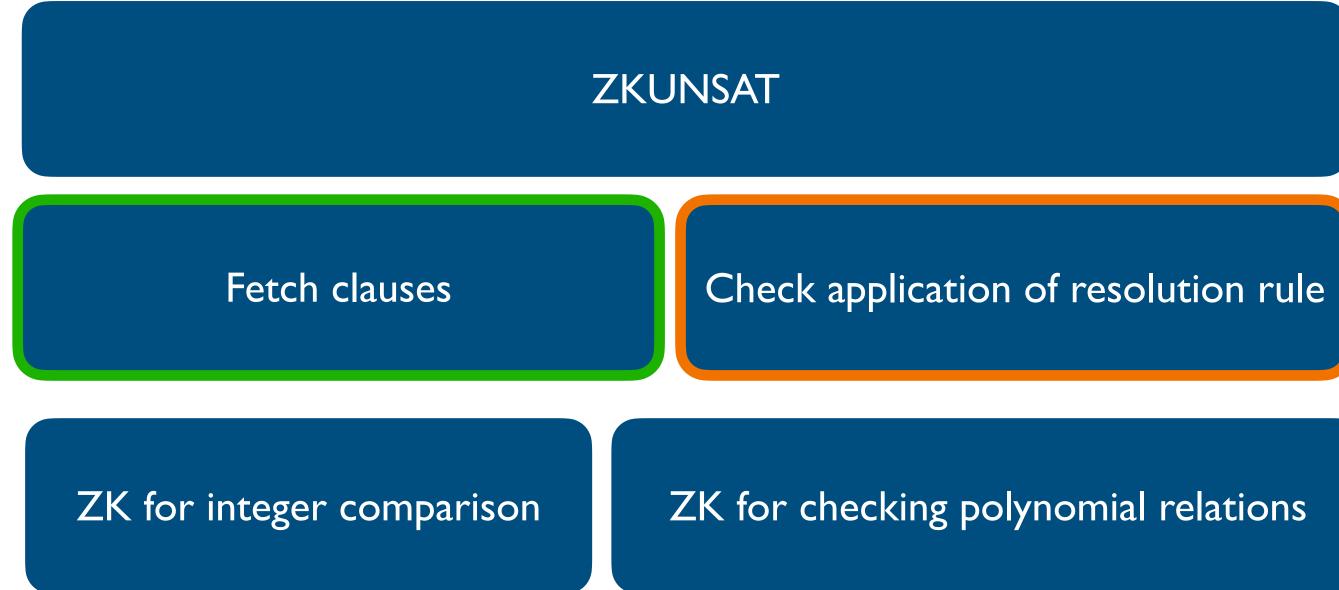
$c_0: (x_1 \lor x_2)$	_, _
$c_1:(\neg x_1 \lor x_2)$	_, _
$c_2:(\neg x_1 \lor \neg x_2)$	_, _
$c_3:(x_1 \vee \neg x_2)$	_, _
$c_4 : x_2$	0, 1
c_5 : $\neg x_2$	2, 3
c_6 : \bot	4, 5



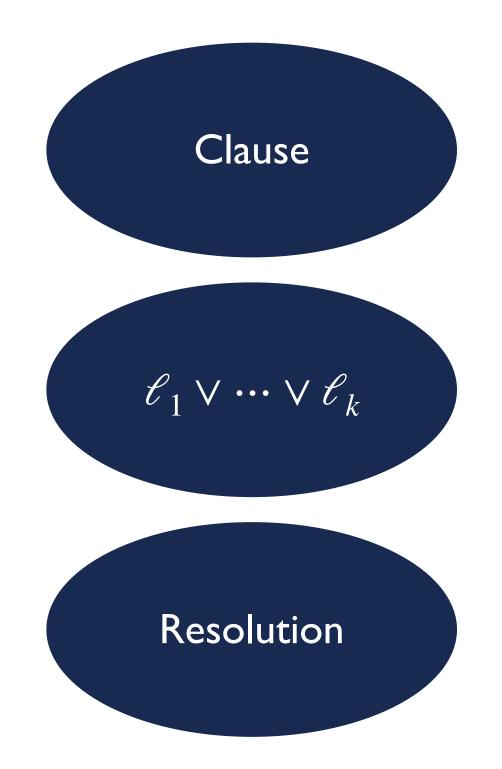
ZK for integer comparison

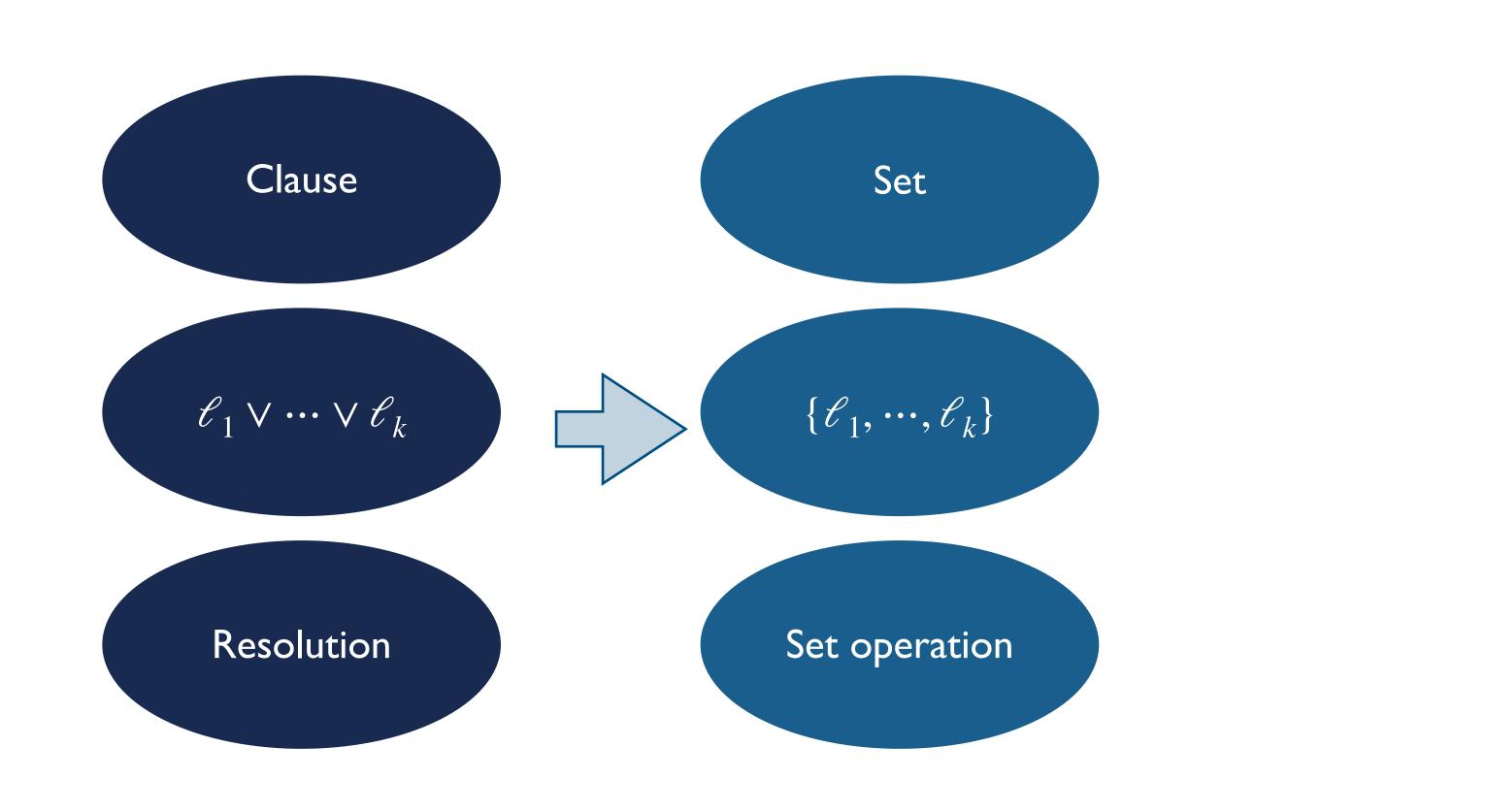
resolution rule	

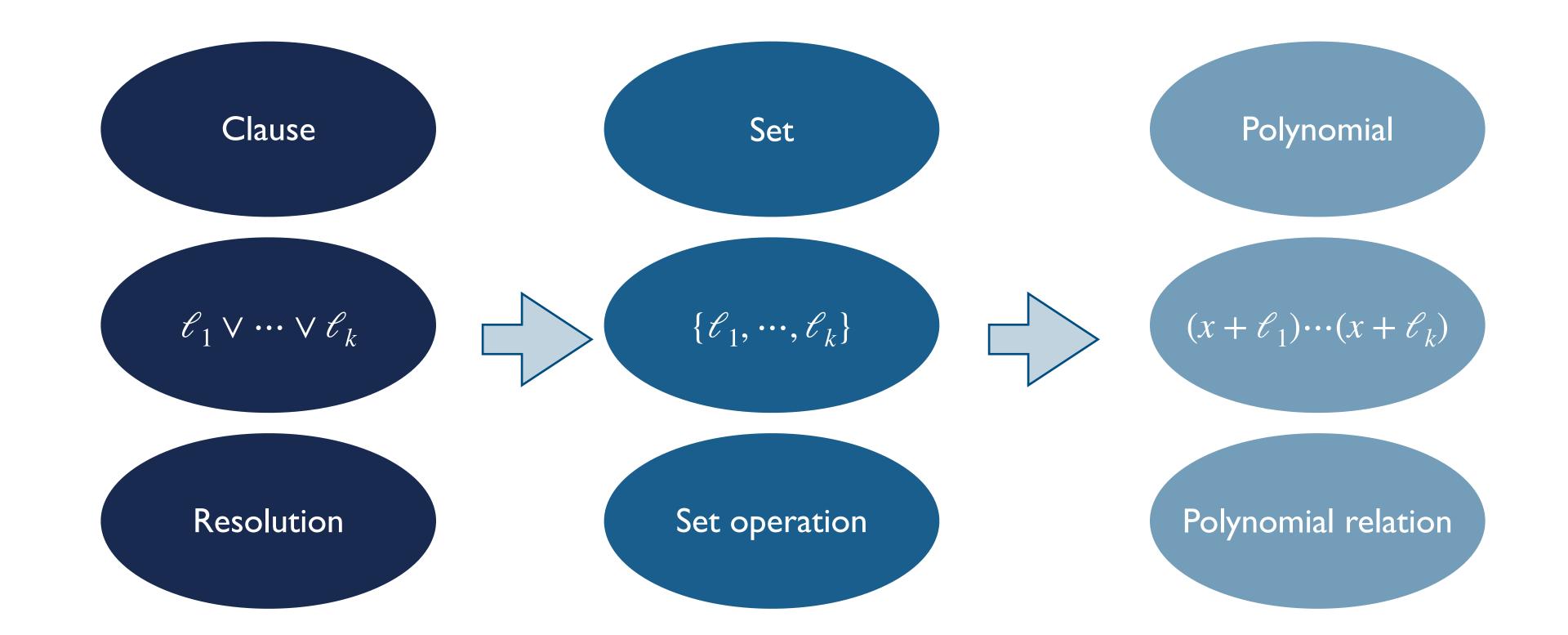
$c_0: (x_1 \lor x_2)$	_, _
$c_1:(\neg x_1 \lor x_2)$	_, _
$c_2:(\neg x_1 \lor \neg x_2)$	_, _
$c_3:(x_1 \vee \neg x_2)$	_, _
$c_4 : x_2$	0, 1
c_5 : $\neg x_2$	2, 3
c_6 : \bot	4, 5

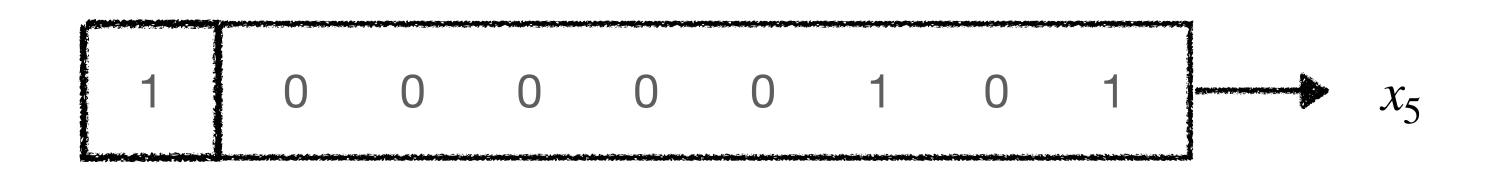


$c_0: (x_1 \lor x_2)$	
$c_1:(\neg x_1 \lor x_2)$,
$c_2:(\neg x_1 \lor \neg x_2)$,
$c_3:(x_1 \vee \neg x_2)$	_, _
$c_4 : x_2$	0, 1
$c_5: \neg x_2$	2, 3
<i>c</i> ₆ : _	4, 5





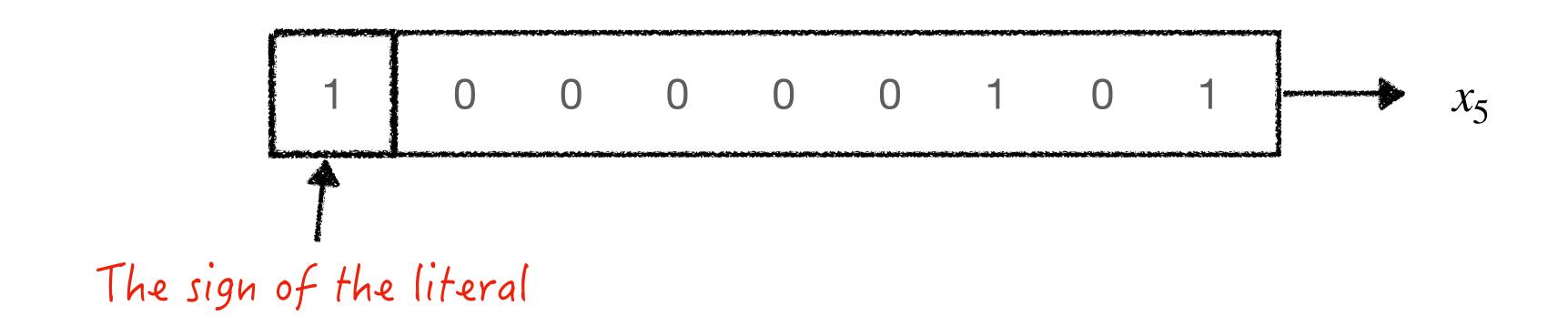




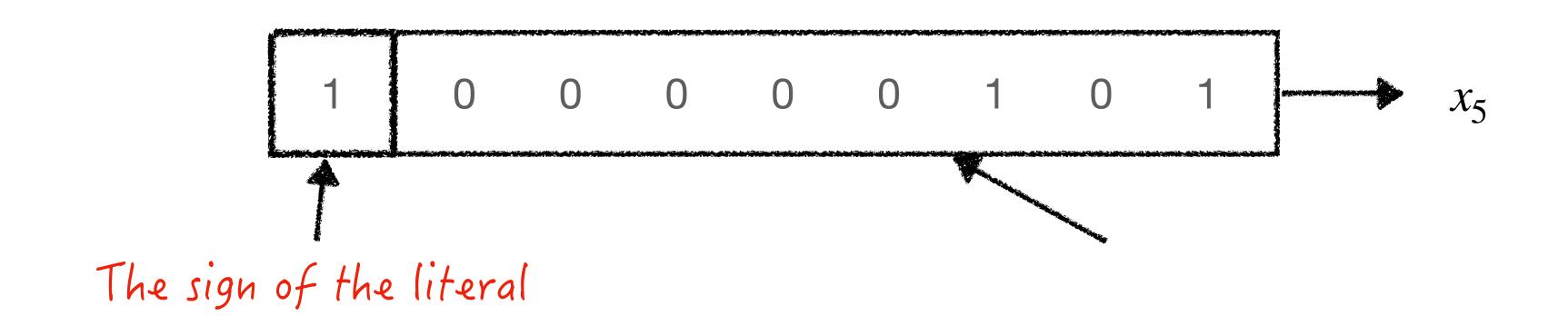
- Any Encoding scheme that satisfies
 - Injective
 - $\epsilon(\ell) + \epsilon(\neg \ell) = c$, c is a public constant



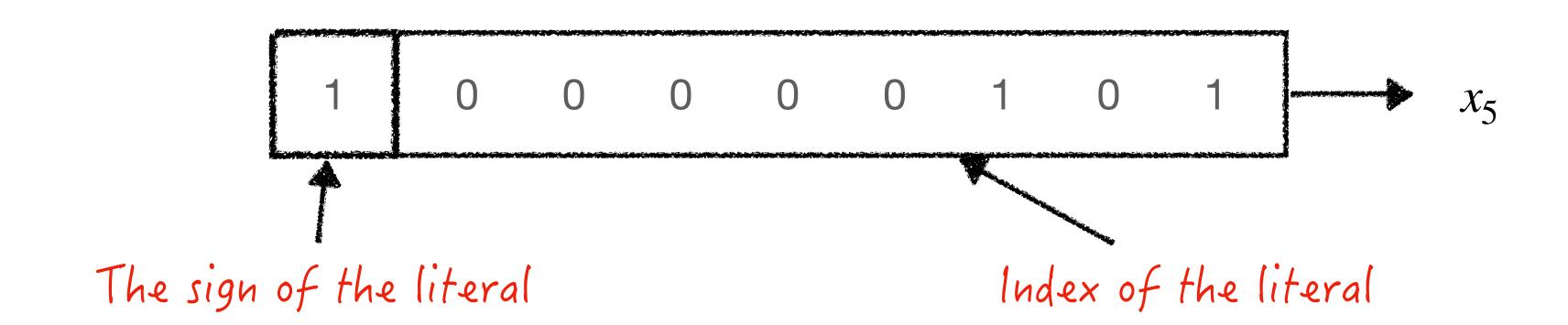
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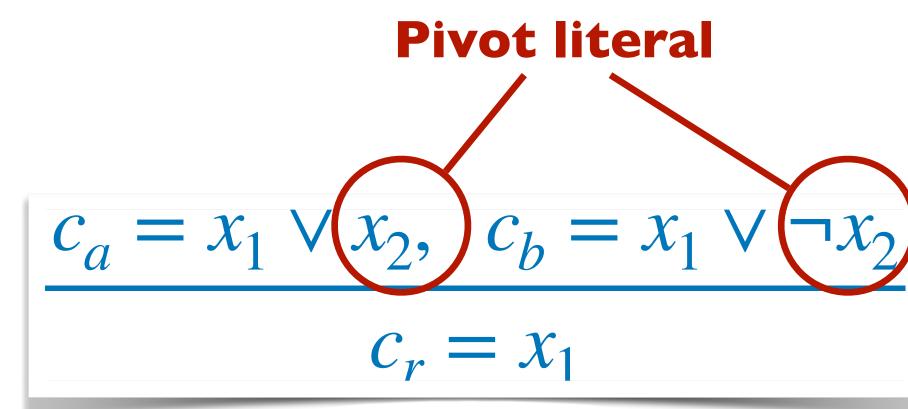
• Clauses are encoded as polynomial over \mathbf{F}_{2^k}

•
$$c = (\ell_0 \lor \ell_1 \lor \cdots \lor \ell_d) : p_c(x) =$$

Example

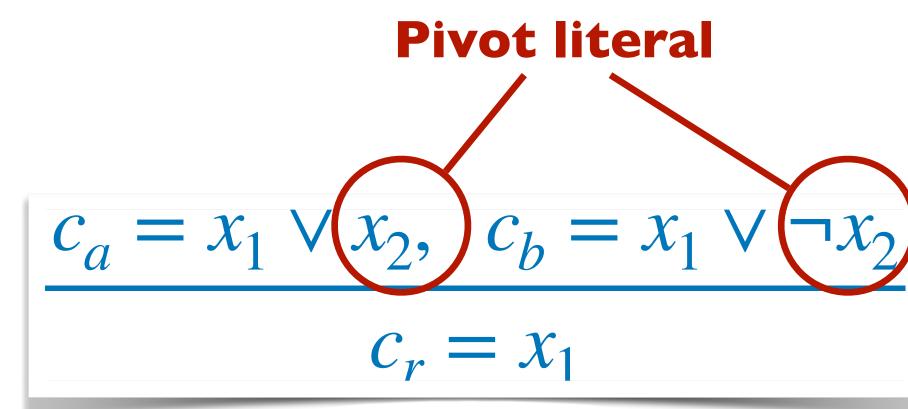
 $c_a = (x_1 \lor x_2) : p_{c_a}(x) = (x + 2^{k-1} + 1)(x + 2^{k-1} + 2)$ $c_b = (x_1 \lor \neg x_2) : p_{c_b}(x) = (x + 2^{k-1} + 1)(x + 2)$

 $(x + \epsilon(\ell_0)) \cdots (x + \epsilon(\ell_d))$





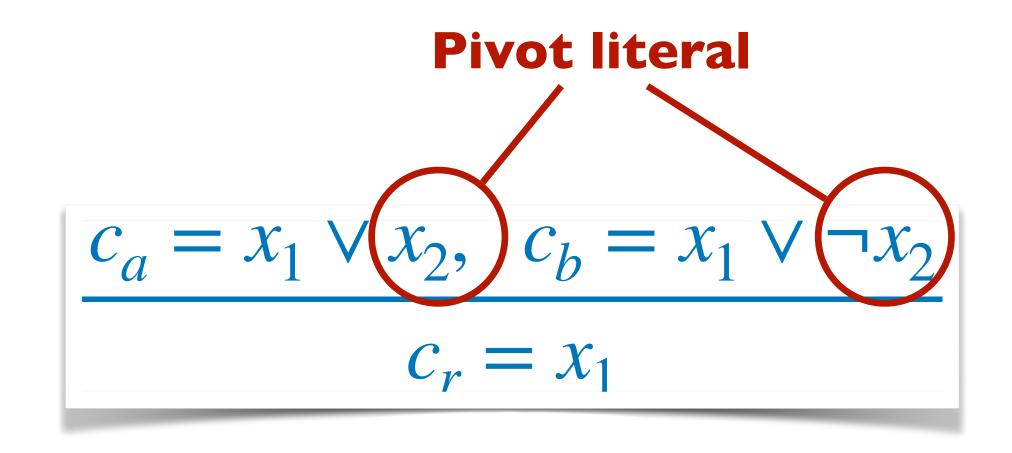
• Checking one resolution proof step:



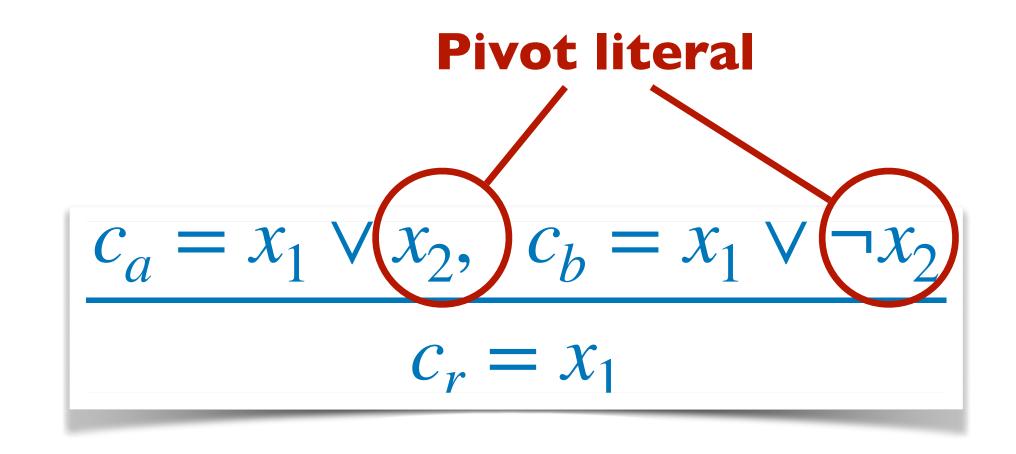


- Checking one resolution proof step: \bullet

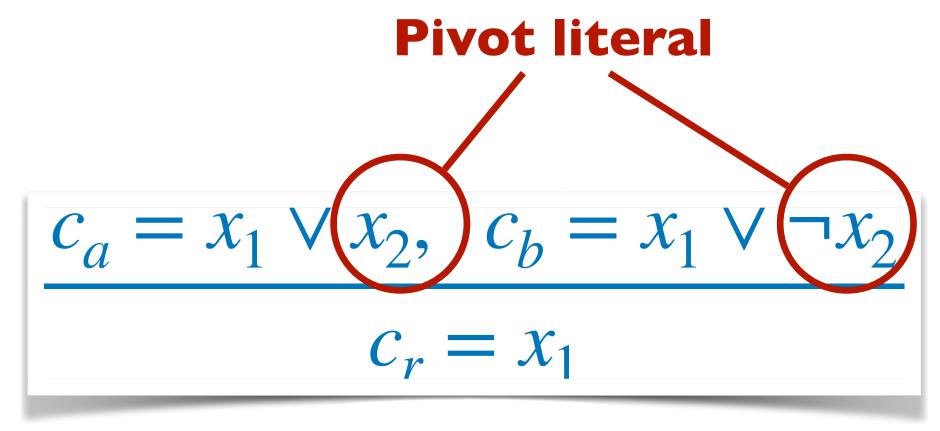
• Prover prepares and inputs a pair of pivot polynomials: $(x + \epsilon(\ell_p)), (x + \epsilon(\neg \ell_p))$



- Checking one resolution proof step:
 - Prover prepares and inputs a pair of pivot polynomials: $(x + \epsilon(\ell_p)), (x + \epsilon(\neg \ell_p))$ • Pivot polynomials: $(x + 2^{k-1} + 2)$ and (x + 2)

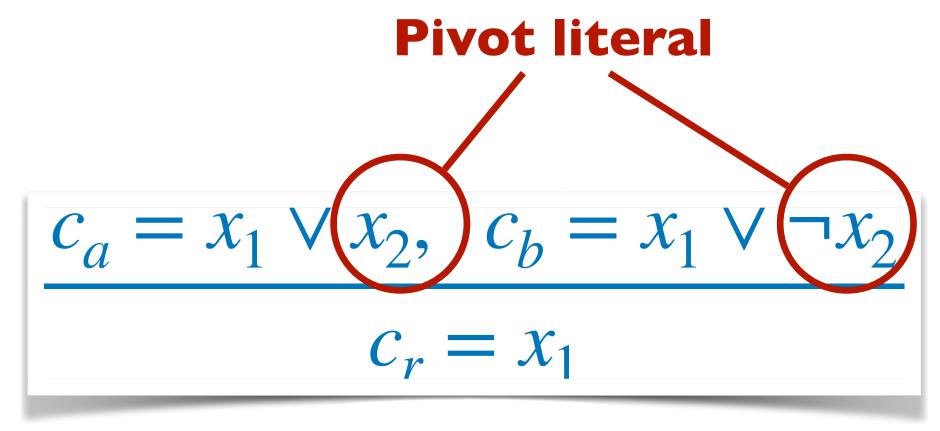


- Checking one resolution proof step:
 - Prover prepares and inputs a pair of pivot polynomials: $(x + \epsilon(\ell_p)), (x + \epsilon(\neg \ell_p))$ • Pivot polynomials: $(x + 2^{k-1} + 2)$ and (x + 2)
- - Prover prepares and inputs polynomial of the resolvent



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 - Prover prepares and inputs a pair of pivot polynomials: $(x + \epsilon(\ell_p)), (x + \epsilon(\neg \ell_p))$ • Pivot polynomials: $(x + 2^{k-1} + 2)$ and (x + 2)
- - Prover prepares and inputs polynomial of the resolvent

•
$$p_{c_r} = (x + 2^{k-1} + 1)$$



- $C_a \subseteq C_r \cup \{\ell_p\}, \ C_b \subseteq C_r \cup \{\neg \ell_p\}$
 - Proving $p_{c_a} | p_{c_r} \cdot (x + \epsilon(\ell_p))$ and $p_{c_b} |$
 - Prover prepares and inputs w_a and w_b
 - Verifier checks
 - $p_{c_a} | p_{c_r} \cdot (x + \epsilon(\ell_p))$ via $p_{c_a} \cdot w$
 - $p_{c_h} | p_{c_r} \cdot (x + \epsilon(\neg \ell_p)) \text{ via } p_{c_h} \cdot$

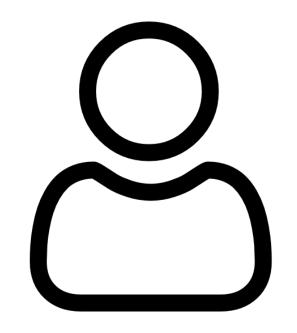
$$c_a = x_1 \lor x_2, \quad c_b = x_1 \lor$$
$$c_r = x_1$$

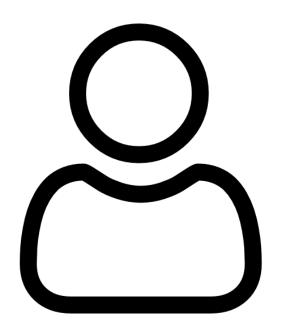
$$p_{c_r} \cdot (x + \epsilon(\neg \ell_p))$$

$$w_a = p_{c_r} \cdot (x + \epsilon(\ell_p))$$
$$w_b = p_{c_r} \cdot (x + \epsilon(\neg \ell_p))$$

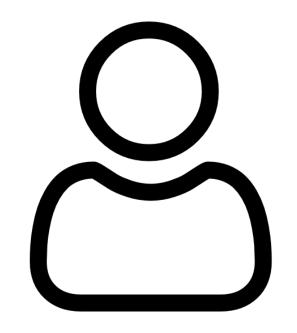


Check application of resolution rule





Check application of resolution rule



 $c_a = x_1 \lor x_2, \ c_b = x_1 \lor \neg x_2$ $c_r = x_1$



Check application of resolution rule

$$p_{c_a} = (x + 2^{k-1} + 1)(x + 2^{k-1} + 2)$$

$$p_{c_b} = (x + 2^{k-1} + 1)(x + 2)$$

$$p_{c_r} = (x + 2^{k-1} + 1)$$
pivot literals: $p_{x_2} = (x + 2^{k-1} + 2), p_{\neg x_2} = (x + 2)$

 $c_a = x_1 \lor x_2, \ c_b = x_1 \lor \neg x_2$ $C_r = X_1$

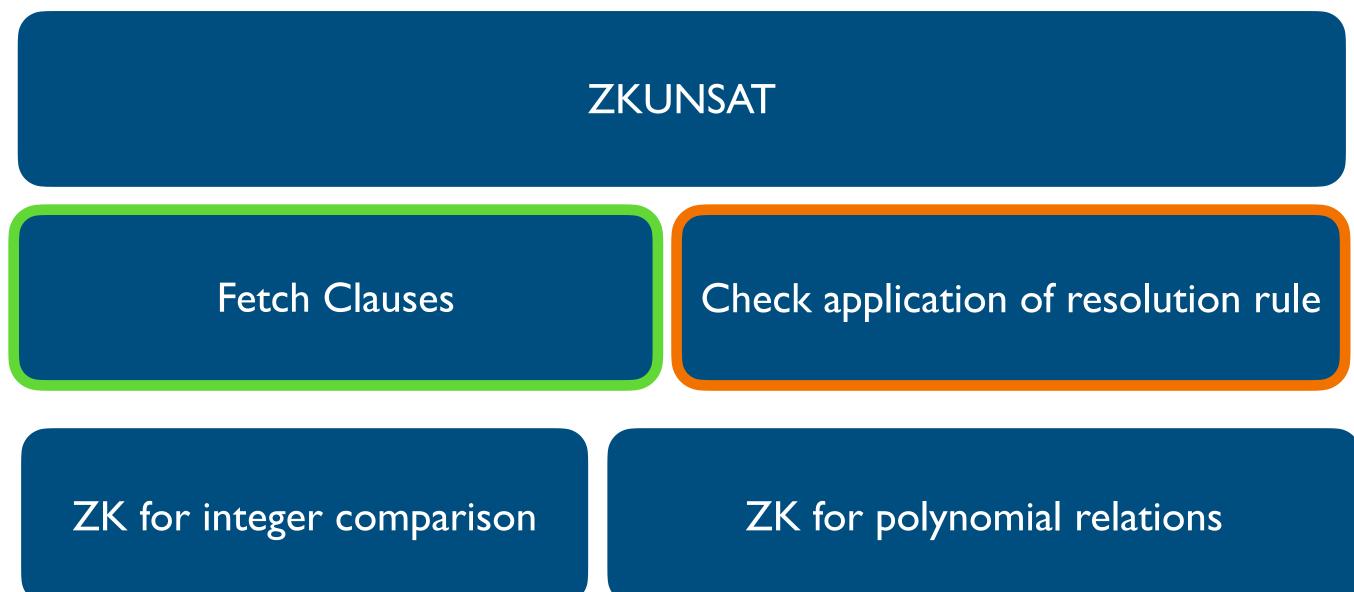


Check application of resolution rule $\begin{array}{ccc} C_a & C_r & \text{pivot} \\ \uparrow & \uparrow & \uparrow \\ p_{c_a} | p_{c_r} \cdot p_{x_2} : \{x_1, x_2\} \subseteq \{x_1\} \cup \{x_2\} \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x_1, \neg x_2\} \subseteq \{x_1\} \cup \{\neg x_2\} \end{array}$

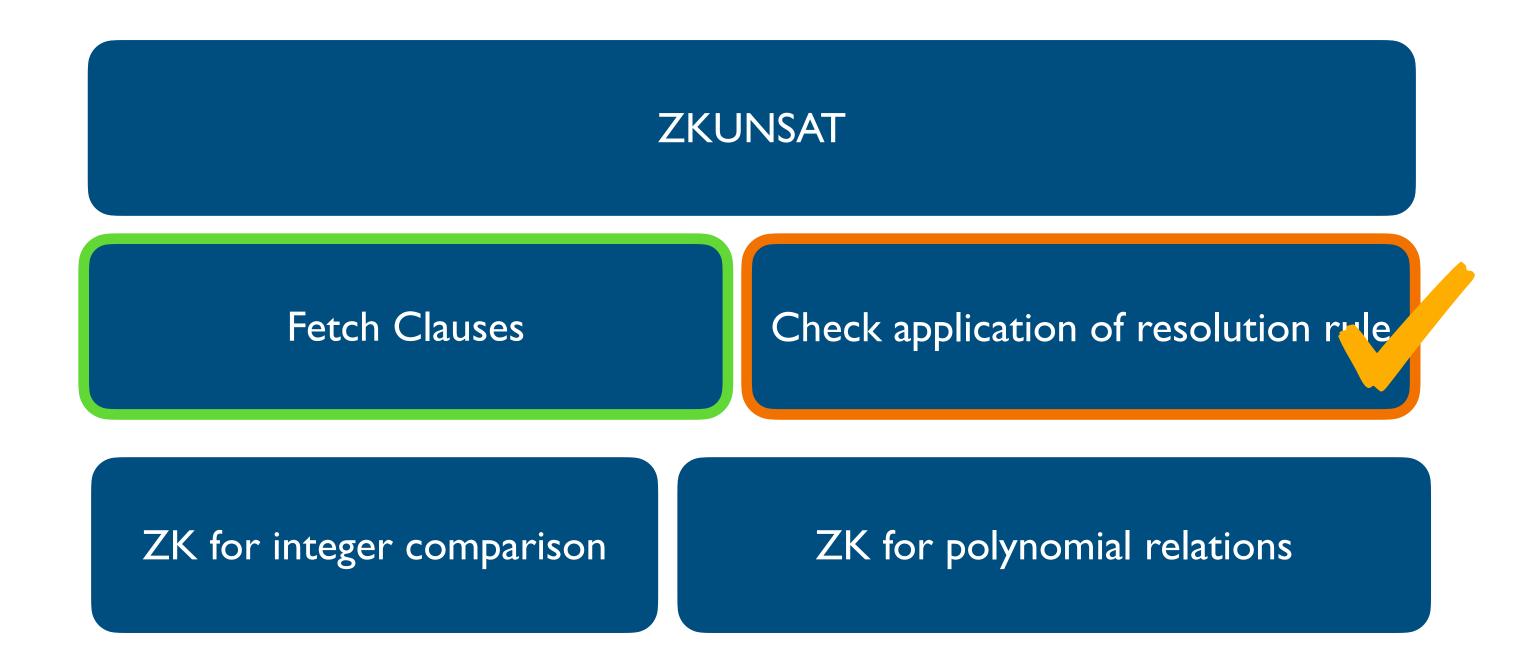
$$p_{c_a} | p_{c_r} \cdot p_{x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p_{c_r} \cdot p_{\neg x_2} : \{x \\ p_{c_b} | p$$

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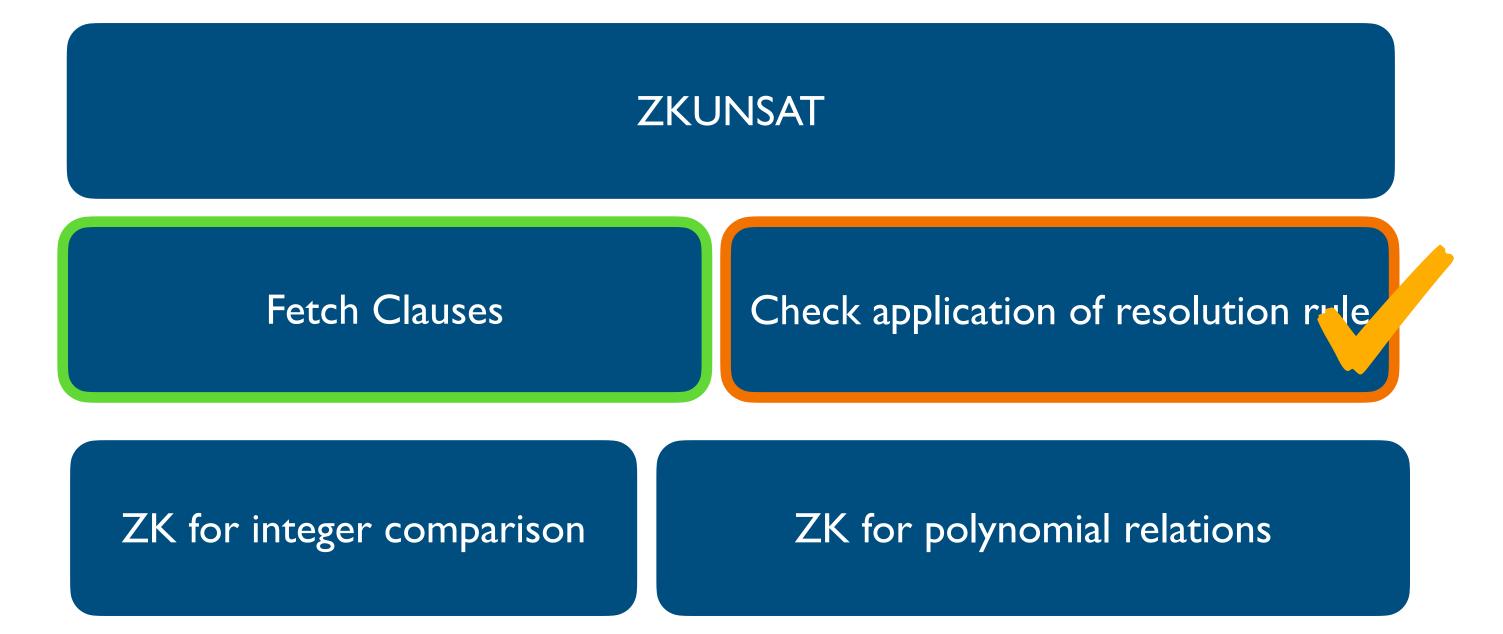
$c_0: (x_1 \lor x_2)$,
$c_1:(\neg x_1 \lor x_2)$	_,
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$c_4 : x_2$	0, 1
c_5 : $\neg x_2$	2, 3
<i>c</i> ₆ : ⊥	4, 5



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Given clauses c_a, c_b, c_r , we now can check $c_a, c_b \vdash_{res} c_r$

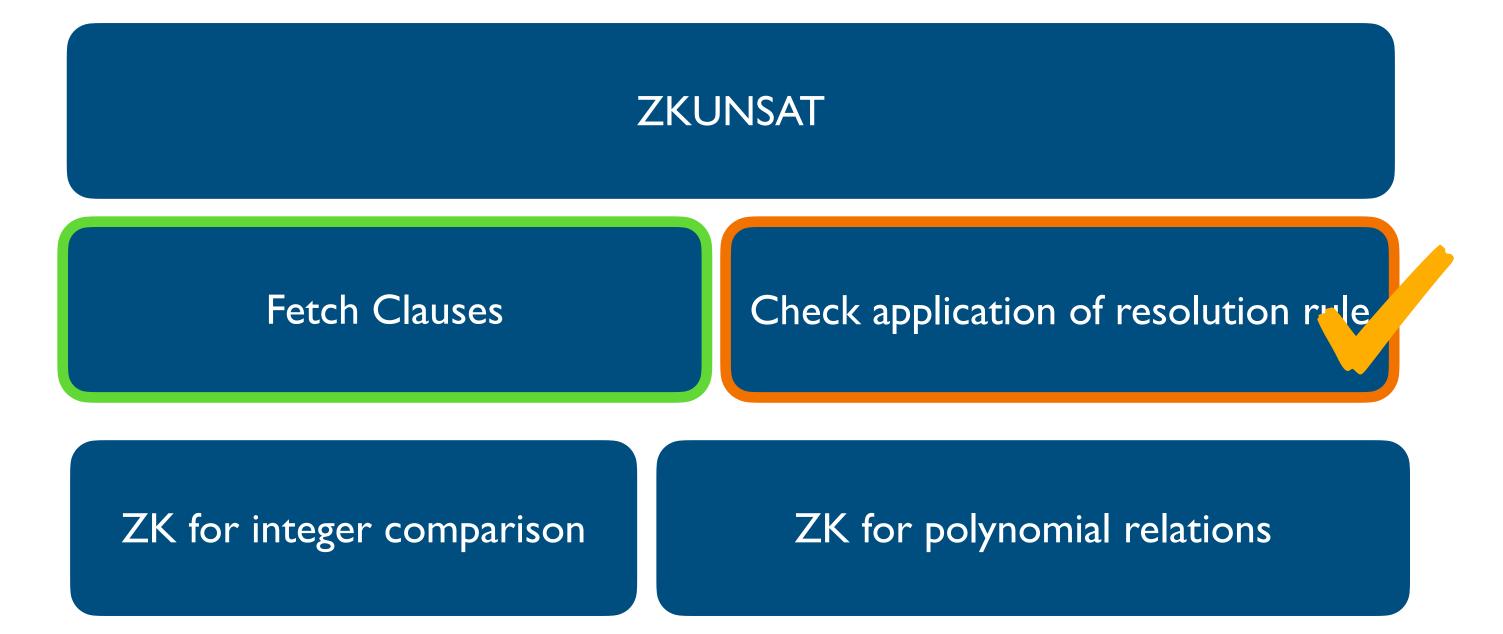
But how we fetch c_a and c_b without revealing the access?



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$c_1:(\neg x_1 \lor x_2)$,
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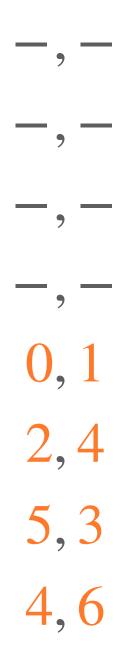
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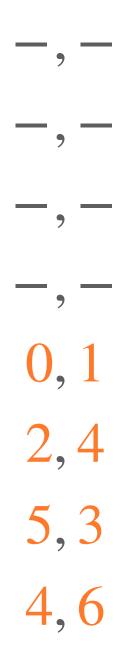
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Zero Knowledge Proof Read-Only Array in ZK (example)

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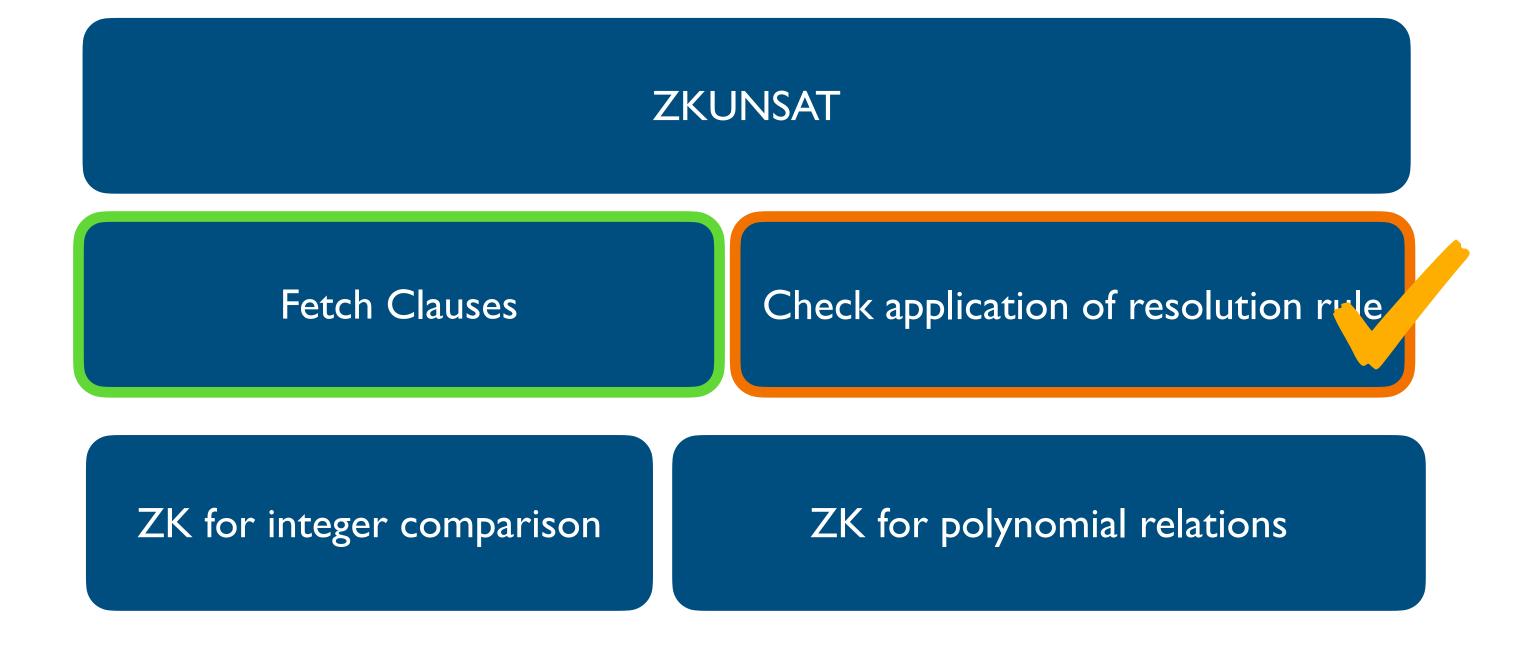
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 - L = L' in the sense of set (using polynomial equivalence)
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$$gets \\ (c_4), (4, c_4), (5, c_5), (6, c_6)]$$

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Unsatisfiability in Zero Knowledge Proof

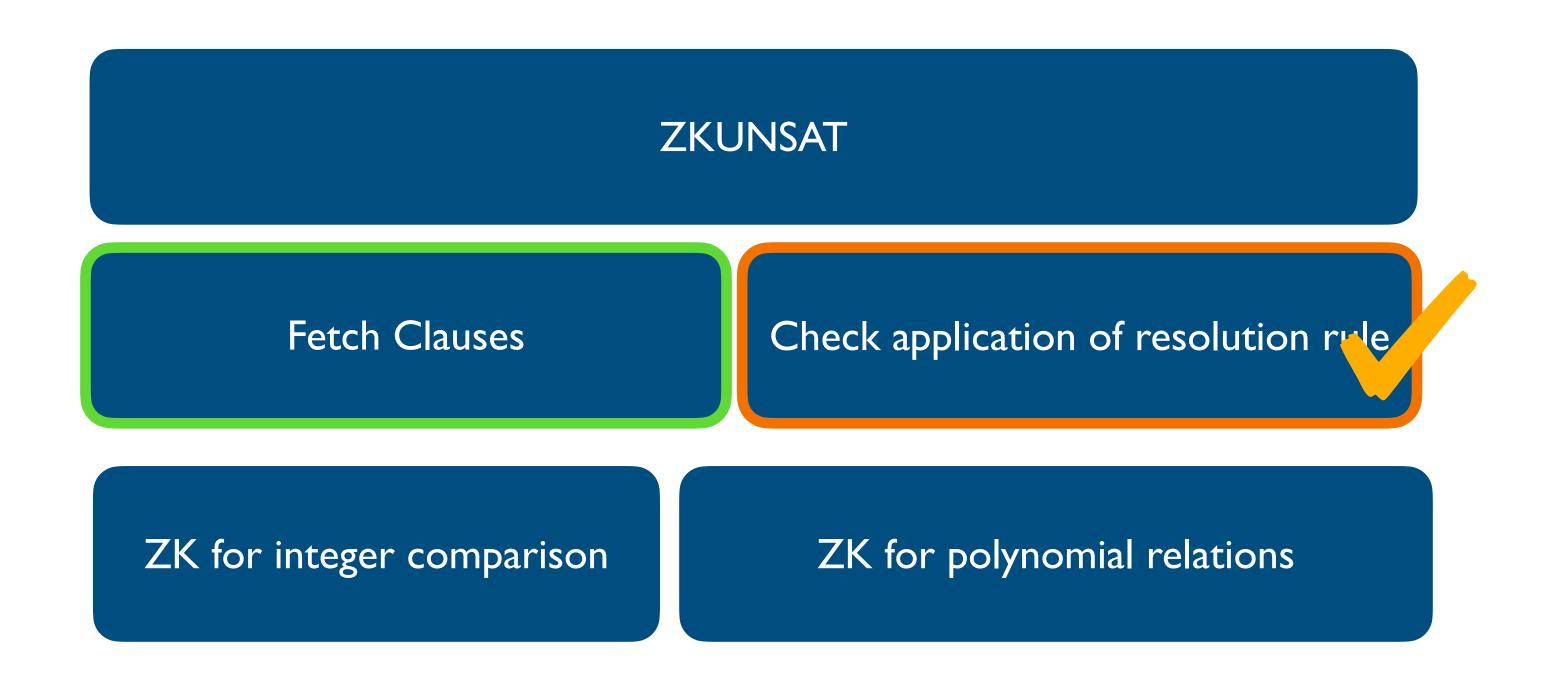


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Unsatisfiability in Zero Knowledge Proof

Put them together:

- •Fetch input clauses for each resolution using ROARRAY in ZK •Check application of the resolution rule using polynomial relations in ZK



$$c_{0} : (x_{1} \lor x_{2}) \qquad -, -$$

$$c_{1} : (\neg x_{1} \lor x_{2}) \qquad -, -$$

$$c_{2} : (\neg x_{1} \lor \neg x_{2}) \qquad -, -$$

$$c_{3} : (x_{1} \lor \neg x_{2}) \qquad -, -$$

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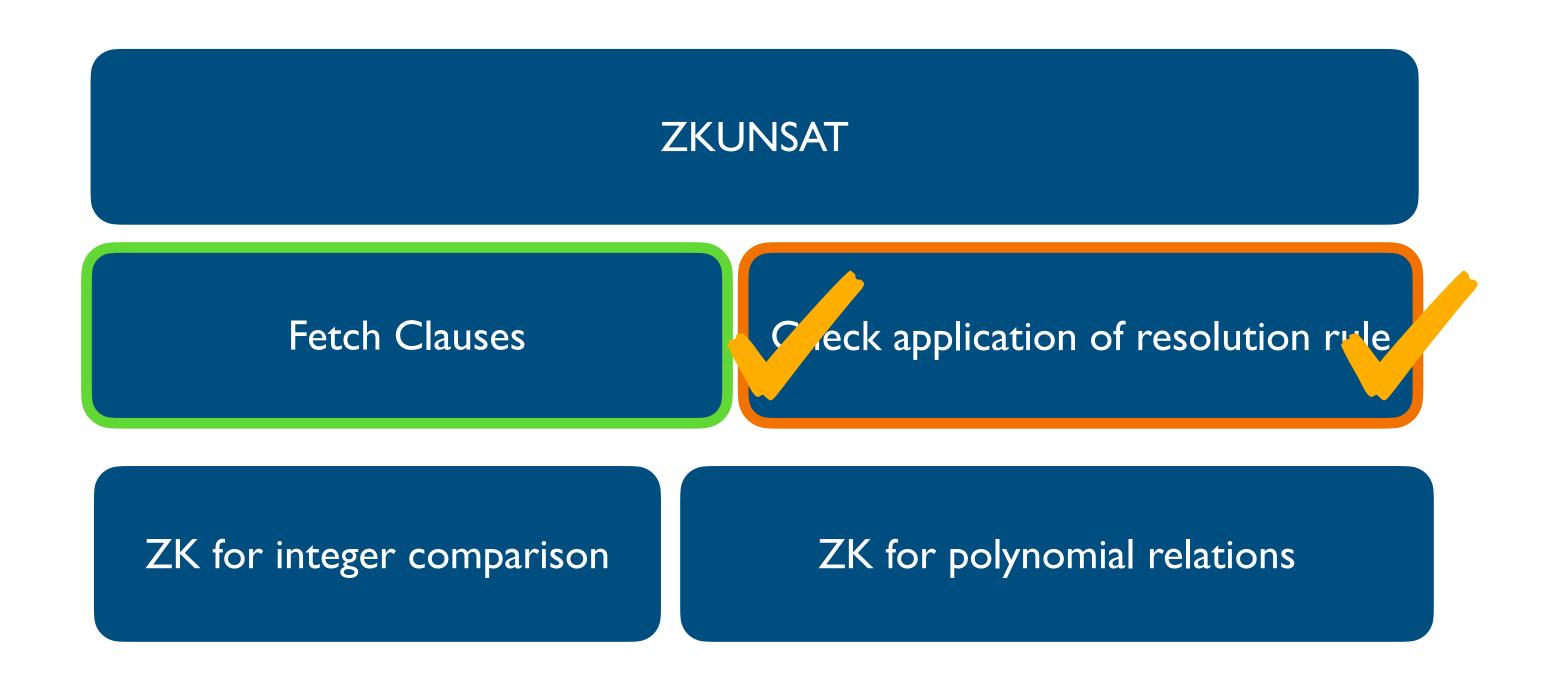
$$c_{5} : \neg x_{2} \qquad 2, 3$$

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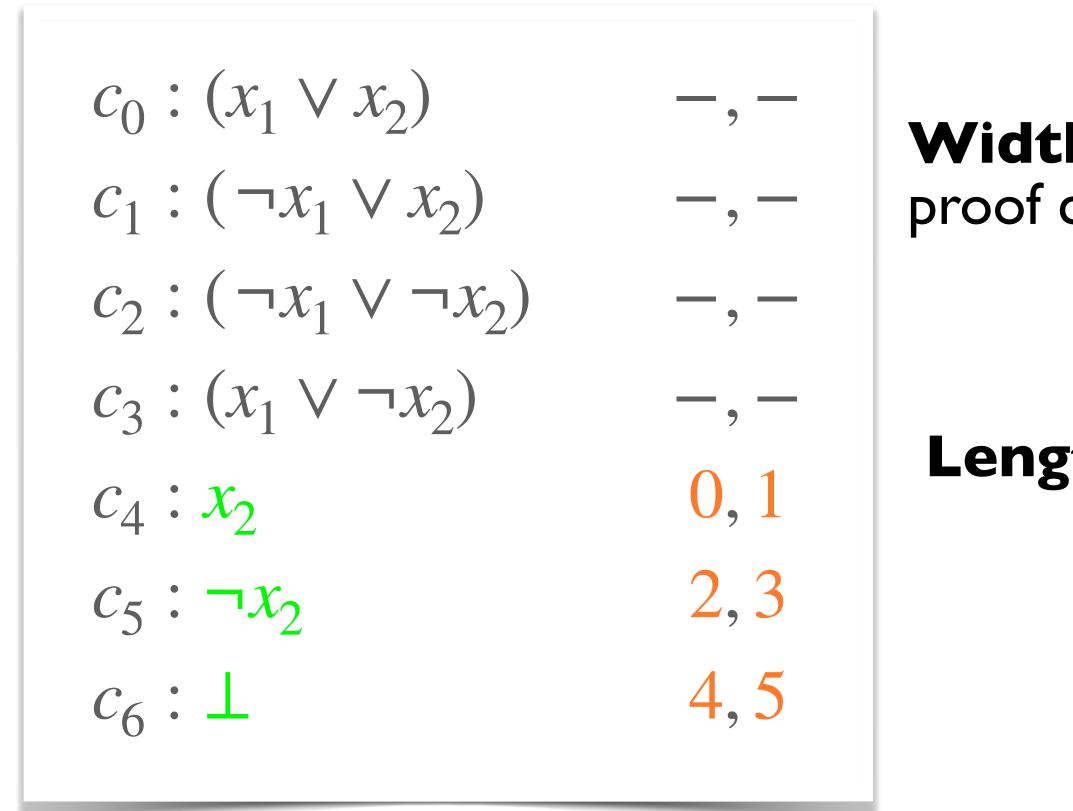
$$c_{6} : \bot \qquad 4, 5$$

Evaluation Benchmark setting



• 64 GB of memory, 16 vCPUs

10 Gbps network connection between the prover and the verifier



Width: the maximum number of literals a clause in the proof can have.

<u>Width = 2 in this example</u>

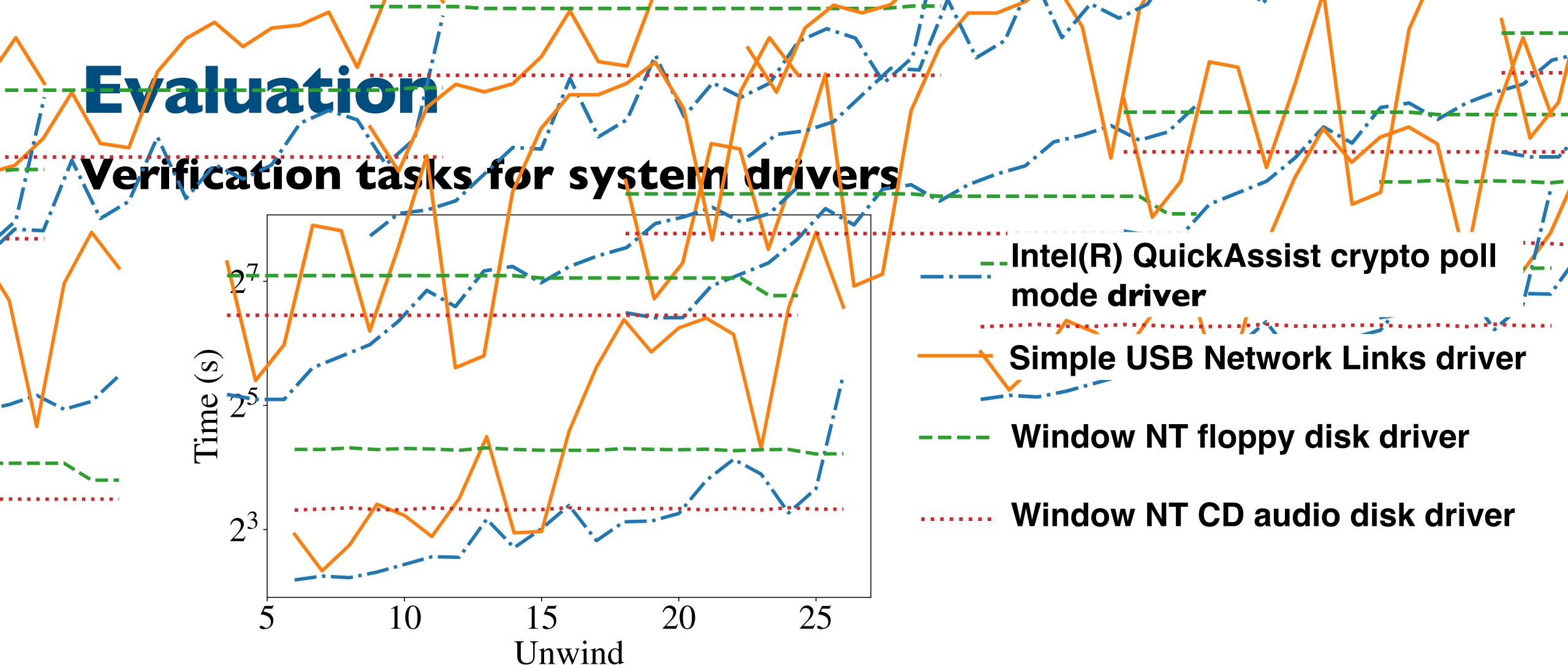
Length: the number of applications of resolution rule.

<u>Length = 3 in this example</u>

• Time and memory requirements depend on the length and clausal width of the proof

• ZKUNSAT takes less than 1 min to verify proofs of large width (400) and length (8000)





- Unwind is a parameter for translating the verification tasks to Boolean formulae
- Width ≤ 256 and Length $\leq 65K$

• ZKUNSAT can verify UNSAT of formulae from system drivers verification tasks within 5min

Other large instances

Program	Len. (K)	Width	Time (s)
inv-square-int	194	414	172.5
rlim-invariant	481	198	1943.3
sin-interpolated-smallrange	375	308	2571.8
interpolation	135	790	3771.6
inv-sqrt-quake	182	749	5764.1
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Further improvement via computing clusters: work to appear in CCS 2023

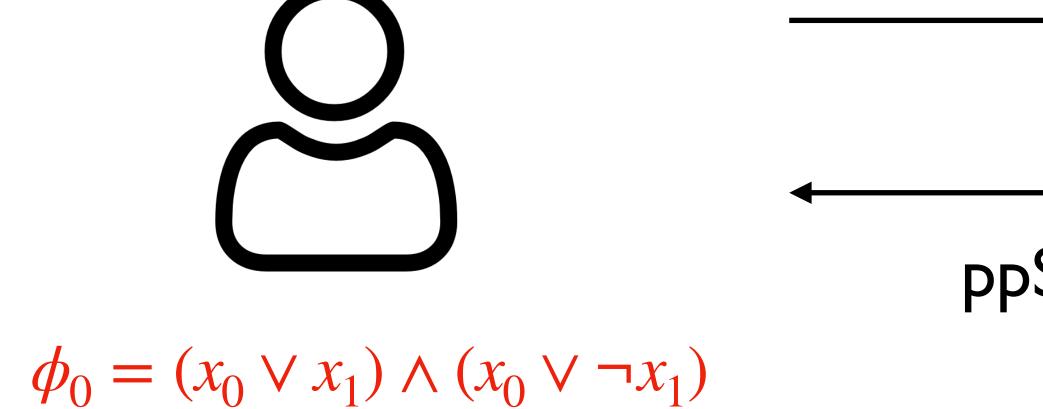
Contribution

- Privacy preserving program verification is in demand
- Encoding resolution proof by polynomials
- UNSAT in ZK is practical

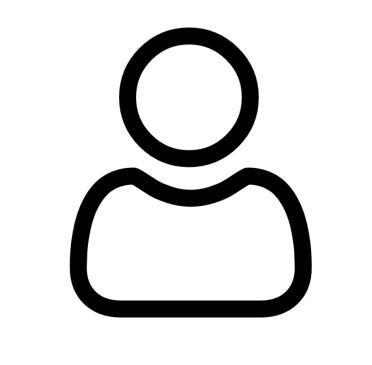
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• SAT : Privacy-preserving SAT solving (ppSAT)

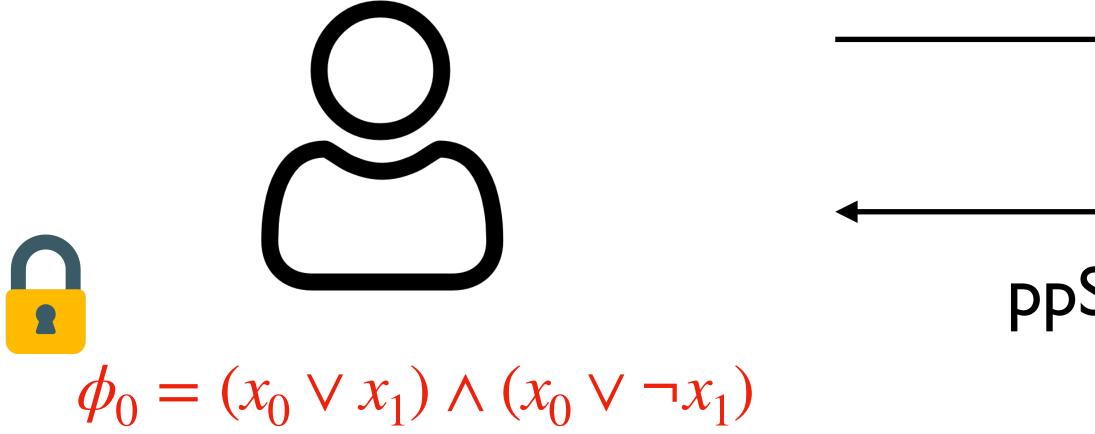


ppSAT

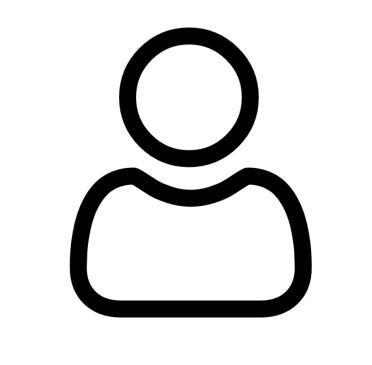


 $\phi_1 = (\neg x_0 \lor x_1) \land (\neg x_0 \lor \neg x_1)$

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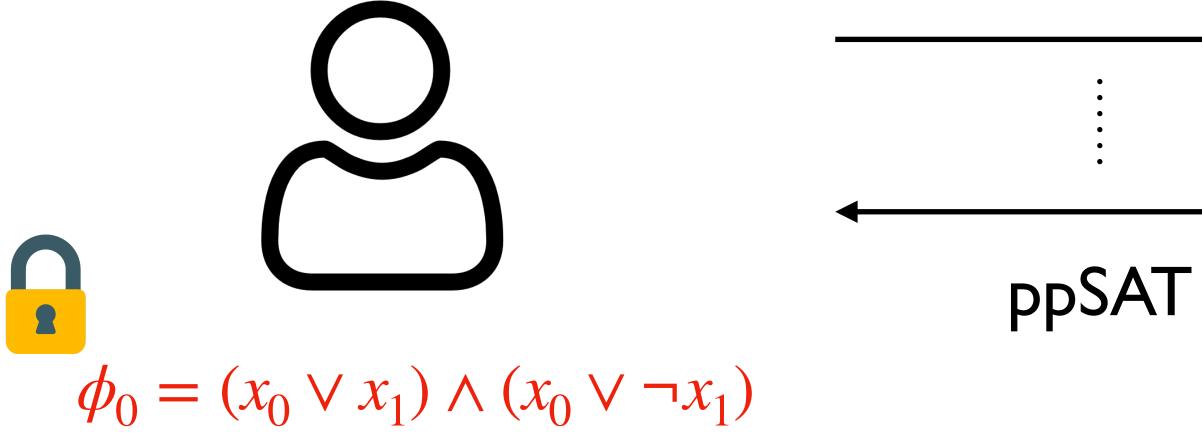


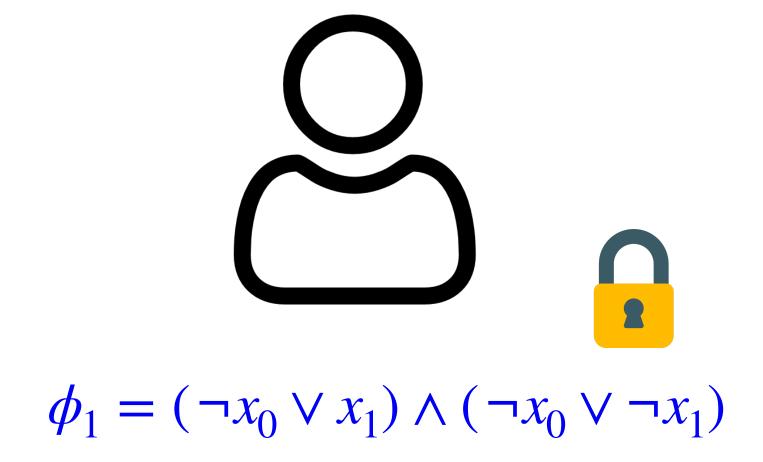
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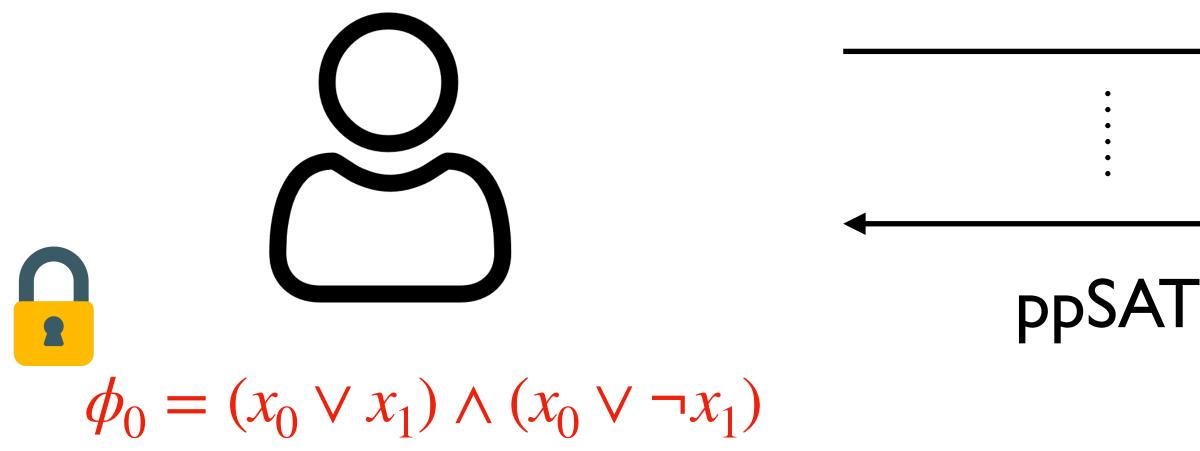
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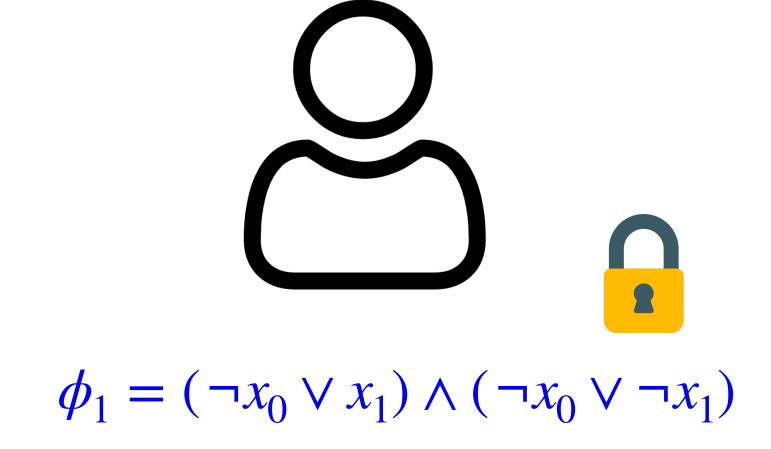




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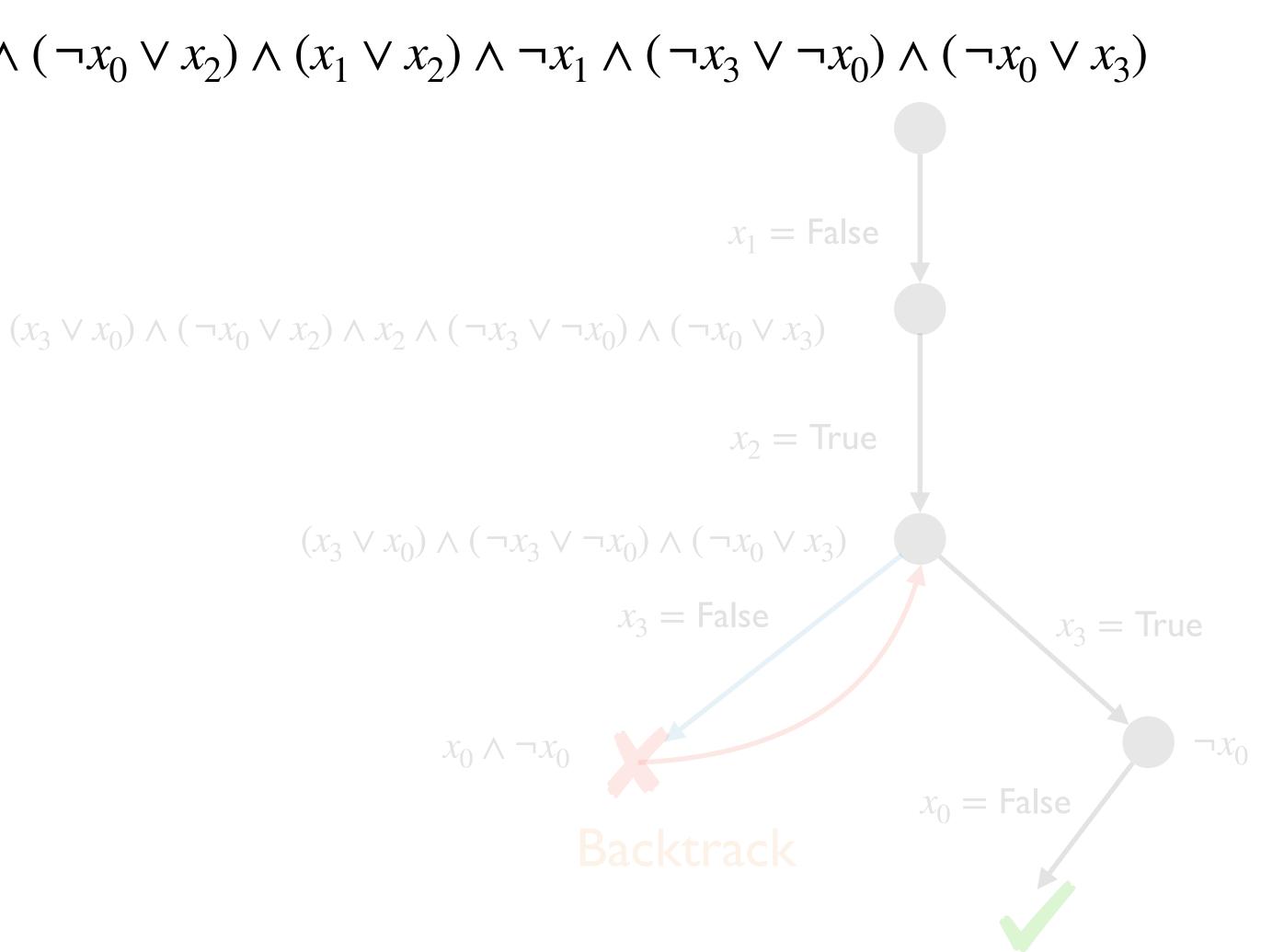




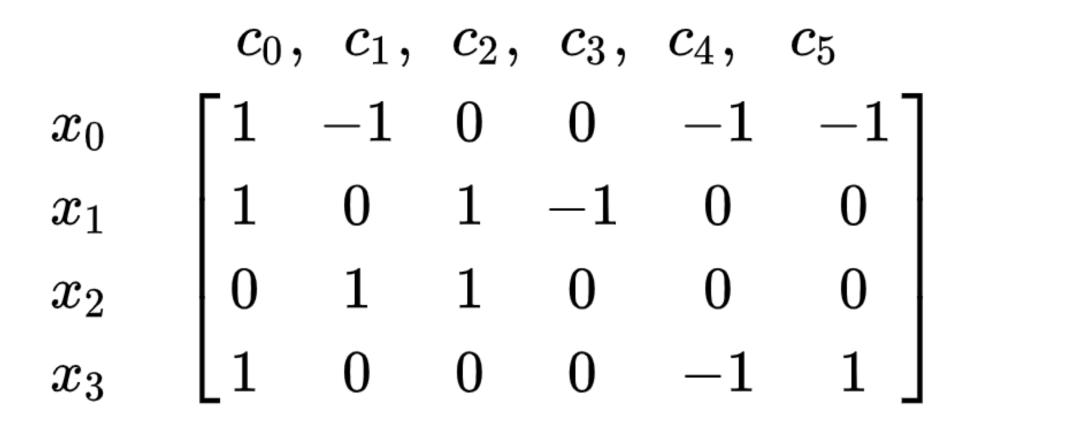


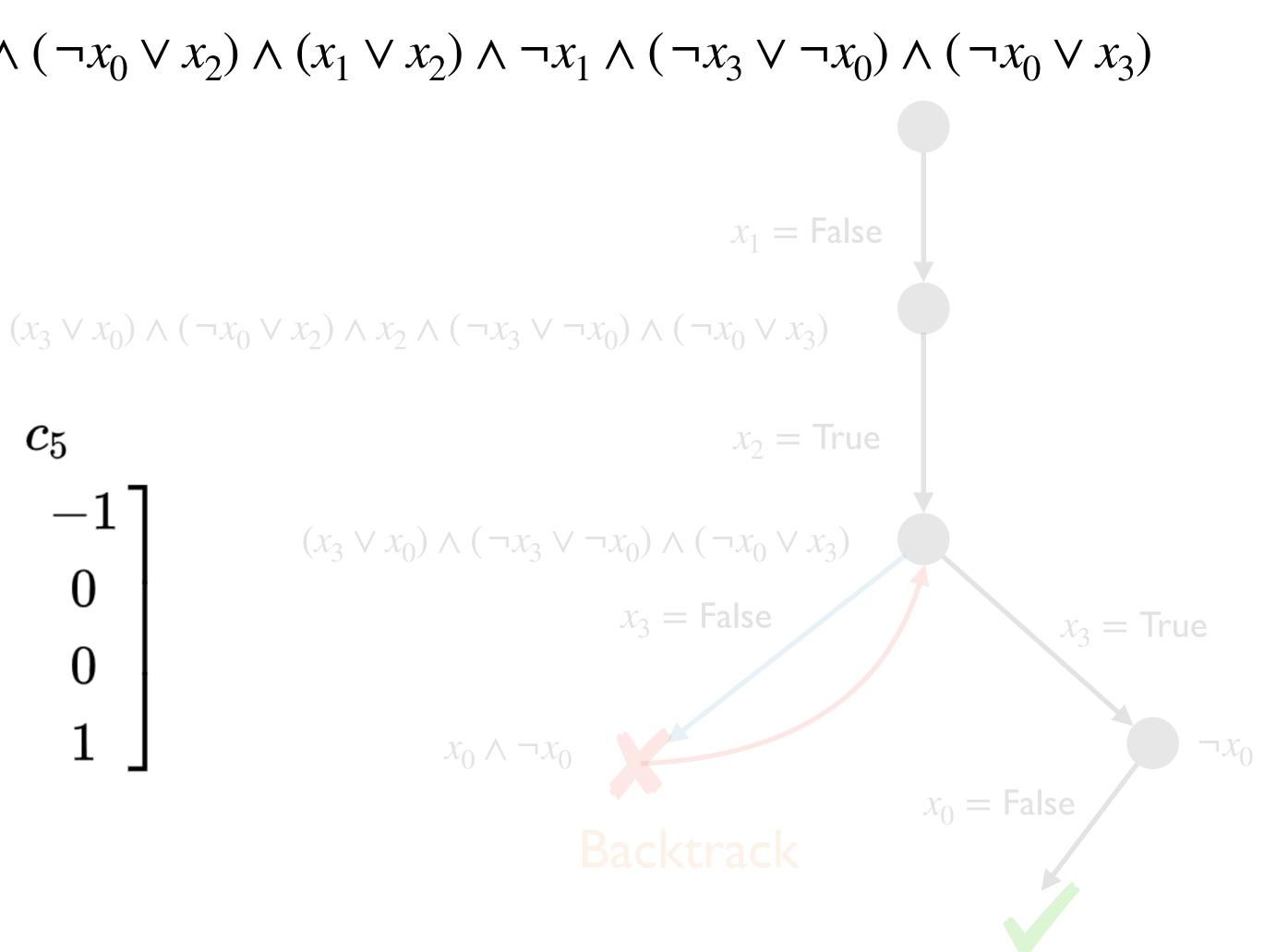
ppSAT: Towards Two-Party Private SAT Solving, USENIX Security 2022

 $\phi(x_0, x_1, x_2, x_3) = (x_3 \lor x_0 \lor x_1) \land (\neg x_0 \lor x_2) \land (x_1 \lor x_2) \land \neg x_1 \land (\neg x_3 \lor \neg x_0) \land (\neg x_0 \lor x_3)$

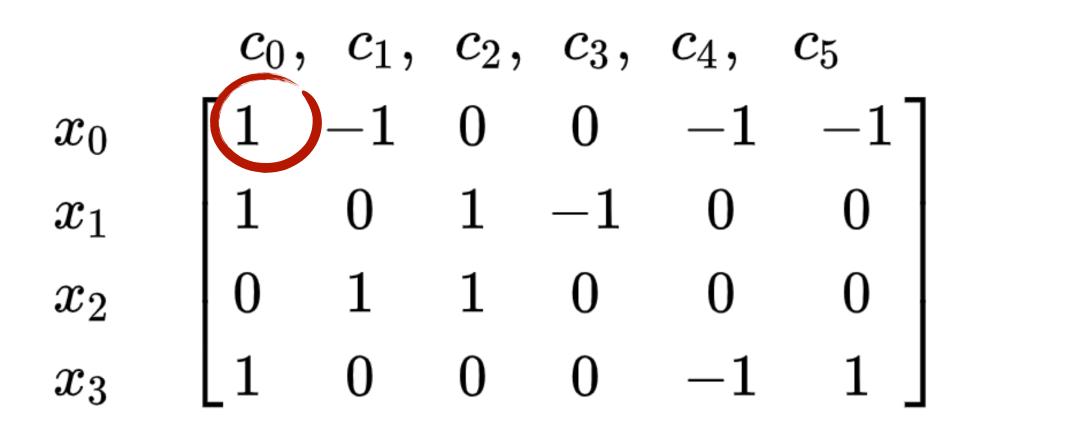


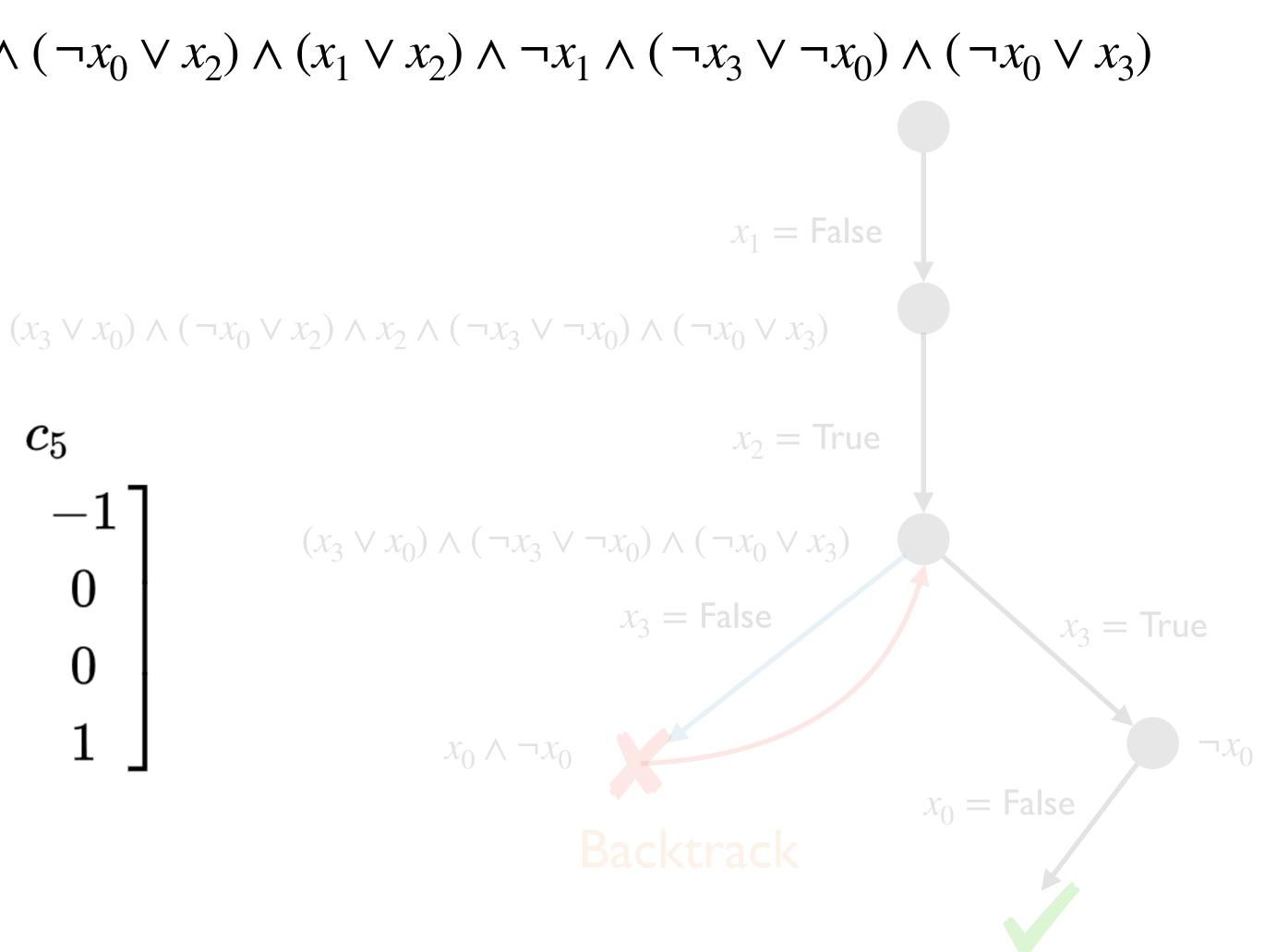
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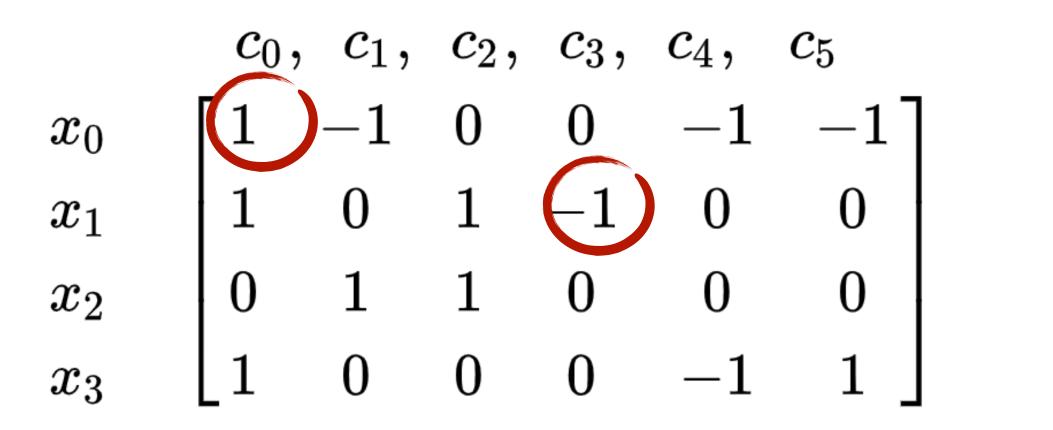


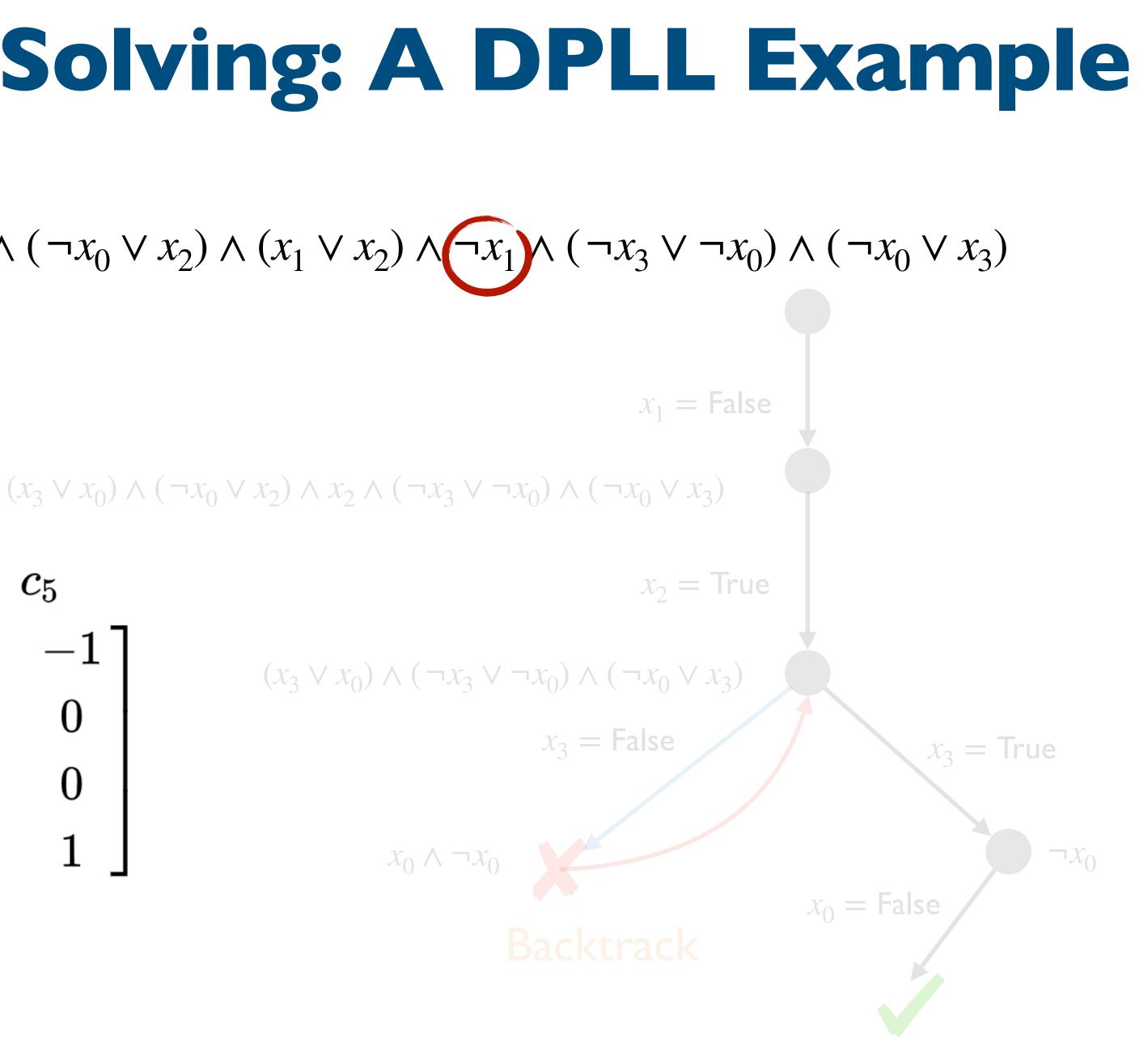
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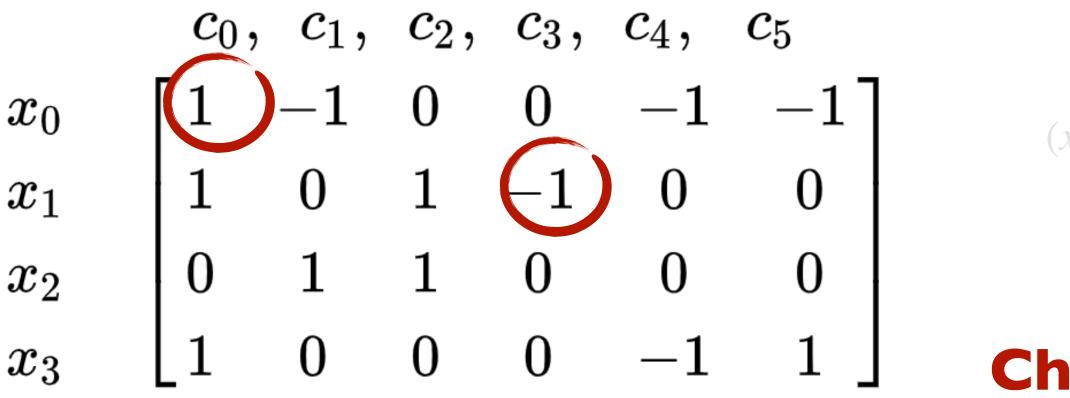


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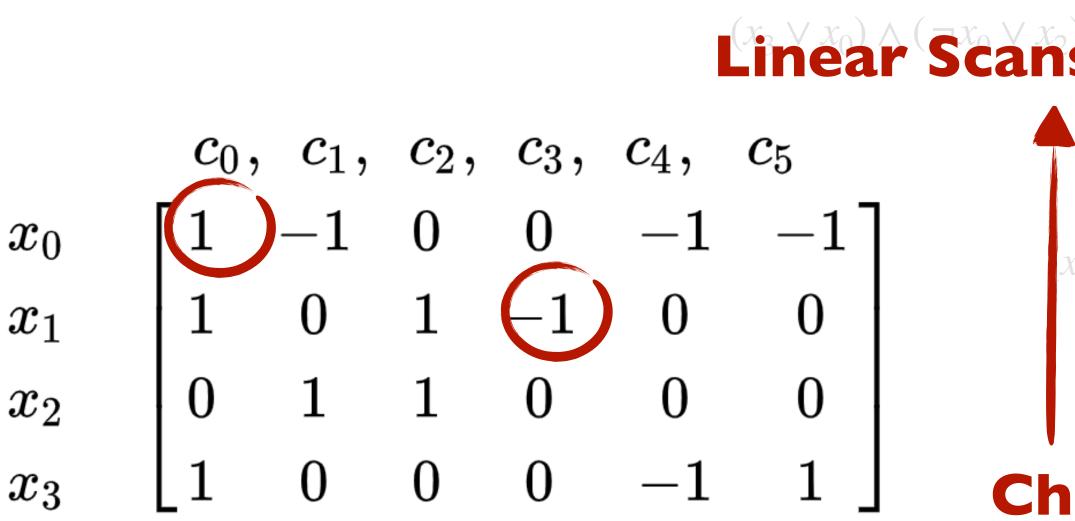




Boolean SAT Solving: A DPLL Example $\phi(x_0, x_1, x_2, x_3) = (x_3 \lor x_0 \lor x_1) \land (\neg x_0 \lor x_2) \land (x_1 \lor x_2) \land (\neg x_1 \land (\neg x_3 \lor \neg x_0) \land (\neg x_0 \lor x_3)$ $x_1 = False$ Unit literal search Propagation $(x_3 \lor x_0) \land (\neg x_0 \lor x_2) \land x_2 \land (\neg x_3 \lor \neg x_0) \land (\neg x_0 \lor x_3)$ $x_2 = \text{True}$ x_0 $(x_3 \lor x_0) \land (\neg x_3 \lor \neg x_0) \land (\neg x_0 \lor x_3)$ 1 0 0 0 x_1 $x_3 = False$ $x_3 =$ True $1 \quad 1$ 0 0 0 0 x_2 $0 \ 0 \ -1$ 0 x_3 $\neg x_0$ **Check** $x_0 \wedge \neg x_0$ $x_0 = False$



Boolean SAT Solving: A DPLL Example $\phi(x_0, x_1, x_2, x_3) = (x_3 \lor x_0 \lor x_1) \land (\neg x_0 \lor x_2) \land (x_1 \lor x_2) \land \neg x_1 \land (\neg x_3 \lor \neg x_0) \land (\neg x_0 \lor x_3)$ Unit literal search $x_1 = \mathsf{False}$ Propagation Linear Scans $x_2 =$ True x_0 $\lor \neg x_0) \land (\neg x_0 \lor x_3)$ $x_3 = False$ x_1 $x_3 = \text{True}$ $0 \ 1 \ 1 \ 0$ 0 0 x_2 $0 \ 0 \ -1$ **Check** $x_0 \wedge \neg x_0$ x_3 $\neg x_0$ $x_0 = False$



• DLIS

- Select the most commonly appearing literal and the smallest index
- Return the assignment that makes it true
- Deterministic

• Example: DLIS will guess $\neg x_0$ for $(x_3 \lor x_0) \land (\neg x_3 \lor \neg x_0) \land (\neg x_0 \lor x_3)$

Heuristics

• Rand

- Uniformly select a random undecided literal
- Randomized
- Example: $(x_3 \lor x_0) \land (\neg x_3 \lor \neg x_0)$

Randomly guess one of $\{x_0, \neg\}$

$$(\neg x_0 \lor x_3)$$

 $(\neg x_0 \lor x_3)$
 $(\neg x_0, x_3, \neg x_3)$ each with $\frac{1}{4}$ probability

• Weighted-Rand

- Select a random undecided literal according to its frequency
- Randomized

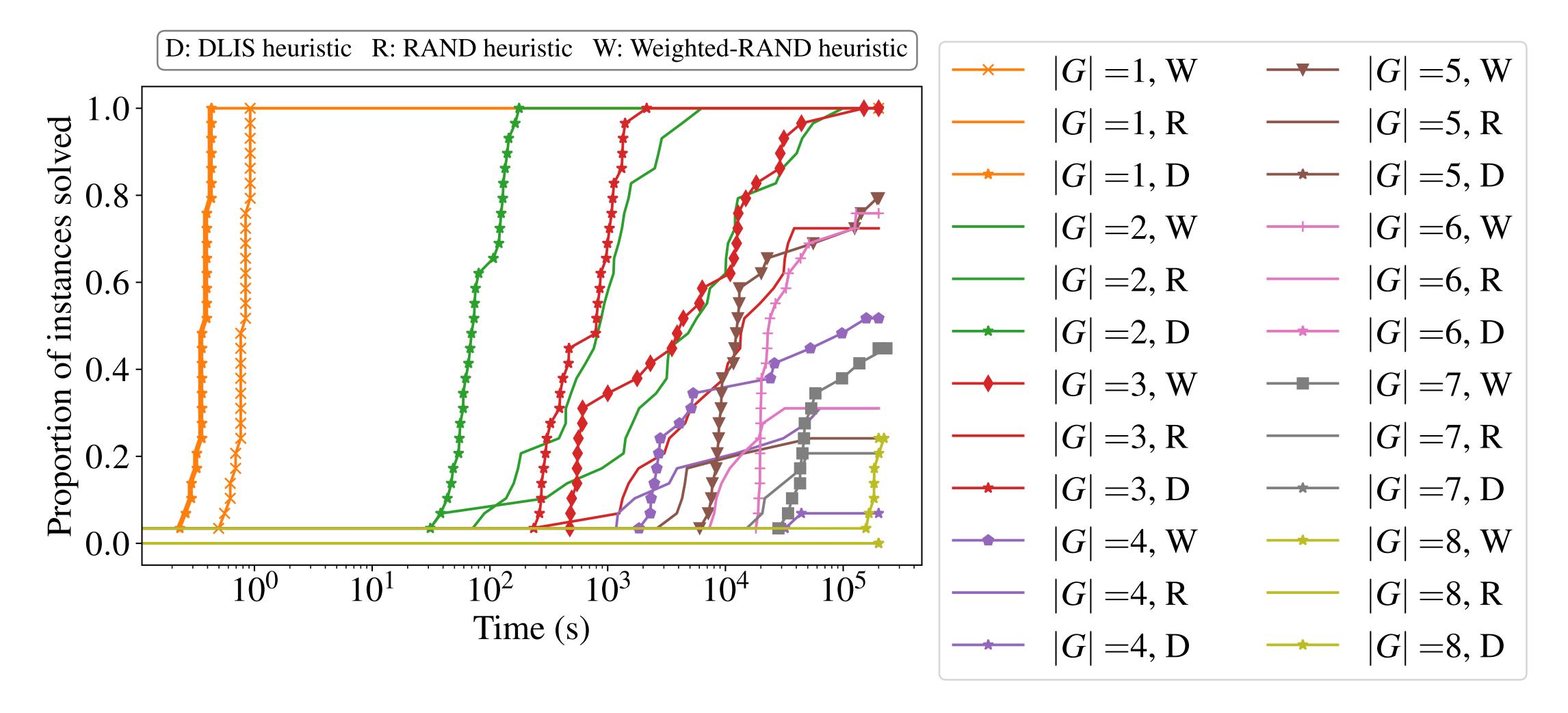
• Example:
$$(x_3 \lor x_0) \land (\neg x_3 \lor x_0) \land (\neg x_0 \lor x_3)$$

guess x_0 with $\frac{1}{6}$ chance, guess $\neg x_0$ with $\frac{2}{6}$ chance, etc.



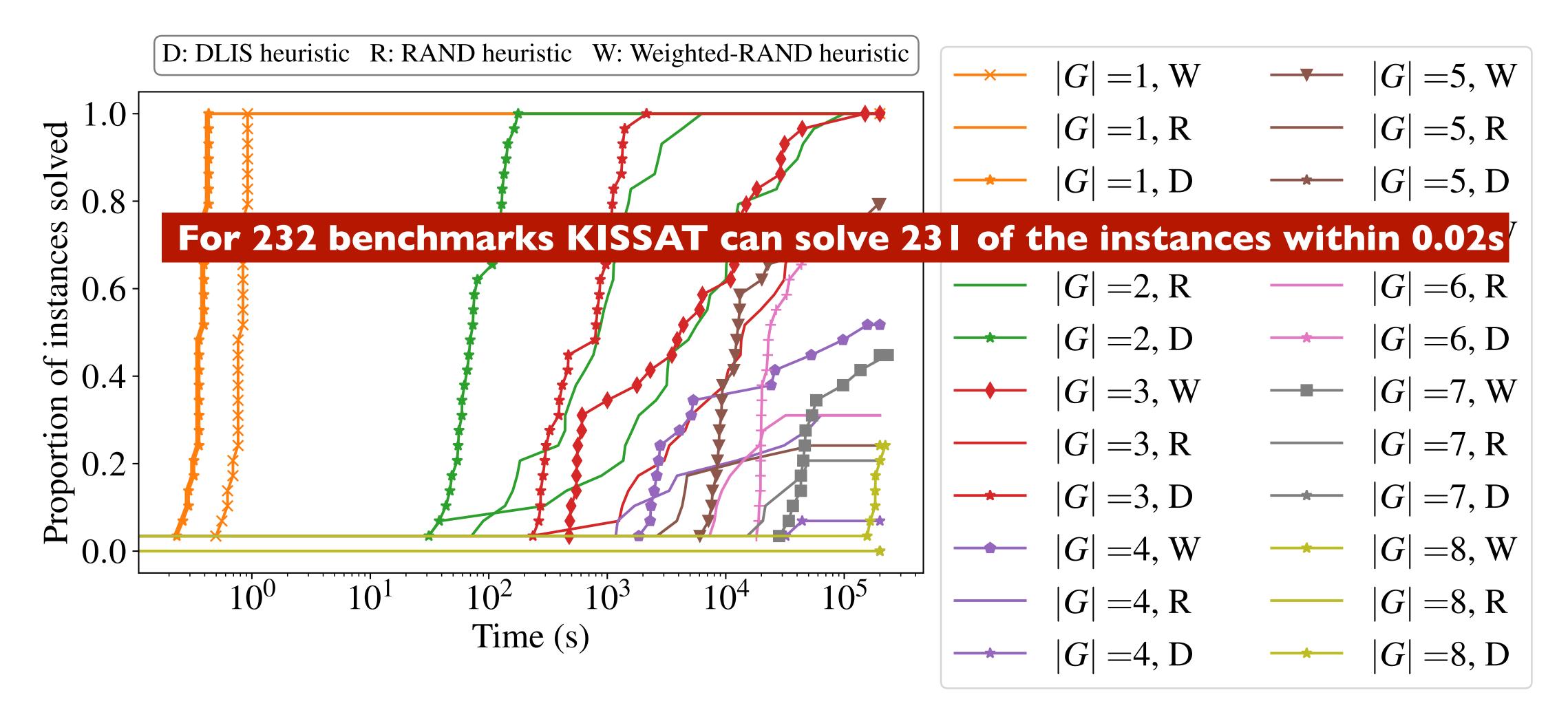


ppSAT: Towards Two-Party Private SAT Solving, USENIX Security 2022





ppSAT: Towards Two-Party Private SAT Solving, USENIX Security 2022







Better heuristics when it comes to privacy preserving setting?

Ning Luo: <u>ning.luo@northwestern.edu</u> https://github.com/PP-FM



<u>I am on job market</u>



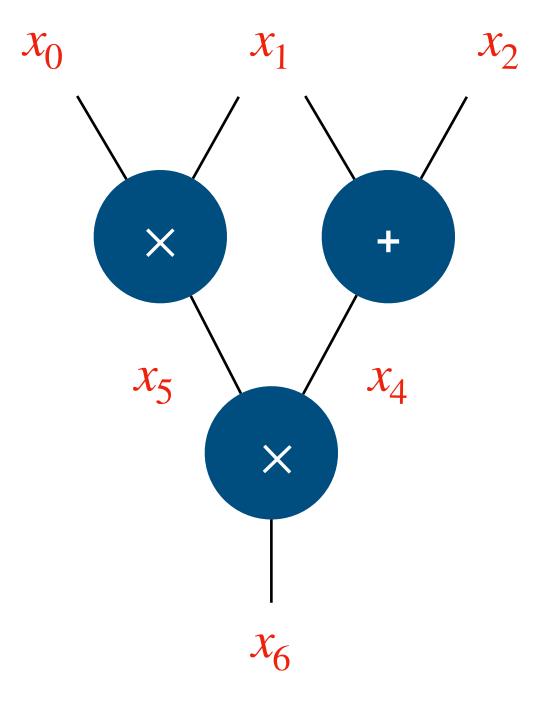
Reference

Donald Beaver, Silvio Micali, and Phillip Rogaway. The round complexity of secure protocols (extended abstract). In 22nd ACM STOC, pages 503–513, Baltimore, MD, USA, May 14–16, 1990. ACM Press.

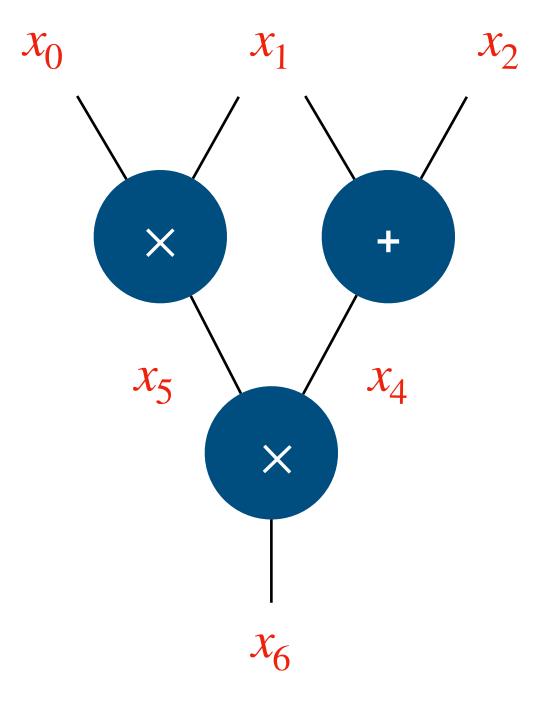
Kang Yang, Pratik Sarkar, Chenkai Weng, and Xiao Wang. Quicksilver: Efficient and affordable zero-knowledge proofs for circuits and polynomials over any field. In ACM Conf. on Computer and Communications Security (CCS) 2021. ACM Press, 2021.

J. A. Robinson. 1965. A Machine-Oriented Logic Based on the Resolution Principle. J. ACM 12, 1 (Jan. 1965), 23–41. DOI: https://doi.org/ 10.1145/321250.321253

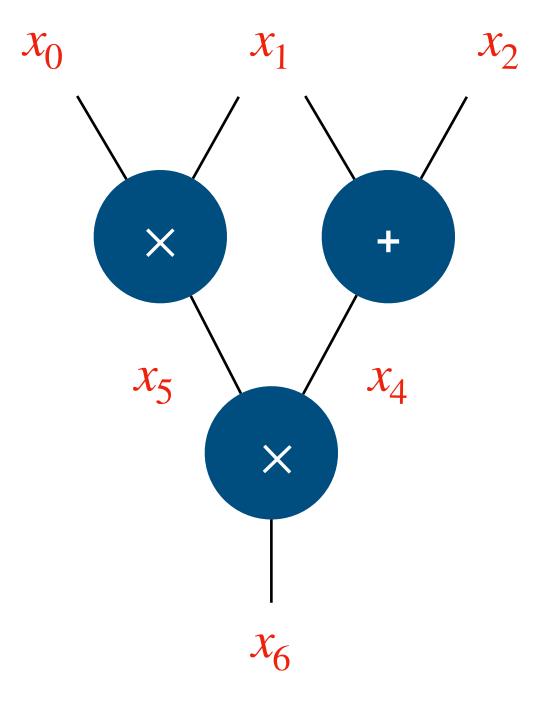
Nicholas Franzese, Jonathan Katz, Steve Lu, Rafail Ostrovsky, Xiao Wang, and Chenkai Weng. 2021. Constant-Overhead Zero-Knowledge for RAM Programs. In <i>Proceedings of the 2021 ACM SIGSAC Conference on Computer and Communications Security</i> (<i>CCS '21</i>). Association for Computing Machinery, New York, NY, USA, 178–191. DOI:<u>https://doi.org/10.1145/3460120.3484800</u>



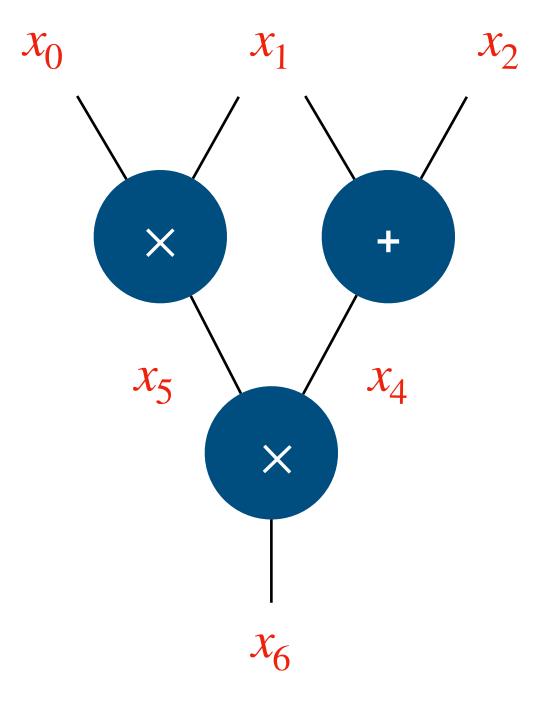
• Information-Theoretic MAC : $k_x = M_x + x \cdot \Delta$



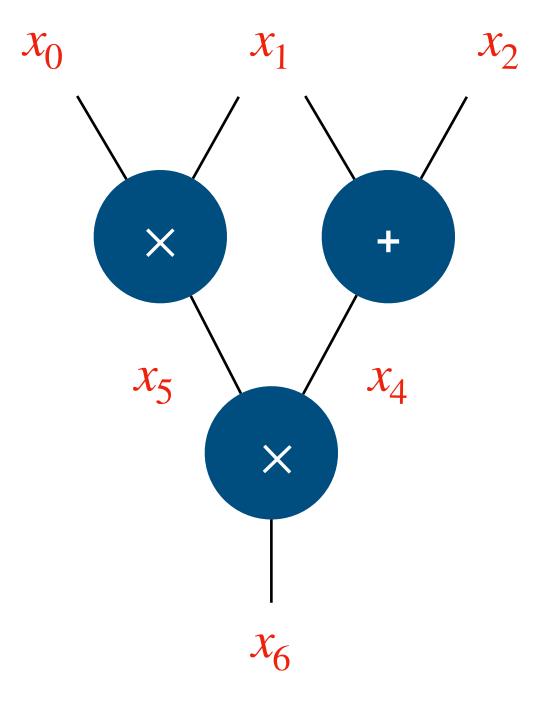
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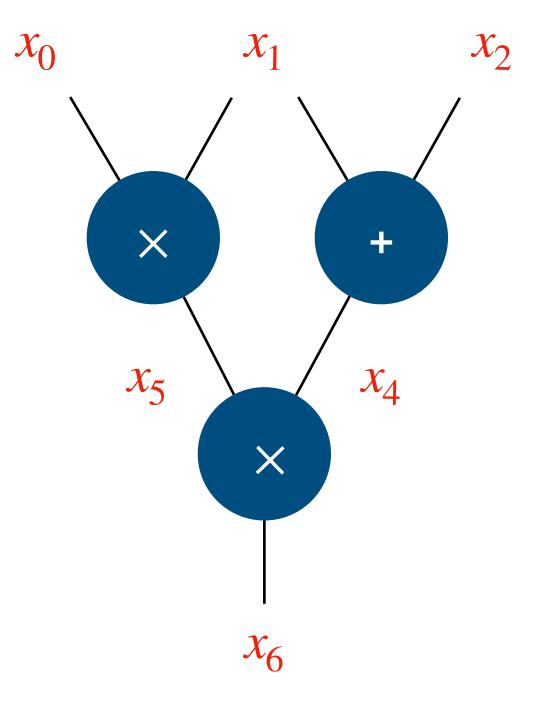
- Information-Theoretic MAC : $k_x = M_x + x \cdot \Delta$
 - x if x is shared in such a way



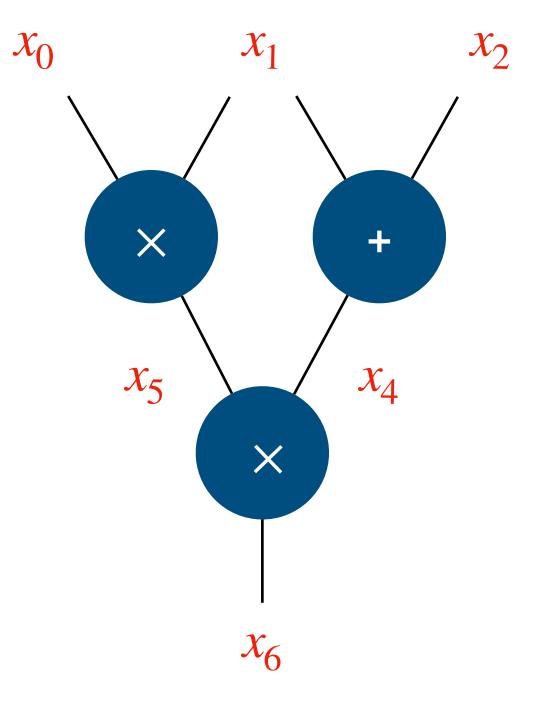
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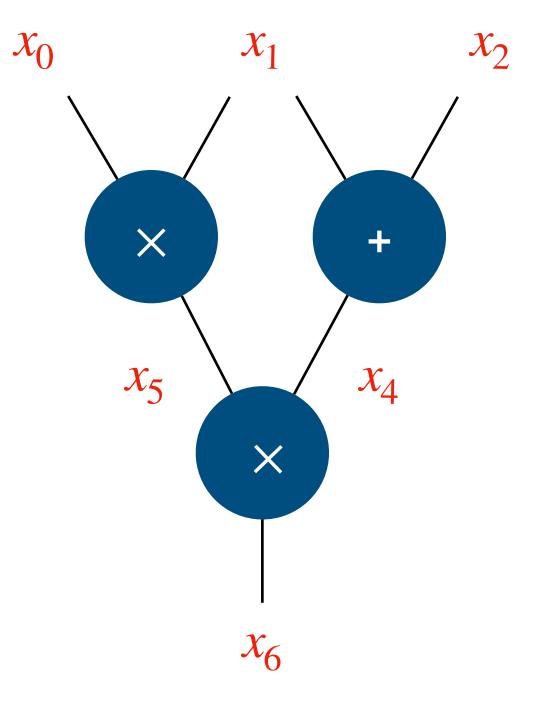
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 - Prover locally computes $M_{p(x,y)} = a \cdot M_x + b \cdot M_y$



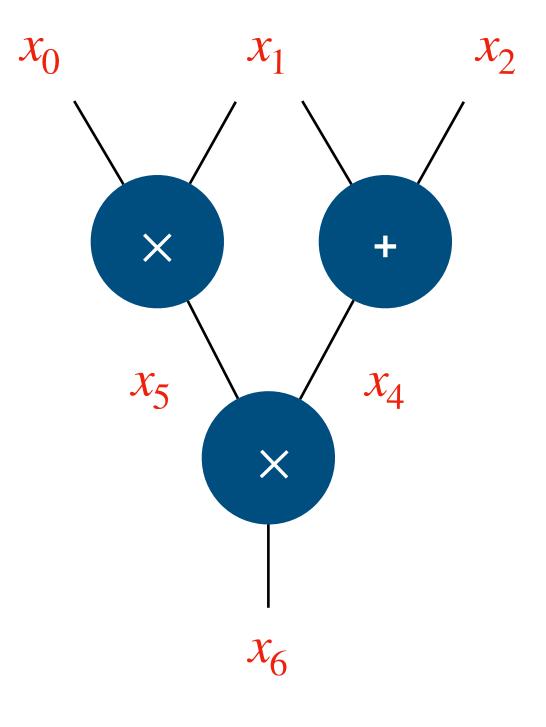
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 - Batching for multiple multiplication gates

